

## Freescale Semiconductor

MPX4250 Rev 7, 1/2009

# Integrated Silicon Pressure Sensor On-Chip Signal Conditioned, Temperature Compensated and Calibrated

The MPX4250 series piezoresistive transducer is a state-of-the-art monolithic silicon pressure sensor designed for a wide range of applications, particularly those employing a microcontroller or microprocessor with A/D inputs. This transducer combines advanced micromachining techniques, thin-film metallization, and bipolar processing to provide an accurate, high-level analog output signal that is proportional to the applied pressure. The small form factor and high reliability of on-chip integration make the Freescale sensor a logical and economical choice for the automotive system engineer.

# MPX4250 Series

0 to 250 kPa (0 to 36.3 psi) 0.2 to 4.9 V Output

## **Application Examples**

 Ideally Suited for Microprocessor or Microcontroller-Based Systems

#### **Features**

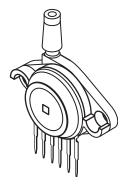
- · Differential and Gauge Applications Available
- 1.4% Maximum Error Over 0° to 85°C
- Patented Silicon Shear Stress Strain Gauge
- Temperature Compensated Over -40° to +125°C
- · Offers Reduction in Weight and Volume Compared to Existing Hybrid Modules
- Durable Epoxy Unibody Element

ORDERING INFORMATION									
Device Name	Package	Case No.	# of Ports		Pressure Type		Davisa Marking		
Device Name	Options		None	Single	Dual	Gauge	Differential	Absolute	Device Marking
Unibody Package (MPX4250 Series)									
MPX4250D	Tray	867	•				•		MPX4250D
MPX4250GP	Tray	867B		•		•			MPX4250GP
MPX4250DP	Tray	867C			•		•		MPX4250DP

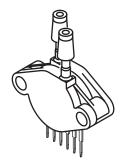
#### **UNIBODY PACKAGES**



MPX4250D CASE 867



MPX4250GP CASE 867B



MPX4250DP CASE 867C





## **Operating Characteristics**

**Table 1. Operating Characteristics** ( $V_S = 5.1 \text{ Vdc}$ ,  $T_A = 25^{\circ}\text{C}$  unless otherwise noted, P1 > P2. Decoupling circuit shown in Figure 3 required to meet electrical specifications.)

Characteristic		Symbol	Min	Тур	Max	Unit
Pressure Range <sup>(1)</sup>			0	_	250	kPa
Supply Voltage <sup>(2)</sup>		Vs	4.85	5.1	5.35	Vdc
Supply Current		Io	_	7.0	10	mAdc
Minimum Pressure Offset @ V <sub>S</sub> = 5.1 Volts <sup>(3)</sup>	(0 to 85°C)	V <sub>off</sub>	0.139	0.204	0.269	Vdc
Full Scale Output @ V <sub>S</sub> = 5.1 Volts <sup>(4)</sup>	(0 to 85°C)	V <sub>FSO</sub>	4.844	4.909	4.974	Vdc
Full Scale Span @ V <sub>S</sub> = 5.1 Volts <sup>(5)</sup>	(0 to 85°C)	V <sub>FSS</sub>	_	4.705	_	Vdc
Accuracy <sup>(6)</sup>	(0 to 85°C)	_	_	_	±1.4	%V <sub>FSS</sub>
Sensitivity		ΔV/ΔΡ	_	18.8		mV/kPa
Response Time <sup>(7)</sup>		t <sub>R</sub>	_	1.0		ms
Output Source Current at Full Scale Output		I <sub>o+</sub>	_	0.1		mAdc
Warm-Up Time <sup>(8)</sup>		_	_	20		ms
Offset Stability <sup>(9)</sup>		_	_	±0.5		%V <sub>FSS</sub>

- 1. 1.0 kPa (kiloPascal) equals 0.145 psi.
- 2. Device is ratiometric within this specified excitation range.
- 3. Offset (Voff) is defined as the output voltage at the minimum rated pressure.
- 4. Full Scale Output (VFSO) is defined as the output voltage at the maximum or full rated pressure.
- Full Scale Span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 6. Accuracy (error budget) consists of the following:

Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.

Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to

and from the minimum or maximum operating temperature points, with zero differential pressure applied.

Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the

minimum or maximum rated pressure, at 25°C.

TcSpan: Output deviation over the temperature range of 0 to 85°C, relative to 25°C.

TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.

Variation from Nominal: The variation from nominal values, for Offset or Full Scale Span, as a percent of V<sub>FSS</sub>, at 25°C.

- 7. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 8. Warm-up Time is defined as the time required for the product to meet the specified output voltage after the Pressure has been stabilized.
- 9. Offset Stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.



## **Maximum Ratings**

Table 2. Maximum Ratings<sup>(1)</sup>

Rating	Symbol	Value	Unit
Maximum Pressure (P1 > P2)	P <sub>MAX</sub>	1000	kPa
Storage Temperature	T <sub>STG</sub>	-40 to +125	°C
Operating Temperature	T <sub>A</sub>	-40 to +125	°C

<sup>1.</sup> Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

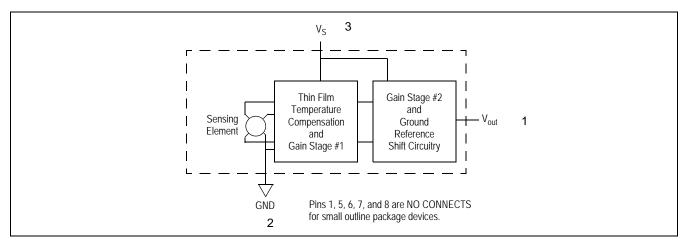


Figure 1. Fully Integrated Pressure Sensor Schematic

## **On-chip Temperature Compensation and Calibration**

Figure 2 illustrates the differential/gauge pressure sensing chip in the basic chip carrier (Case 867). A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the sensor diaphragm.

The MPX4250 series pressure sensor operating characteristics and internal reliability and qualification tests are based on use of dry air as the pressure media. Media, other than dry air, may have adverse effects on sensor

performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

Figure 3 shows the recommended decoupling circuit for interfacing the output of the integrated sensor to the A/D input of a microprocessor or microcontroller.

Figure 4 shows the sensor output signal relative to pressure input. Typical, minimum, and maximum output curves are shown for operation over a temperature range of 0° to 85°C using the decoupling circuit shown in Figure 3. The output will saturate outside of the specified pressure range

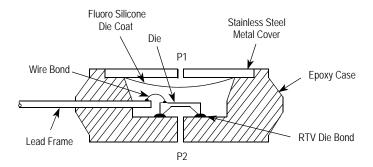


Figure 2. Cross Sectional Diagram (not to scale)

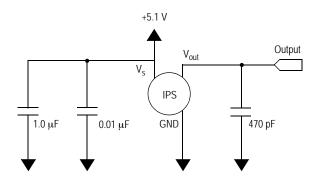


Figure 3. Recommended Power Supply Decoupling and Output Filtering (For additional output filtering, please refer to Application Note AN1535)

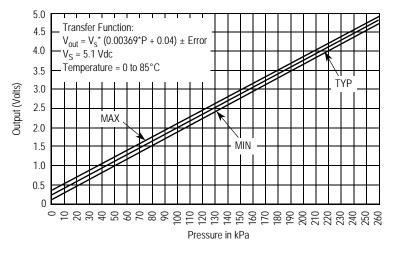


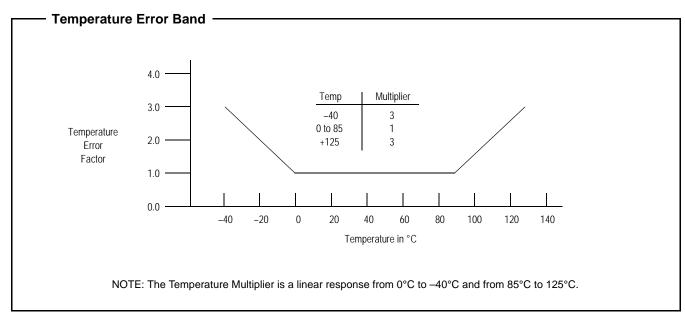
Figure 4. Output versus Absolute Pressure

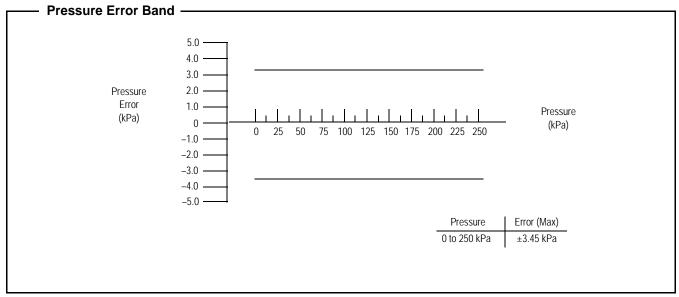


## **Transfer Function (MPX4250)**

Nominal Transfer Value:  $V_{out} = V_S x (0.00369 \times P + 0.04)$   $\pm (Pressure Error \times Temp. Factor \times 0.00369 \times V_S)$ 

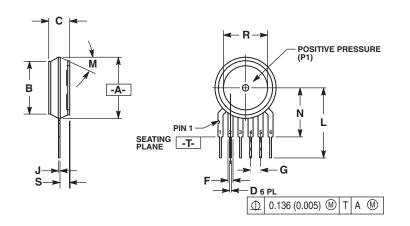
 $V_S = 5.1 \pm 0.25 \text{ Vdc}$ 







## PACKAGE DIMENSIONS

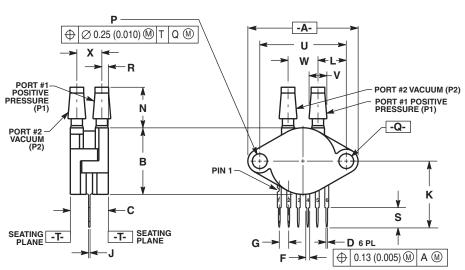


- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION -A- IS INCLUSIVE OF THE MOLD STOP RING. MOLD STOP RING NOT TO EXCEED 16.00 (0.630).

	INC	HES	MILLIM	ETERS	
DIM	MIN MA		MIN	MAX	
Α	0.595	0.630	15.11	16.00	
В	0.514	0.534	13.06	13.56	
С	0.200	0.220	5.08	5.59	
D	0.027	0.027 0.033		0.84	
F	0.048	0.064	1.22	1.63	
G	0.100	BSC	2.54 BSC		
J	0.014	0.016	0.36	0.40	
L	0.695	0.725	17.65	18.42	
M	30° 1	MOV	30° NOM		
N	0.475	0.495	12.07	12.57	
R	0.430	0.450	10.92	11.43	
S	0.090	0.105	2.29	2.66	

- STYLE 1: PIN 1. VOUT 2. GROUND 3. VCC 4. V1 5. V2 6. VEX
- STYLE 2:
  PIN 1. OPEN
  2. GROUND
  3. -VOUT
  4. VSUPPLY
  5. +VOUT
  6. OPEN
- STYLE 3:
  PIN 1. OPEN
  2. GROUND
  3. +VOUT
  4. +VSUPPLY
  5. -VOUT
  6. OPEN

#### **BASIC ELEMENT (D) CASE 867-08 ISSUE N**



#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: INCH.

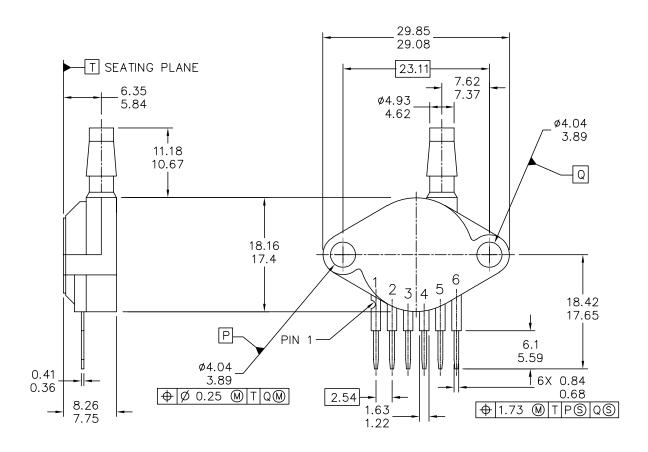
	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	1.145	1.175	29.08	29.85	
В	0.685	0.715	17.40	18.16	
С	0.405	0.435	10.29	11.05	
D	0.027	0.033	0.68	0.84	
F	0.048	0.064	1.22	1.63	
G	0.100	BSC	2.54	BSC	
J	0.014	0.016	0.36	0.41	
K	0.695	0.725	17.65	18.42	
L	0.290	0.300	7.37	7.62	
N	0.420	0.440	10.67	11.18	
Р	0.153	0.159	3.89	4.04	
Q	0.153	0.159	3.89	4.04	
R	0.063	0.083	1.60	2.11	
S	0.220	0.240	5.59	6.10	
U	0.910 BSC		23.11	BSC	
٧	0.182	0.194	4.62	4.93	
W	0.310	0.330	7.87	8.38	
Χ	0.248	0.278	6.30	7.06	

STYLE 1:
PIN 1. Vout
2. GROUND
3. Vcc
4. V1
5. V2
6. Vex

PRESSURE AND VACUUM SIDE DUAL PORTED (DP) **CASE 867C-05 ISSUE F** 



## **PACKAGE DIMENSIONS**



FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICA	L OUTLINE	PRINT VERSION NO	TO SCALE
TITLE:	DOCUMENT NO	]: 98ASB42796B	REV: G	
SENSOR, 6 LEAD UNIBO	CASE NUMBER: 867B-04 28 JUL 2005			
AP & GP 01ASB09	STANDARD: NO	IN-JEDEC		

PAGE 1 OF 2

# PRESSURE SIDE PORTED (GP) CASE 867B-04 ISSUE G

MPX4250



## **PACKAGE DIMENSIONS**

#### NOTES:

- 1. DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
- 3. 867B-01 THRU -3 OBSOLETE, NEW STANDARD 867B-04.

## STYLE 1:

PIN 1: V OUT

2: GROUND 3: VCC 4: V1

5: V2

6: V EX

	MECHANICAL OUTLINE		PRINT VERSION NOT TO SCALE	
TITLE:	DOCUMENT NO: 98ASB42796B REV:		REV: G	
SENSOR, 6 LEAD UNIBO	CASE NUMBER: 867B-04 28 JUL 200			
AP & GP 01ASB09	STANDARD: NO	ON-JEDEC		

PAGE 2 OF 2

PRESSURE SIDE PORTED (GP) **CASE 867B-04 ISSUE G** 



#### How to Reach Us:

Home Page:

www.freescale.com

Web Support:

http://www.freescale.com/support

#### **USA/Europe or Locations Not Listed:**

Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

## Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) www.freescale.com/support

#### Japan:

Freescale Semiconductor Japan Ltd. Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064 Japan 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

#### Asia/Pacific:

Freescale Semiconductor China Ltd. Exchange Building 23F No. 118 Jianguo Road Chaoyang District Beijing 100022 China +86 010 5879 8000 support.asia@freescale.com

#### For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405
Denver, Colorado 80217
1-800-441-2447 or +1-303-675-2140
Fax: +1-303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale<sup>™</sup> and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2009. All rights reserved.



# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

# NXP:

MPXAZ4250AC6T1 MPX4250GP MPXA4250AC6T1 MPX4250D MPX4250DP