

31		16 15		0		
Device ID		Vendor ID				00h
Status		Command				04h
Class Code				Revision ID		08h
BIST	Header Type	Lat. Timer		Cache Line S.		0Ch
Base Address Registers						10h
						14h
						18h
						1Ch
						20h
						24h
Cardbus CIS Pointer						28h
Subsystem ID			Subsystem Vendor ID			2Ch
Expansion ROM Base Address						30h
Reserved				Cap. Pointer		34h
Reserved						38h
Max Lat.	Min Gnt.	Interrupt Pin		Interrupt Line		3Ch