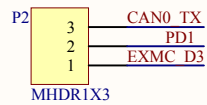


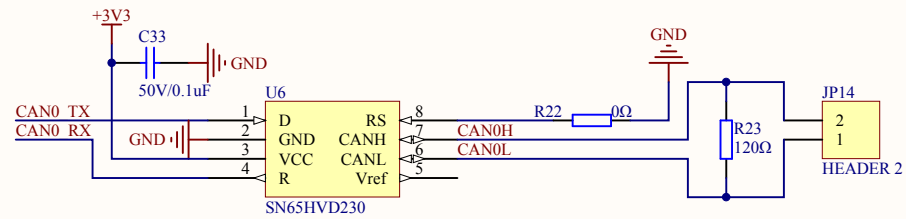
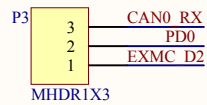
Company Name: GigaDevice		
File Name: AD_DA		
Revision: 1.0	Data: 2017-4	Author: XuFei

## CAN

Short P2(1,2) for EXMC function  
Short P2(2,3) for CAN0 function

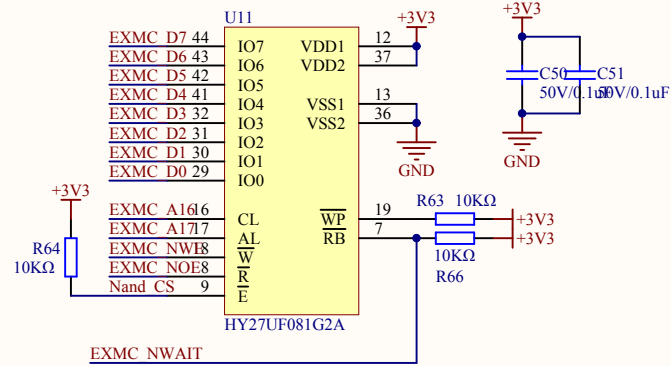


Short P3(1,2) for EXMC function  
Short P3(2,3) for CAN0 function

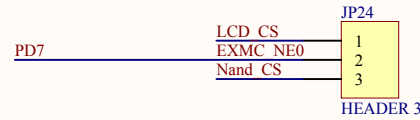
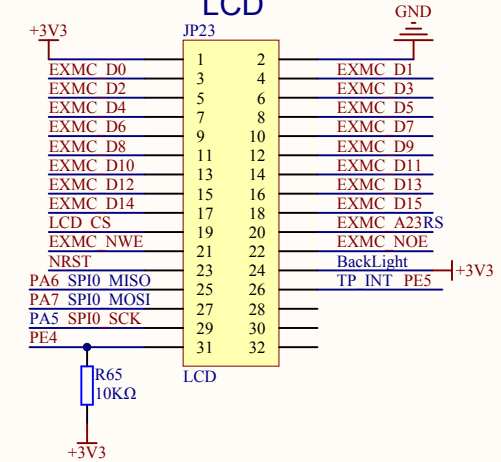


Company Name: GigaDevice		
File Name: CAN		
Revision: 1.0	Data: 2017-4	Author: XuFei

## Nand Flash



## LCD



PD0,PD1 are AFIOs, please refer to CAN schematic for right config

PD14	EXMC D0	PD11	EXMC A16
PD15	EXMC D1	PD12	EXMC A17
PD0	EXMC D2	PE2	EXMC A23
PD1	EXMC D3	PD4	EXMC NOE
PE7	EXMC D4	PD5	EXMC NWE
PE8	EXMC D5	PD6	EXMC NWAIT
PE9	EXMC D6		
PE10	EXMC D7		
PE11	EXMC D8		
PE12	EXMC D9		
PE13	EXMC D10		
PE14	EXMC D11		
PE15	EXMC D12		
PD8	EXMC D13		
PD9	EXMC D14		
PD10	EXMC D15		

Company Name: GigaDevice

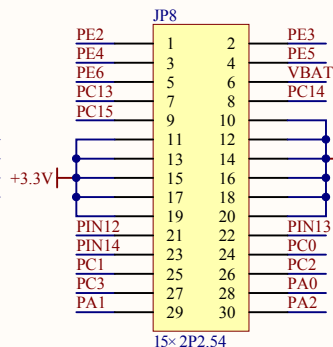
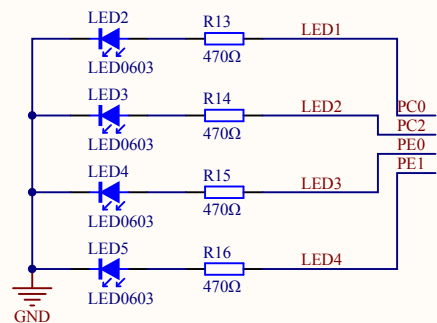
File Name: EXMC

Revision: 1.0

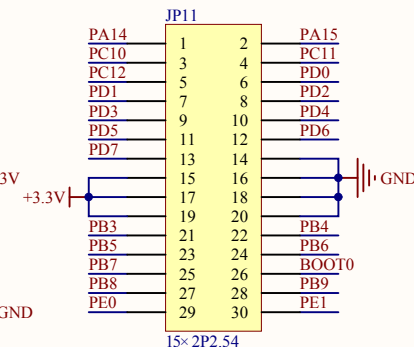
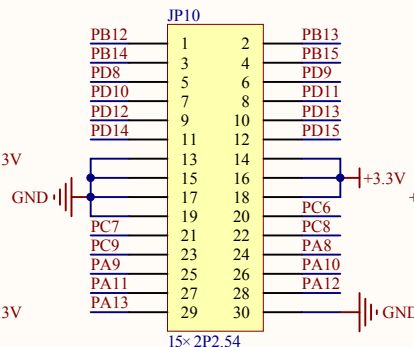
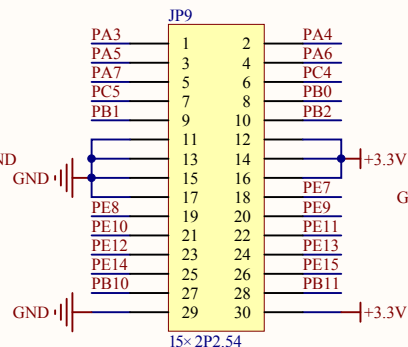
Data: 2017-4

Author: XuFei

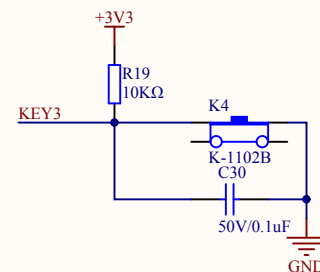
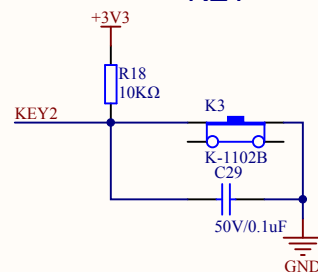
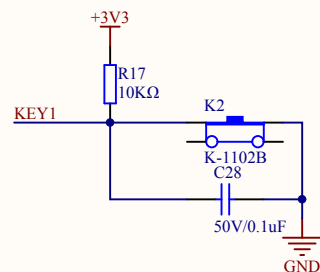
## LED



## Extension Pin



## KEY



PA0 KEY1  
PC13 KEY2  
PB14 KEY3

Company Name: GigaDevice

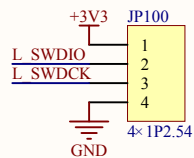
File Name: Extension

Revision: 1.0

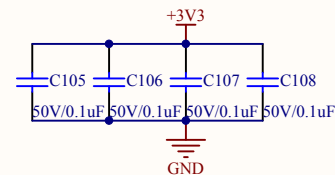
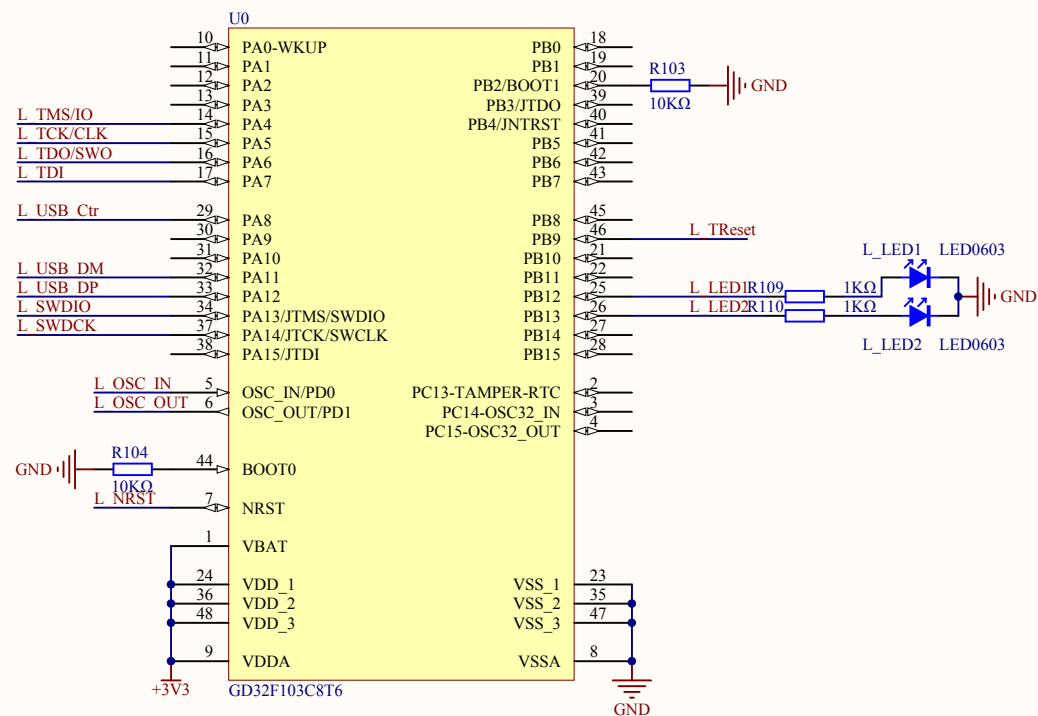
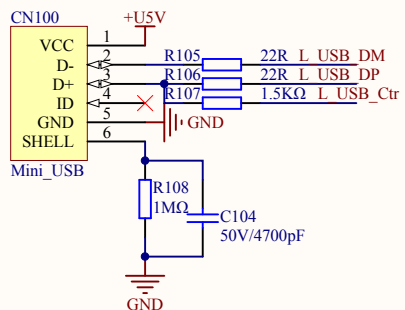
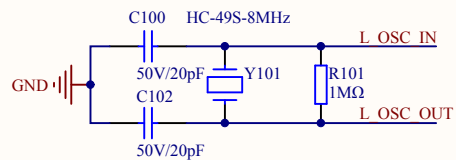
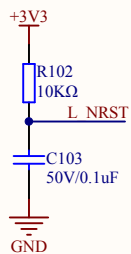
Data: 2017-4

Author: XuFei

## MCU SWD



## Reset



Company Name: GigaDevice

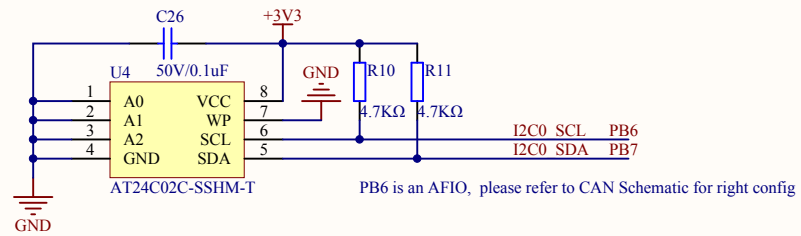
File Name: GDLink

Revision: 1.0

Data: 2016-03

Author: XuFei

## I2C



Company Name: GigaDevice		
File Name: I2C_SMbus		
Revision: 1.0	Data: 2017-4	Author: XuFei

1

2

3

4

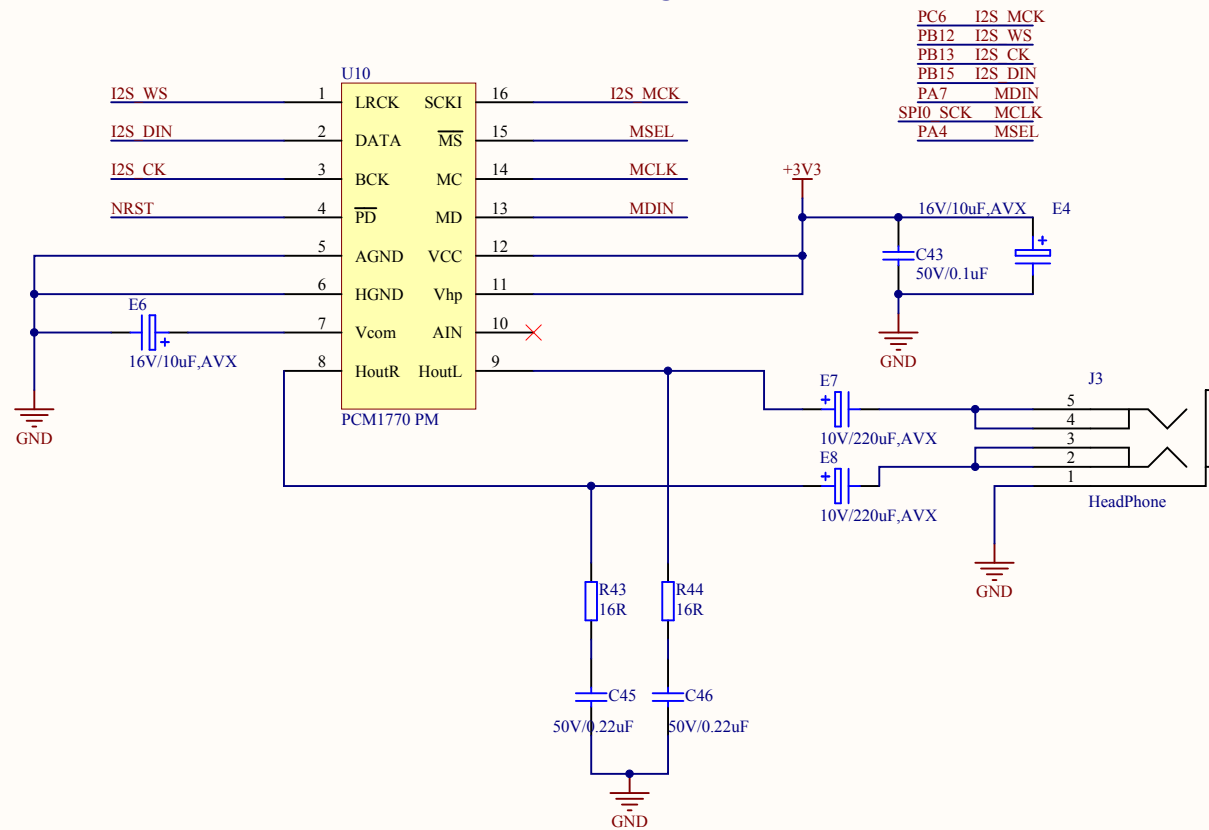
A

B

C

D

## I2S



Company Name: GigaDevice

File Name: I2S

Revision: 1.1

Data: 2017-4

Author: Xufei

1

2

3

4

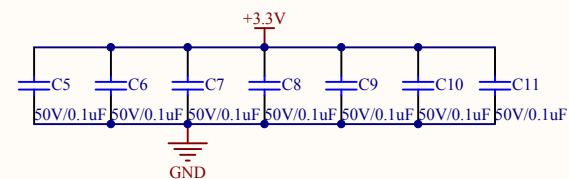
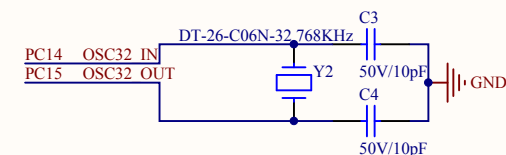
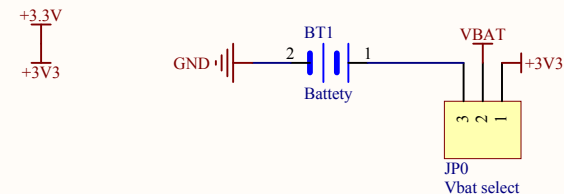
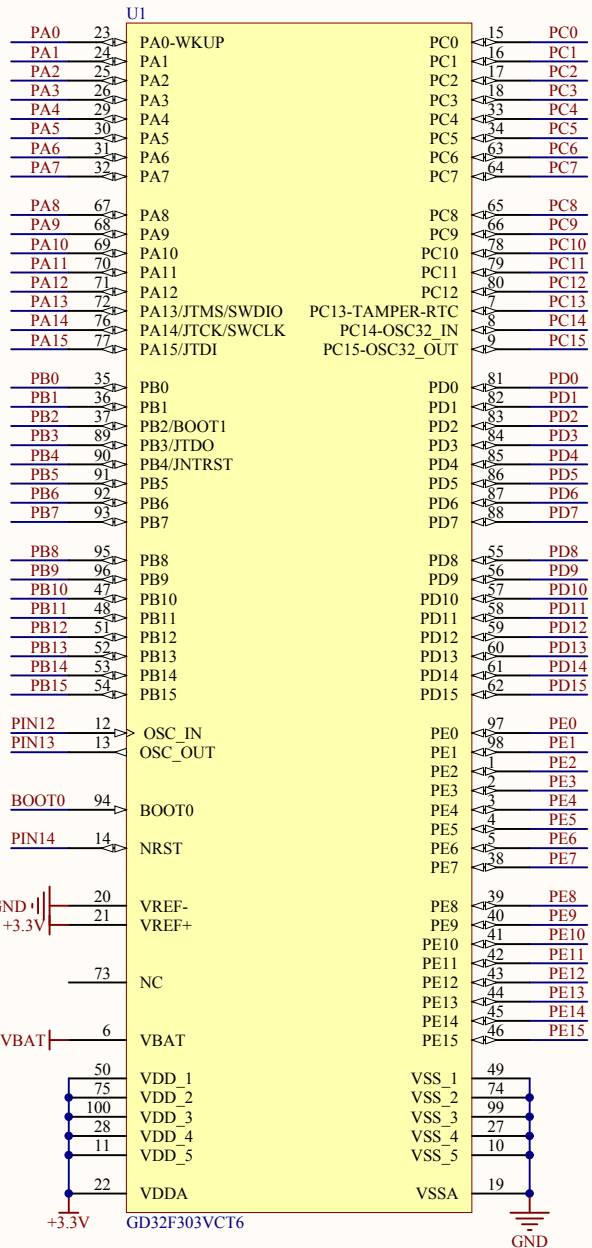
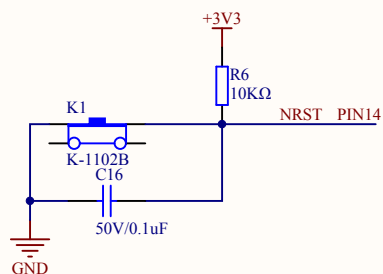
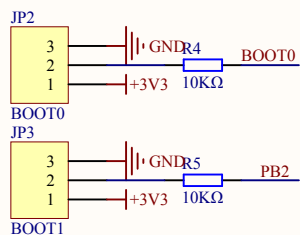
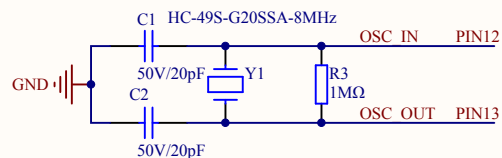
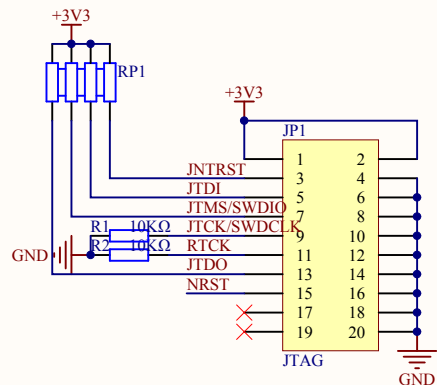
1

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4

GDLink	JTAG	
L TDI	JNTRST	PB4
L TMS/IO	JTDI	PA15
L TCK/CLK	JTMS/SWDIO	PA13
L TDO/SWO	JTCK/SWDCLK	PA14
L TReset	JTDO	PB3
	NRST	



Company Name: GigaDevice

File Name: MCU

Revision: 1.0

Data: 2017-4

Author: XuFei

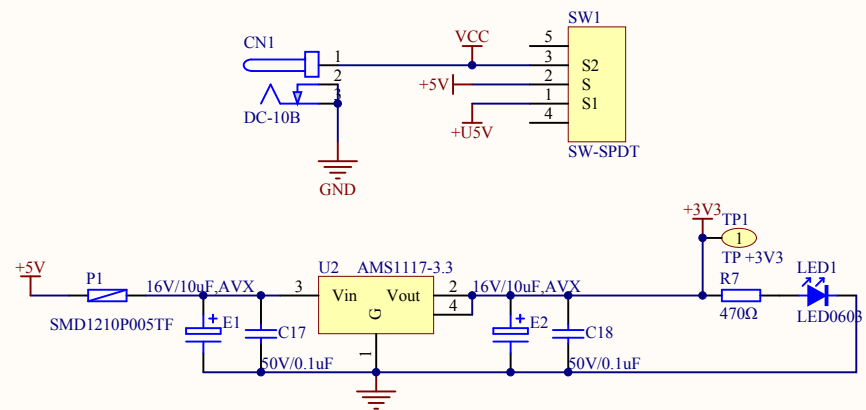
1

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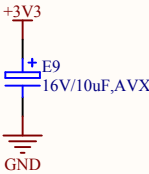
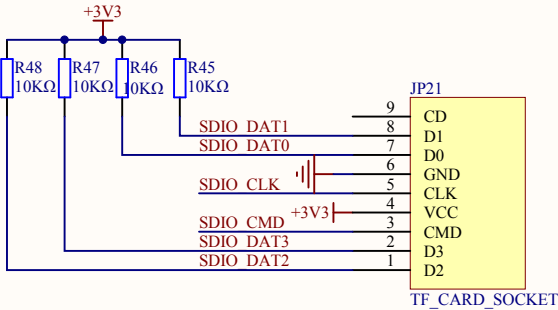




Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2017-4	Author: XuFei

SDIO

PC11	SDIO_DAT3
PD2	SDIO_CMD
PC12	SDIO_CLK
PC8	SDIO_DAT0
PC9	SDIO_DAT1
PC10	SDIO_DAT2



Company Name: GigaDevice

File Name: SDIO

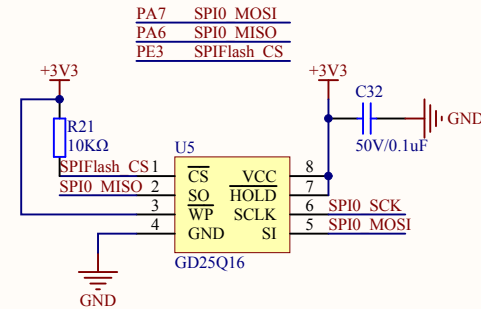
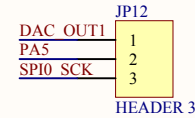
Revision: 1.1

Data: 2017-4

Author: Xufei

SPI Flash

Short JP12(1,2) for DAC function  
Short JP12(2,3) for SPI0 function



Company Name: GigaDevice

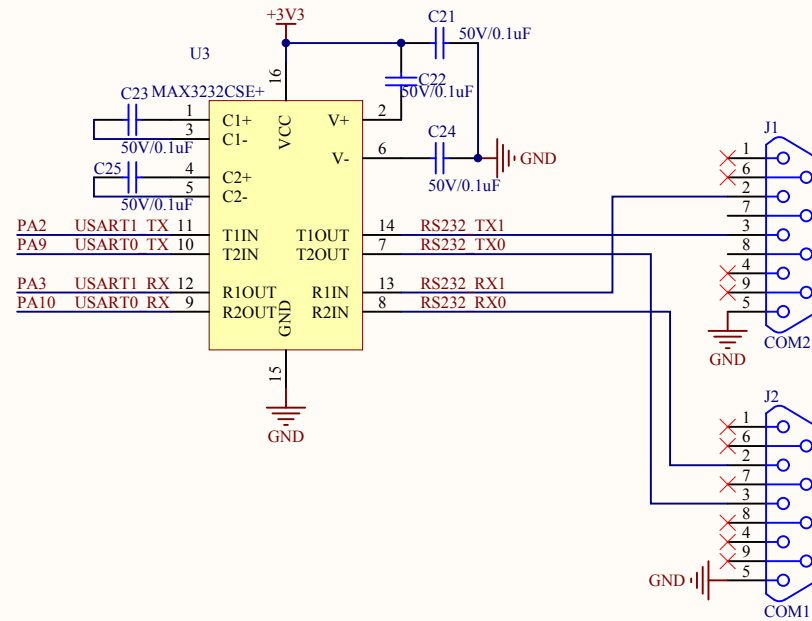
File Name: SPI

Revision: 1.0

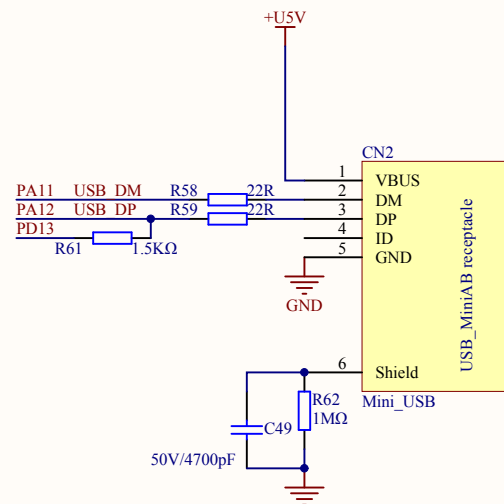
Data: 2017-4

Author: XuFei

## USART0/USART1



Company Name: GigaDevice		
File Name: USART		
Revision: 1.0	Data: 2017-4	Author: XuFei



Company Name: GigaDevice		
File Name: USB		
Revision: 1.1	Data: 2017-4	Author: Xufei

