



Writing Efficient Code for OpenCL® Applications on 3rd Generation Intel® Core™ Processors

Webinar #2 in the Three-part OpenCL® Webinar Series

July 18, 2012

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About the Presenters



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Intel® SDK for OpenCL® Applications 2012



SDK Package

Development Environment

- Supports Both CPU and Intel HD Graphics

Development Tools

- Offline Compiler
- CPU Kernel Debugger

Documentation and Samples

- Getting Started guide
- Optimization Guide

Drivers and Support

Runtime/Drivers

- Intel® HD Graphics Driver with support for both CPU and Graphics
- Intel® SDK for OpenCL® – CPU only runtime package

Online Resources

- Code Samples
- Support Forum
- Tech Articles, and more.

Optimization Tools

- Intel® VTune Amplifier XE
- Intel® Graphics Performance Analyzers (Intel® GPA)

Intel SDK for OpenCL Applications Makes Development Faster and Easier

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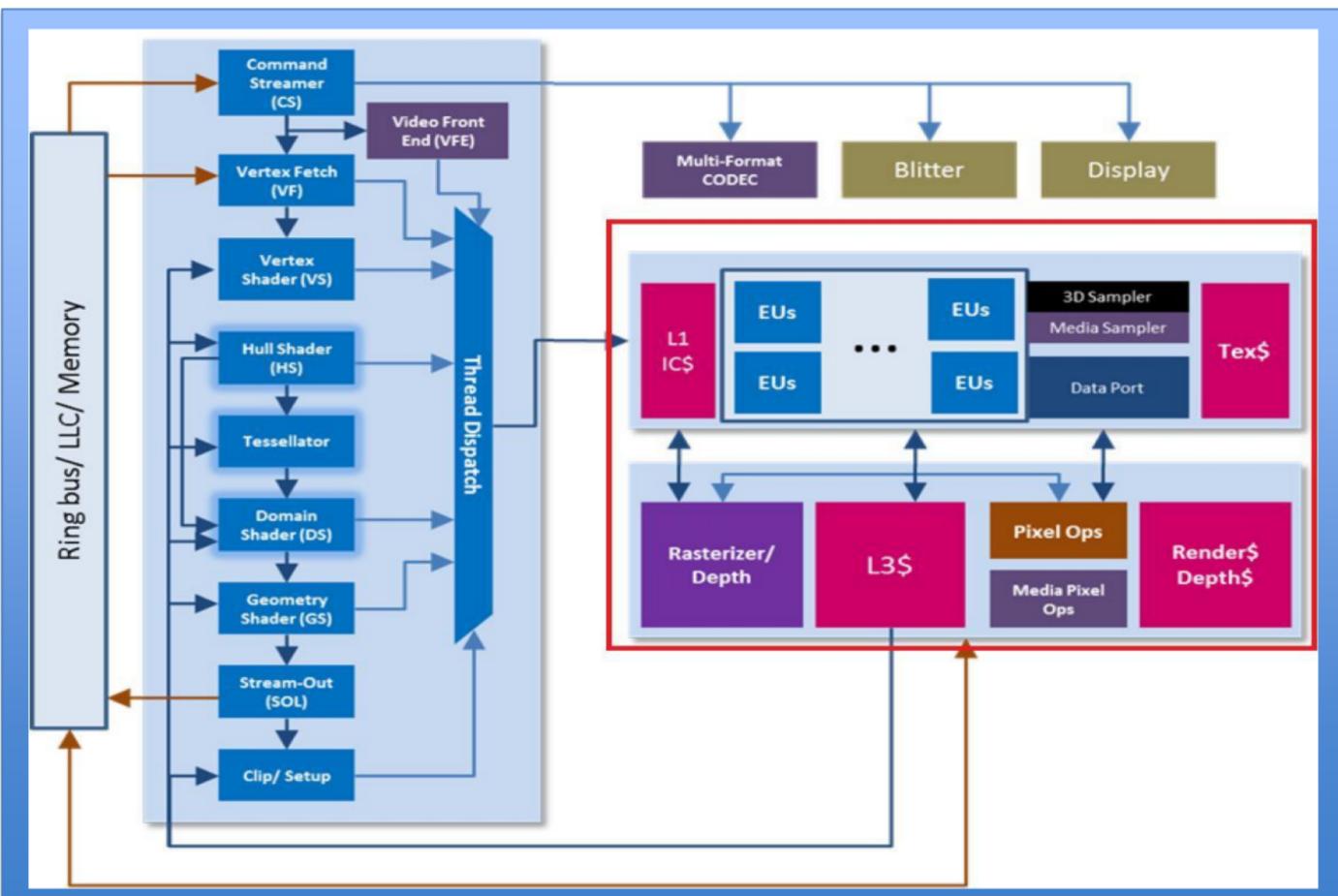
- OpenCL* on 3rd Generation Intel® Core™ Processors
- OpenCL Kernel Optimization
- Optimizing for Memory
- Intel® Quick Sync Video and OpenCL* Interoperability
- ArcSoft* Inc. Presentation
 - Utilizing OpenCL for Multimedia Applications on 3rd Generation Intel Core Processors
- What's next

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Intel® HD Processor Graphics



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OPENCL® KERNEL OPTIMIZATION

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Avoid Invariant Operations in Kernel

```
_kernel void test1(__global int* data,  
int2 size, int base )  
{  
    int offset = size.y * base + size.x;  
    float offsetF = (float)offset;  
    ...  
}
```



```
_kernel void test1(__global int* data, int  
offset, float offsetF )  
{  
    ...
```



Move Invariant Code to Host

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Avoid Initialization in Kernel

```
__kernel void something(__global int* data)
{
    size_t tid = get_global_id(0);
    if (0 == tid)
    {
        //Do initializing
    }
    barrier(CLK_GLOBAL_MEM_FENCE);
    //Regular kernel code
}
```



Move One Time Initialization to Host Code, or in a Different Kernel

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Use Built-in Functions

```
dp = V0.x * V1.x + V0.y * V1.y  
+ V0.z * V1.z + V0.w + V1.w;
```



```
dp = dot(V0, V1);
```

```
C = sqrt(A * A + B * B);
```



```
C = hypot(A, B);
```

```
cl =  
fmin(fmax(X,minVal),maxVal);
```



```
cl = clamp(X,  
minVal, maxVal);
```

Allow the Compiler to Optimize Better

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Trade Accuracy vs. Speed

```
c[id] += a[id]*b[id];
```



```
c[id] =  
mad(a[id], b[id], c[id]);
```

```
a[id] = sin(a[id]);
```



```
a[id] =  
native_sin(a[id]);
```

**Use Native_* Versions of Trigonometry Functions
or Compile with -cl-fast-relaxed-math Option**

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Edge Conditions

```
__kernel void myKernel(__global int* data, int maxR, int maxC)
{
    int row = get_global_id(0);
    int col = get_global_id(1);
    if (row > maxRow)
    {
        ...
    }
    else if (col > maxCol)
    {
        ...
    }
    else
    {
        ...
    }
}
```

Possible alternatives:

- Limit the NDRange
- Use padded buffers
- Set edge values using Min/Max built-ins
- Use images for Processor Graphics

Avoid Checking Edge-Conditions

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Avoid Byte/Short Load and Stores

```
_kernel void k(global uchar* a, global uchar* b)
{
    int gid = get_global_id(0);
    a[gid] *= b[gid];
    . . .
}
```



```
_kernel void k(global uint4* a, global uint4* b)
{
    int gid = get_global_id(0);
    a[gid] *= b[gid];
    . . .
}
```

Avoid Byte or Short Loads. Load and Store in Greater Chunks

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Maximizing Overall Utilization



Intel® HD Processor Graphics Device Optimization



- Optimize the number of work-groups
- Optimize the work-group size
- Use barriers in kernels wisely
- Optimize thread utilization

Amount of SLM required by kernel	Recommended work-group size	Max # of concurrently running work-groups	Comments
0	16	128	Good (Not using SLM/barriers)
4K	64	32	Good
8K	128	16	Good
16K	256	8	Good
32K	512	4	Good

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Maximizing Overall Utilization



Intel® CPU Device Optimization

- Use vector data types. E.g. float8, double4, int4 etc.
- Preferred work group size for kernels is 64 or 128 work items.
 - Workgroup size multiple of 8
 - Query CL_KERNEL_PREFERRED_WORK_GROUP_SIZE_MULTIPLE parameter by calling to clGetKernelWorkgroupInfo
 - Match number of work groups to logical cores.

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OPTIMIZING FOR MEMORY

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Buffer Objects Vs. Image Objects

- CPU Device
 - Avoid using Image Object
 - Buffer Objects are preferred
- Intel® HD Processor Graphics Device
 - **For Buffer Objects**
 - Share physical memory between CPU and Intel® HD Processor Graphics (CL_MEM_USE_HOST_PTR)
 - Preferred for Look-up tables
 - Use local memory for explicit caching of buffer values
 - **For Image Objects**
 - Sampler capabilities – clamping coordinates, interpolation
 - Packing/Unpacking of elements e.g. uchar4 to uint4
 - Prefer Image objects if access pattern is irregular (not linear)

Try Both Object Types

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Shared Local Memory – Processor Graphics



Intel® HD Processor Graphics Optimization

- The size of Shared Local Memory (SLM) can be found by `clGetDeviceInfo(...CL_DEVICE_LOCAL_MEM_SIZE)`

```
__kernel void k(global int* d, global int* t, local
int* slm)
{
    int gid = get_global_id(0);
    // populate "slm" with data from t
    barrier(CLK_LOCAL_MEM_FENCE);
    d[gid] = slm[d[gid]];
}
```

Use SLM for Small Tables, Coefficients, Etc.

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Data Copy to Shared Local Memory

(When SLM entries are not equal to number of work items)



Intel® HD Processor Graphics Optimization

```
kernel void foo(global int* t, local int* l /*256 enteries*/)
{ //initialize SLM (performed for each work-item!)
  for( uint id = 0; id < 256; id ++ )
    l[id] = t[id];
  barrier(CLK_LOCAL_MEM_FENCE);
. . .
```



```
kernel void foo(global int* t, local int* l /*256 enteries*/)
{ //initialize SLM
int    lidx = get_local_id(0);
int    size_x = get_local_size(0);
for( uint id = lidx; id < 256; id += size_x )
  l[id] = t[id];
barrier(CLK_LOCAL_MEM_FENCE);
. . .
```



Divide the SLM Copy Among the Local Work Items

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Data Copy to Shared Local Memory

(When SLM entries are equal to number of work items in local work group)



Intel® HD Processor Graphics Optimization

```
kernel void foo(global int* t, l int* l)
{ //initialize SLM
int    lidx = get_local_id(0);
int    lidy = get_local_id(1);

int    id = lidx + lidy * get_local_size(0);

l[id] = t[id];

barrier(CLK_LOCAL_MEM_FENCE);
. . .
```



Each Work Item Copies One Table Entry

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Memory Access

- Intel® CPU and Intel® HD Processor Graphics share Last Level Cache (LLC)
- Intel® HD Processor Graphic specific L3 Cache
 - Cache line size is 64 bytes
- Memory can be accessed at 8-bit, 16-bit, or 32-bit quantities
- Access at least 32-bit quantities from 32-bit aligned boundaries
 - Char4
 - int

Memory Access Pattern and Size is Important for the Performance

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Access Patterns to L3 Cache

- Consider the indexing pattern: $a[gid(1)*width+gid(0)]$
- Let's look at what happens with work groups processing 16 elements:
 - By row: size <16, 1>
 - By tile: size <4, 4>
 - By column: <1, 16>
- For the “row” case, 16 consecutive integers are accessed within the workgroup, so all could fit in a single cache line:



BEST Performance in This Case

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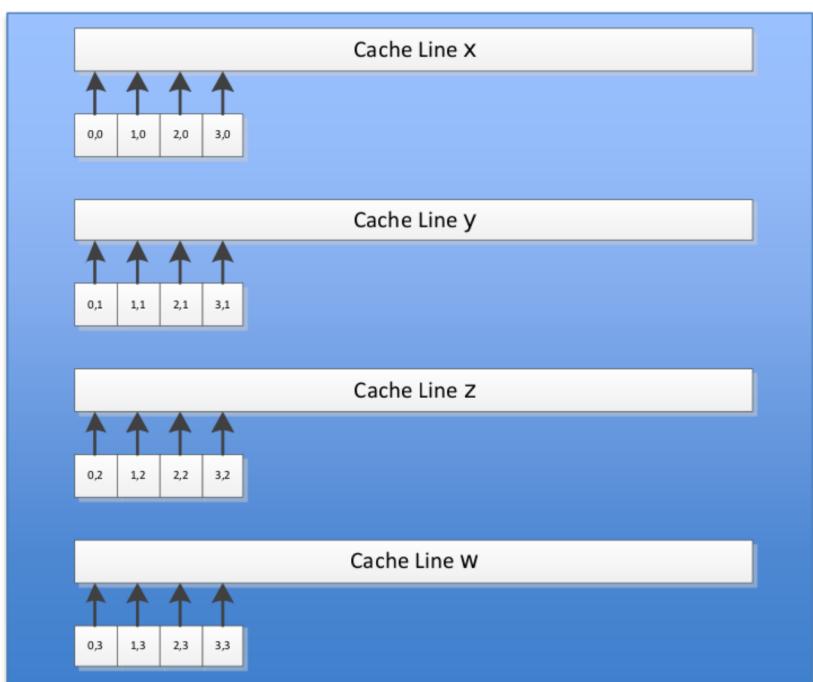
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Access Patterns to L3 Cache

- For the “tile” case, the global ID(1) is different for every four work items within the group, causing a stride of several cache lines:

- Only a quarter of the bandwidth is achieved!

- For the “column” case, 16 cache lines are required



Performance Will be Impacted if Work Items Access Different Cache Lines in the Same Work Group

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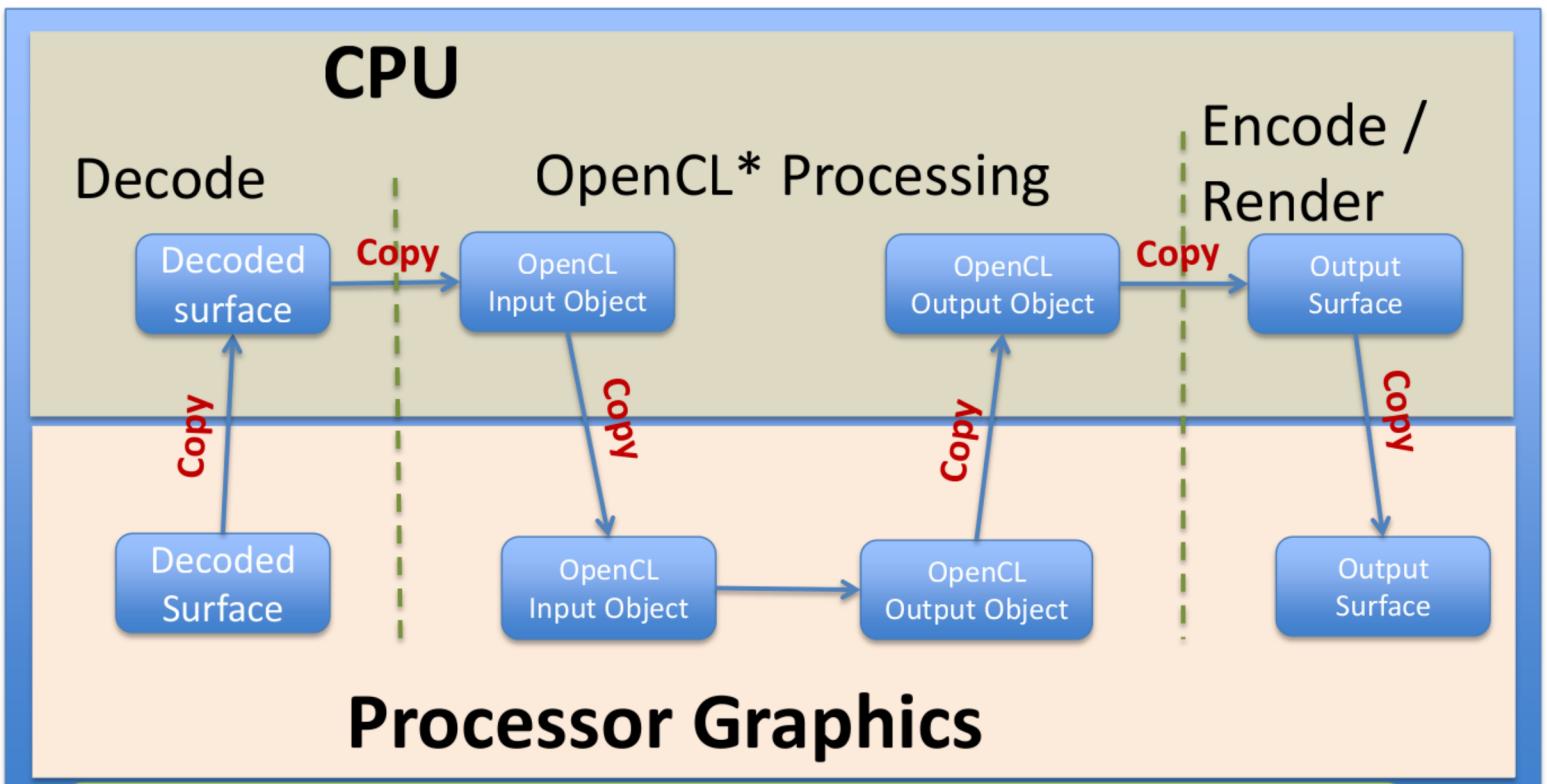
INTEL® QUICK SYNC VIDEO AND OPENCL® INTEROPERABILITY

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OpenCL® Kernels with Transcode Pipeline



Processor Graphics

Surface copies overhead can decrease application performance

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Transcode Pipeline Using Intel® Quick Sync Video and OpenCL® Kernels

Microsoft DirectX® 9 Surface Sharing Extensions



CPU

Decode

OpenCL® Processing

Encode /
Render

Decoded
Surface

DirectX® 9
Media
Sharing
Extensions

OpenCL
Input Object

OpenCL
Output Object

DirectX 9
Media
Sharing
Extensions

Output
Surface

Processor Graphics

Minimum or No surface Copies – Best Performance

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Intel® Quick Synch Video USER Class Functions



- Intel® Quick Synch Video USER Class of Functions

MFXVideoUSER_Register

MFXVideoUSER_ProcessFrameAsync

MFXVideoUSER_Unregister

- OpenCL* and DirectX* 9 Media Sharing APIs

clGetDeviceIDsFromDX9INTEL

clCreateFromDX9MediaSurfaceINTEL

clEnqueueAcquireDX9ObjectsINTEL

clEnqueueReleaseDX9ObjectsINTEL

Sample Code Available with Intel® SDK for OpenCL* Applications

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Best Performance Using Multiple OpenCL® Devices



Single OpenCL® Context on the Intel Platform

- Partitioning the work between devices
- Sharing resources efficiently
- Unified code base
- Synchronization (across command queues)

Intel® SDK for OpenCL® Applications - Optimization Guide

<http://software.intel.com/sites/landingpage/opencl/optimization-guide/index.htm>

Partner with Intel to Improve Application Performance
on the Intel Platform

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Utilizing OpenCL* for Multimedia Applications on 3rd Generation Core™ Intel Processors

July 2012

Nao Yoshikawa

Director, OEM Marketing and Services

ArcSoft. Inc.

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Company Overview

ArcSoft is an industry-leading software developer of multimedia technologies and applications for PC, Mac, mobile phones, tablets, smart TVs, and cameras.

- Established in 1994
- Headquartered in Silicon Valley, California
- Over 800 employees worldwide

Great alliance with Intel

- Multi-core / multi-thread optimization on Dual / Quad core Intel CPUs
- SSE, AVX optimization
- Intel® Media SDK, Intel® Quick Sync Video support
- PAVP support for Blu-ray Disc* playback
- Working on OpenCL* optimization



Why OpenCL*?

- OpenCL* is an open standard heterogeneous parallel computing framework.
- Scalable. Balance resources between CPU and GPU
 - Free CPU resource for non-graphic tasks
 - Lower power consumption
 - Scalable for various system configuration
- Cross-platform. Highly portable to various platforms
 - PC, Mac*, Android*, etc
 - OpenCL Embedded Profile for mobile and embedded devices
- Interoperable with Microsoft* DirectX* Video Acceleration (DXVA)
 - Avoid extra copies of video buffer
 - Easy to include in video decoding / transcoding pipeline



OpenCL* in ArcSoft Products

SimHD*

SD-to-HD video upscaling

- TotalMedia* Theatre
- ShowBiz*

Sim3D*

2D-to-3D video conversion

- TotalMedia* Theatre
- ShowBiz*

OpenCL* in ArcSoft
Products

H.264 encoding

H.264 video encoding

* currently using Intel® Quick Sync Video on Intel platform

- ShowBiz*
- MediaConverter*

Panorama stitching

Stitch multiple pictures into one

- Panorama Maker*

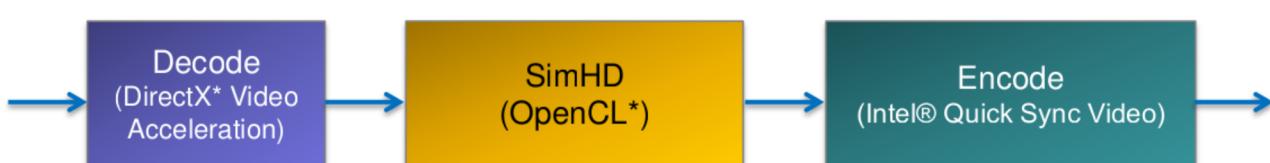




SimHD*



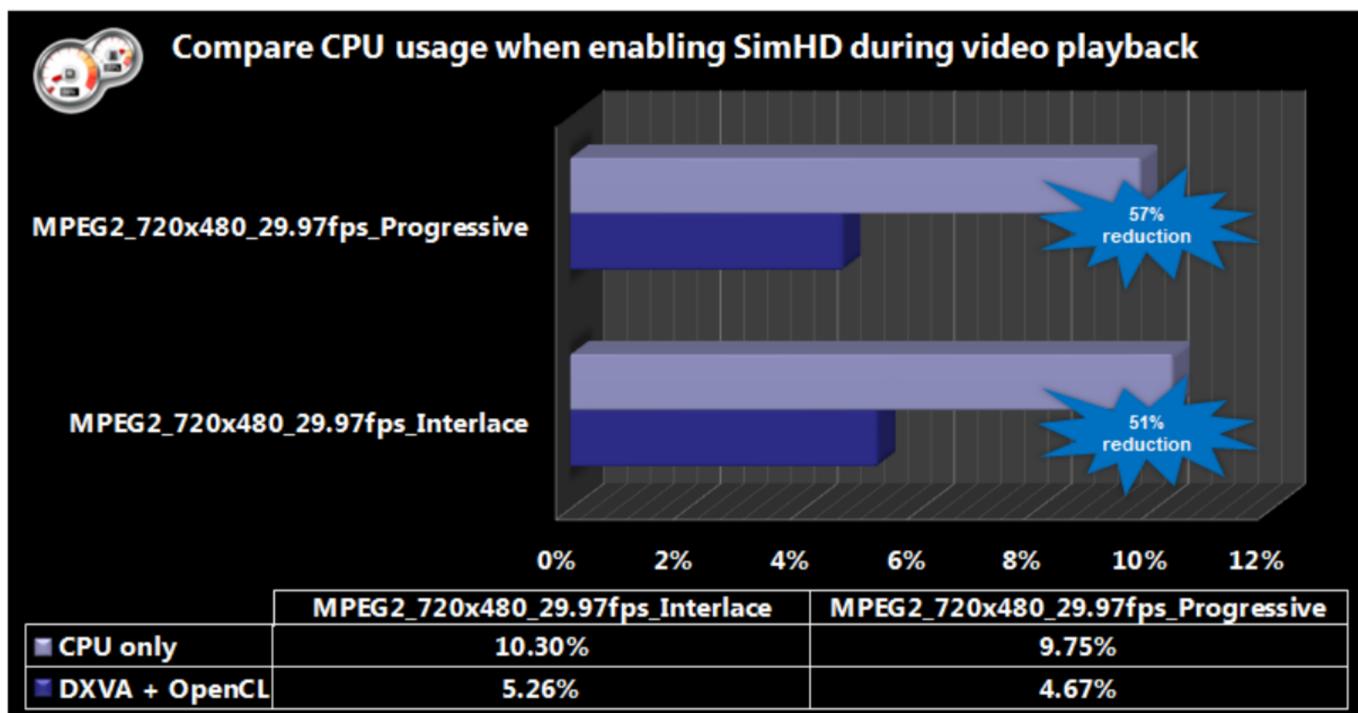
- SimHD*: SD-to-HD video upscaling engine
- Available in ArcSoft TotalMedia* Theatre (universal player) for playback
- Available in ArcSoft ShowBiz* (video editing & authoring) for encoding



SimHD* Benchmark

- Test Platform

- 3rd Generation Intel® Core™ Processor, 1.80GHz (4 core) with Intel® HD Graphics 4000
- Intel® HD Graphics Driver Version: 8.15.10.2669



Performance tests done by ArcSoft Inc. and are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary.



OpenCL® Tools, Progress and Goal

▪ Tools

- Intel® SDK for OpenCL® Applications
- DirectX® / OpenCL Interop API
- Intel® Graphics Performance Analyzers (Intel® GPA) tuning tool



▪ Progress

- Fine-tuning & optimizing OpenCL SimHD* on 3rd Generation Intel Core™ Processor
 - ✓ Optimizing playback pipeline to support DirectX Video Acceleration (DXVA) decoding and OpenCL SimHD together by using DirectX / OpenCL interop API
 - ✓ Reduce CPU usage, balance between CPU and GPU
- Optimizing kernel to fully take advantage of Intel® HD Graphics
 - ✓ Use local share memory instead of global memory to improve data access rate
 - ✓ Use simple calculation instead of reading data from local/global memory
 - ✓ Reduce number of registers to increase the parallelism of the kernel
 - ✓ Use logical operation instead of comparison/condition operation

▪ Goal

- Apply OpenCL to various ArcSoft* multimedia applications / engines



Thank you!





Q&A

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JULY 11 Getting Started with Intel® SDK for OpenCL Applications
JULY 18 Writing Efficient Code for OpenCL Applications
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May 24, 2012
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May 21, 2012
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- Free Downloads of Intel® Visual Computing Tools
- Code Samples
- Tech Articles
- Case Studies
- Forums
- Beta Programs

intel.com/software/vcsource
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Programming with Intel® SDK for OpenCL® Applications Tools

Tool Name	Used for	Included with the SDK for OpenCL	Supported devices and operating systems:		
			Windows* OSs		Linux* OSs
			CPU	GPU	CPU
Offline Compiler (Standalone)	Create and Build				
Offline Compiler (Microsoft Visual Studio* plug-in)	Create and Build				
Step by Step Kernel Debugger	Debug				
Intel® VTune™ Amplifier XE	Tune	For purchase on intel.com			
Intel® GPA System Analyzers	Tune	For free download on intel.com			
Intel® GPA Platform Analyzers with OpenCL® Traces Views	Tune	For free download on intel.com			

- The Intel® VTune™ Amplifier XE 2011 is available for download at <http://software.intel.com/en-us/articles/intel-vtune-amplifier-xe/>
- Intel® Graphics Performance Analyzers 2012 (Intel® GPA) is available as a free download at www.intel.com/software/gpa

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Mark the date and come and meet us in person

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August 5-9 | Los Angeles, California | Los Angeles Convention Center

- **IDF USA**

September 11 – 13 | San Francisco, California

- **Supercomputing 2012**

November 10 – 16, 2012 | Salt Lake City, Utah | Salt Palace Convention Center

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- Download Intel® Media SDK 2012 at No Cost
- Learn about OpenCL® tools: Register to next week webinar
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