

# Note for lecture 03:

P<sub>1</sub> P<sub>2</sub>

Definition: a "parity check code (or linear code)" is a linear transformation from the string of data bits to the string of data bits and parity checks.

Example: single parity check code

$$\underbrace{1011000}_{\text{data bits}} \xrightarrow{\text{transform}} \underbrace{1011000}_{\text{data bits}} \underbrace{1}_{\text{parity check}}$$

In general, there can be  $K$  data bits and  $L$  parity checks.

Definition: a "code word" is the result of the transform of a certain parity check code.

→ A code word is the data bits plus parity checks for

- ① single parity check
- ② horizontal & vertical parity check
- ③ CRC

a form of a code word:  $\boxed{\overbrace{10011 \dots 0}^K \overbrace{10 \dots 1}^L}$

The sender of data sends the code word to the receiver, who ~~then~~ will decode the code word to

- ① see if there is any bit error in the code word;
- ② retrieve the original data bits.

The receiver will not be able to detect bit error if the error has changed the code word to another code word, in which case, <sup>say, C<sub>A</sub></sup> ~~for~~ from the receiver's viewpoint, <sup>say, C<sub>B</sub></sup> it is entirely possible that code word C<sub>B</sub> was obtained by <sup>some other</sup> ~~a certain~~ string of data bits and was not because of the error.

Therefore, a useful criterion to measure the effectiveness of a parity check code is to look at the smallest number of bit changes that can convert one code word into another, which we say to be the minimum distance of a code.

A longer minimum distance is better, because it would take more bit errors to make a data receiver unable to detect an error; in other words, such a parity check code is more resilient to bit errors!



Exercise: show that the minimum distance of a code using a single parity check is 2.

Answer: We can first show that no two code words in this case can be differed by one.

Suppose code words  $X_1$  and  $X_2$  are differed by only one bit, then

prove by contradiction

→  $X_1$  and  $X_2$  cannot both have even number of 1s.

→ either  $X_1$  or  $X_2$  must have odd number of 1s, which cannot be a code word.

→ a contradiction.

Next, we give a witness, i.e., two code words such that the distance in between is 2.

$$S_1(D) = D^2 + D \rightarrow C_1(D) = 0$$

$$S_2(D) = D^2 + 1 \rightarrow C_2(D) = 0$$

and the distance between

$$S_1(D) \cdot D + C_1(D)$$

and

$$S_2(D) \cdot D + C_2(D) \text{ is } 2$$

### P3 Reasoning CRC:

represent a code word by  $X(D)$

$$X(D) = S(D) \cdot D^L + C(D)$$

by definition of a code word

its coefficients are data bits

its coefficients are parity checks

By choosing a generator polynomial,  $g(D)$  and compute  $\frac{S(D) \cdot D^L}{g(D)}$ ,

$$\text{we have } S(D) \cdot D^L = g(D) \cdot \zeta(D) + C'(D)$$

use ~~the~~ the remainder polynomial  $C'(D)$  ~~as~~ as parity checks.  $\Rightarrow C(D) = C'(D)$

$$\begin{aligned} \Rightarrow X(D) &= S(D) \cdot D^L + C(D) \\ &= g(D) \cdot \zeta(D) + C'(D) + C(D) \end{aligned}$$

$$= g(D) \cdot \zeta(D) \quad (\text{according to modular 2 computation})$$

which means  $g(D)$  divides  $X(D)$ .

Then, Send  $X(D) = g(D) \cdot \zeta(D)$  which equals  $S(D) \cdot D^L + C(D)$  to the receiver.

Now, use  $e(D)$  to represent errors introduced along the sending path.

Then the receiver gets  $Y(D) = X(D) + e(D)$

compute  $\frac{Y(D)}{g(D)}$ , and we see if  $e(D) = 0$

then  $g(D)$  must divide  $Y(D)$  and remainder = 0, otherwise, we say there's error.



An example of using CRC:

Suppose that 1100 are data bits to send, and  
 suppose that 101 are coefficients of the generator polynomial; i.e.,  $g(D) = D^2 + 1$

Therefore  $L = 2$ , which is equal to the degree of  $g(D)$ .

$$\Rightarrow X(D) = S(D) \cdot D^2 \text{ which gives } \boxed{110000}$$

to be replaced by  $C(D)$

Then the long division (modulo 2):

$$\begin{array}{r} 1111 \leftarrow g(D) \\ 101 \overline{) 110000} \leftarrow X(D) \\ \underline{101} \phantom{00} \\ 110 \phantom{00} \\ \underline{101} \phantom{00} \\ 110 \phantom{00} \\ \underline{101} \phantom{00} \\ 110 \phantom{00} \\ \underline{101} \phantom{00} \\ 11 \leftarrow C(D) \end{array}$$

$\nearrow$  I forgot to append this two bits when giving lecture in class on 9/28.

$\rightarrow$  The code word is  $\boxed{110011}$

we then send the code word to the other end of the channel.

The receiver of the code word can check out if there's any errors by performing another long division (again, modulo 2) by  $g(D)$ :

$\rightarrow$  see next page

$$\begin{array}{r} 1111 \\ 101 \overline{) 110011} \\ \underline{101} \phantom{00} \\ 110 \phantom{00} \\ \underline{101} \phantom{00} \\ 111 \phantom{00} \\ \underline{101} \phantom{00} \\ 101 \phantom{00} \\ \underline{101} \phantom{00} \\ 0 \end{array}$$

$0 \leftarrow C(D) = 0$   
 implies that there's no error!

Suppose there's an error and the receiver got 110111 instead:

$$\begin{array}{r} 1110 \\ 101 \overline{) 110111} \\ \underline{101} \phantom{00} \\ 111 \phantom{00} \\ \underline{101} \phantom{00} \\ 101 \phantom{00} \\ \underline{101} \phantom{00} \\ 01 \end{array}$$

$C(D) \neq 0$  implies that there's error!

The error polynomial in this case is  $e(D) = D^2$ , or 100, and we see that  $g(D)$  does not divide  $e(D)$

$$\begin{array}{r} 1 \\ 101 \overline{) 100} \\ \underline{101} \\ 01 \end{array}$$

this is in accordance with our reasoning on P4.

Finally, notice that CRC cannot detect error if  $e(D)$  happened to be equal to  $g(D)$ :

$$\begin{array}{r} 1 \\ 101 \overline{) 101} \\ \underline{101} \\ 0 \end{array} \quad \begin{array}{r} 1110 \\ 101 \overline{) 110111} \\ \underline{101} \phantom{00} \\ 111 \phantom{00} \\ \underline{101} \phantom{00} \\ 101 \phantom{00} \\ \underline{101} \phantom{00} \\ 00 \end{array} \equiv \underbrace{110011}_{\text{the code word}} + \underbrace{101}_{e(D)}$$

$00 \leftarrow C(D) = 0!$