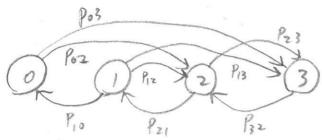
Name: Solution

Student ID: CSC0036 Homework 4

1

With the no-buffering assumption and m= 3 and \ =1.5 pkt/see



$$\begin{cases} P_{0} (P_{02} + P_{03}) = P_{1} (P_{10}) \\ P_{1} (P_{12} + P_{13} + P_{10}) = P_{2} (P_{21}) \end{cases}$$

$$P_{2} (P_{23} + P_{21}) = P_{3} (P_{32}) + P_{1} (P_{12}) + P_{0} (P_{02})$$

$$P_{3} (P_{32}) = P_{0} (P_{03}) + P_{1} (P_{13}) + P_{2} (P_{23})$$

$$P_{0} + P_{1} + P_{2} + P_{3} = 1$$

$$\Rightarrow \begin{cases}
P_0 + P_1 + P_2 + P_3 = 1 \\
P_3 (P_{32}) = P_0 (P_{03}) + P_1 (P_{13}) + P_2 (P_{23}) \\
P_2 (P_{21}) = P_0 (P_{02} + P_{03}) + P_1 (P_{12} + P_{13}) \\
P_1 (P_{10}) = P_0 (P_{02} + P_{03})
\end{cases}$$

 $P_{10} = Q_{\alpha}(0,1) Q_{\gamma}(1,1) = (1-g_{\alpha})^{2} g_{\gamma}$   $P_{21} = Q_{\alpha}(0,2) Q_{\gamma}(1,2) = (1-g_{\alpha}) \cdot 2 \cdot (1-g_{\gamma}) g_{\gamma}$   $P_{32} = Q_{\alpha}(0,3) Q_{\gamma}(1,3) = 3 \cdot (1-g_{\gamma})^{2} g_{\gamma}$   $P_{02} = Q_{\alpha}(2,0) = 3 \cdot (1-g_{\alpha}) g_{\alpha}^{2}$   $P_{03} = Q_{\alpha}(3,0) = g_{\alpha}^{3}$   $P_{12} = Q_{\alpha}(1,1) (1-Q_{\gamma}(0,1)) = 2 \cdot (1-g_{\alpha}) g_{\alpha}^{2} g_{\gamma}$   $P_{13} = Q_{\alpha}(2,1) = g_{\alpha}^{2}$   $P_{23} = Q_{\alpha}(1,2) \cdot (1-Q_{\gamma}(0,2)) = g_{\alpha}(1-(1-g_{\gamma})^{2})$ 

1.1 
$$9r = 0.2$$
,  $9r = 1 - e^{-\lambda/3} = 0.393$   
 $\Rightarrow 5 P_{10} = 0.093$ ,  $P_{02} = 0.281$ ,  $P_{13} = 0.154$   
 $P_{21} = 0.194$ ,  $P_{03} = 0.06$ ,  $P_{23} = 0.141$   
 $P_{32} = 0.384$ ,  $P_{12} = 0.095$ 

$$\Rightarrow N = 1.899 \Rightarrow T = 1.251$$
 seconds

2.1) The structure of the introduction section is simple here:
overview -> motivating example -> challenges -> how the paper contributes -> outline.
Each paragraph serves one purpose above.

The challenge is "with platform constraints and application requirements, it is hard to meet the requirement while being efficient."

P2 (2.2) The contributions are summarized in the fourth paragraph:

Da holistic analysis for real-time reliable Iot edge computing;

3 the ARREC orchitecture;

3 an implementation of ARREC and its empirical performance evaluation.

We may think in this way: on architecture needs to be based on some theoretical analysis that addresses the challenges we are to tackle; the analysis offers guidelines for us to design an architecture. An architecture needs to be implemented in a way that demonstrates the promised performance; the implementation and evaluation offer evidences for us to see the architecture's capability in dealing with the challenges mentioned.

3.2.1) Here's a reason for the misleading result:

The data in eze. delay. mislending contains some laterey results obtained while the broker was still busying accepting new connections. Those results have larger latercy. You may verify this by examining the first few lines in ere. delay. misleading. Now, with a longer sampling duration we essentially average out these abnormal results, and therefore the averaged end-to-end latency is shorter.

An important lesson that we may learn here: A software system often needs some time to "worm up" and thus we should avoid mixing the measurement obtained in the warm-up phase with that obtained in the later phase, to prevent some misinterpretation of the result.