& Signals, Systems, and Computing - In computing systems, information (and energy) is stored and transferred in terms of signals, which ove currents or voltages as a function of time. A circuit (or a system of circuits), as we study in this course, is used to o carry signals 1 Transform signals from one to another Computing is also a transformation of signals; though the signals are digital and the transformation is described at a higher layer of the abstraction (Figure 1.1 in the textbook). IV Example: We use a smart phase to record this lecture (voice signal -> convents and voltages), plied and the recording is stored in the phone, transferred via USB to your laptop, uploaded to a cloud drive, downloaded by your friend who cannot make it to the heat) class, and finally played by your friends speakers/ headphone (currents and voltages -> voice signals) analog signal to digital signal via discretization: 25V fine

P2 - The lumped circuit abstraction for now, let's just consider elements having two terminals, e.g., terminal pattery,

1 dement 2 lightbulb we may ignore the internal structure of an element and consider it as a lump", where we may completely describe relevant properties (such as voltage and current) by only observing its terminals. For example, if a resistor with resistance R obeys Ohm's law, then we may compute the current flowing through the resistor by the voltage across its terminals: I = V/RThe lumped circuit abstraction works only under certain constraints, which we call the lumped matter discipline (LMD)

From Physics to Electronic Circuit: A The lumped matter discipline (LMD) an activity or situation. -> simplify the analysis of electronic circuit -> modularize a complex circuit into analyzable elements Derivation of LMD: goal#1: example element able to describe a unique voltage 燈絲,U型,長度 across the terminals X and y definition of voltage: Vyx = - STE. dl where IE is the electrical field (a vector) dl is a tiny portion of I and Faraday's law of induction: $\oint E \cdot d\ell = -\frac{\partial \Phi g}{\partial t}$ where \$\phi_{\beta}\$ is the magnetic flux. & represents a closed path integral a sufficient condition Thus, we see that if there's no time-varying magnetic flux, $\oint |E \cdot d\ell| = O \Rightarrow \int_{X}^{Y} |E \cdot d\ell| + \int_{Y}^{X} |E \cdot d\ell| = O$ along Path 1 path 2 =) Sy IE. dl = Sy IE. dl Think about it. 也就是說 So IE-de 的值 why is this important? 和路徑無關! (= Sy E.dl)

P4 Therefore, the constraint for goal # 1 is $\frac{\partial \phi_B}{\partial t} = 0$ and we assumed that holds for all time. (To make sure this constraint holds, we may need to revise the model and introduce an element called able to define a unique current "inductor") through the terminals x and y First of all, the definition of current: $I = \int_{S_2} J \cdot dS$ where I is the current density at a given point within a filament and Sz is the cross-sectional surface of the filament at point z. Due to the conservation of charge, we have $\oint J \cdot dS = -\frac{28}{31}$ for a closed surface 流出的电量 減力的电量 J SX X T Sy Thus, if there's no time-varying charge within the closed surface, we have & J. ds = 0 => - S. J. ds + S.y J. ols = 0 假設公為唯一人口 \Rightarrow $\int_{S_y} J \cdot ds = \int_{S_x} J \cdot ds$ Sy為唯一出口. I in Jourt In = I out

Therefore, the constraint for good # 2 is 一一一一 and we assume that holds for all time. (To make sure this constraint holds, we may need to : Vise introduce revise the model and introduce an element called "capacitor".) r".) Besides goals #1 and #2, we also need to see, frequency ossume that the signal timescale must be much larger 5...0110100... for example than the propagation delay of electromagnetic waves across the lumped elements. (Otherwise, ... see tathor) => the size of our lumped elements must be much smaller than the wavelength associated with the V and I signals, and such a condition may be challenging to hold as we reduce the element size and increase the operating frequency (e.g., a 26Hz CPU)rtace definition of wavelength: the distance between two adjacent points in the wave having the same phase.

T: period, i.e., time required for a wave to travel for the distance of one wavelength. 100 C $\lambda = V \cdot T = V \cdot \frac{1}{f} \Rightarrow f \uparrow \text{ then } \lambda$ wavelength wave period frequency

For example, electromagnetic waves travel at about 15×104 km/s, or 15×109 cm/s, within a microprocessor (to be specific, through silicon dioxide). Now, suppose that the microprocessor operates at a clock rate of 2 GHz. This translates to the wavelength equal to) = 15×109/2×109 = 7.5 cm, which means that LMD may not hold if the microprocessor chip is larger than 7.5 cm on a side. Think about it: what if O clock rate ?? 3 wave speed ?? In general, in computer engineering, people are often working to meet various constraints (such as this) so that they may apply a previously erties established model (such as LMD) and make use of known results/ properties that depend on the given model. This is like "Standing on the shoulders of giarts." - Basic lumped element 1: batteries Two key properties energy 能量 (unit: joule "ampere-hours) 255 power Isize (unit: nott) 能量轉換或使用的速率 P=V·I itain (4MD)

Pa let & be the amount of energy supplied to an element over an interval T, then we have $\varepsilon = P \cdot T$, In general, the amount of energy supplied is the time integral of the power. 1 joule = | watt-second Example: Suppose a Raspherry P. consumes 2W of power and its energy is supplied by or 3.7 V, 2600 mA-h bottery. For how long can the battery power the Rospheny Pi? P=V-I = 3.7 × 2600 × 10-3 W-h = 9.62 W-h 9-62 W-h = 4.81 hours * - Basic lumped element 2: resistors (linear) Ohm's law: the voltage measured across the terminals of a resistor is linearly proportional to the current flowing through the resistor. That is, | = R we call it the resistance of a resistor. Further, R=Pa (see Appendix A.3 resistivity a month of the textbook)

also, $R = \rho \frac{1}{wh}$ with for a cube nitT, Example: Consider three planar resistors as follows HIL RI HIL RZ HIL R3 Let Ro = Po 1.H = 2 ks and ossume Po=P,=P2 = P3 Then $R_1 = P_1 = \frac{3}{3 \cdot H} = R_0 = 2 \times 72$ $\frac{R_1}{R_2} = \frac{P_1}{P_2} \frac{3H}{4H} = 1$ and $R_2 = R_1 = 2 k S R_2$ $\frac{R_2}{R_3} = \frac{P_2 + H}{P_3 + \frac{5}{3}H} = \frac{1}{3} \Rightarrow R_3 = \frac{1}{3} = \frac{1}{3} \Rightarrow R_3 = \frac{1}{3} =$ exercise: you can verify that $\frac{R_1}{R_3} = \frac{R_2}{R_3}$ > 等比例缩小長及寬,則相對電阻值_ ⇒缩小晶片的大小不會改變相對電阻值 A. J B. J C. 不變 ninals -) Often, signal values are derived as a function of resistance ratios. Therefore, by such a process tor. shrink, the chip may continue to function as before! sexample: a "voltage divider," which we will study soon this semester.