International Rectifier

IRLML2803

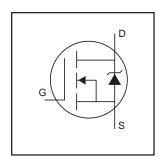
HEXFET® Power MOSFET

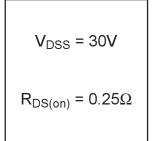
- Generation V Technology
- Ultra Low On-Resistance
- N-Channel MOSFET
- SOT-23 Footprint
- Low Profile (<1.1mm)
- Available in Tape and Reel
- Fast Switching

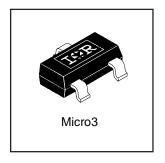


Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

A customized leadframe has been incorporated into the standard SOT-23 package to produce a HEXFET Power MOSFET with the industry's smallest footprint. This package, dubbed the Micro3, is ideal for applications where printed circuit board space is at a premium. The low profile (<1.1mm) of the Micro3 allows it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards.







Absolute Maximum Ratings

	•		
	Parameter	Max.	Units
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	1.2	
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V	0.93	A
I _{DM}	Pulsed Drain Current ①	7.3	
P _D @T _A = 25°C	Power Dissipation	540	mW
	Linear Derating Factor	4.3	mW/°C
V _{GS}	Gate-to-Source Voltage	± 20	V
dv/dt	Peak Diode Recovery dv/dt ②	5.0	V/ns
T _{J.} T _{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{eJA}	Maximum Junction-to-Ambient @		230	°C/W

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30			V	$V_{GS} = 0V, I_D = 250 \mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.029		V/°C	Reference to 25°C, I _D = 1mA
	Ctatic Busin to Course On Besintance			0.25		V _{GS} = 10V, I _D = 0.91A ③
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.40	Ω	V _{GS} = 4.5V, I _D = 0.46A ③
V _{GS(th)}	Gate Threshold Voltage	1.0			V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
9fs	Forward Transconductance	0.87			S	V _{DS} = 10V, I _D = 0.46A
I _{DSS}	Drain-to-Source Leakage Current			1.0		V _{DS} = 24V, V _{GS} = 0V
טאטי	Drain-to-Godroe Leakage Garrent			25	μA	V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
lasa	Gate-to-Source Forward Leakage			-100	nA	V _{GS} = -20V
I _{GSS}	Gate-to-Source Reverse Leakage			100	IIA	V _{GS} = 20V
Qg	Total Gate Charge		3.3	5.0		$I_D = 0.91A$
Q _{gs}	Gate-to-Source Charge		0.48	0.72	nC	V _{DS} = 24V
Q _{gd}	Gate-to-Drain ("Miller") Charge		1.1	1.7		V _{GS} = 10V, See Fig. 6 and 9 ③
t _{d(on)}	Turn-On Delay Time		3.9			V _{DD} = 15V
t _r	Rise Time		4.0			$I_D = 0.91A$
t _{d(off)}	Turn-Off Delay Time		9.0		ns	$R_G = 6.2\Omega$
t _f	Fall Time		1.7			R_D = 16 Ω , See Fig. 10 ③
C _{iss}	Input Capacitance		85			V _{GS} = 0V
Coss	Output Capacitance		34		pF	V _{DS} = 25V
C _{rss}	Reverse Transfer Capacitance		15			f = 1.0MHz, See Fig. 5

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			0.54		MOSFET symbol
	(Body Diode)			0.54	Α	showing the
I _{SM}	Pulsed Source Current			7.0		integral reverse
	(Body Diode) ①			7.3		p-n junction diode.
V _{SD}	Diode Forward Voltage			1.2	V	T _J = 25°C, I _S = 0.91A, V _{GS} = 0V ③
t _{rr}	Reverse Recovery Time		26	40	ns	$T_J = 25$ °C, $I_F = 0.91A$
Q _{rr}	Reverse RecoveryCharge		22	32	nC	di/dt = 100A/μs ③

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- $\textcircled{2} \ \ I_{SD} \leq 0.91 \text{A}, \ di/dt \leq 120 \text{A}/\mu\text{s}, \ V_{DD} \leq V_{(BR)DSS}, \qquad \textcircled{4} \ \ \ \text{Surface mounted on FR-4 board}, \ \ t \leq \ 5 \text{sec}.$ T_J ≤ 150°C

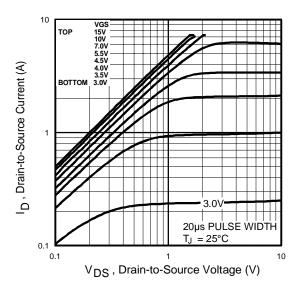


Fig 1. Typical Output Characteristics

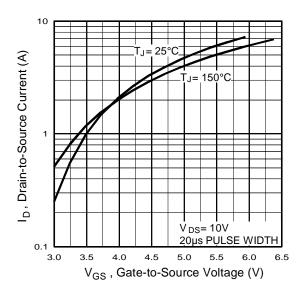


Fig 3. Typical Transfer Characteristics

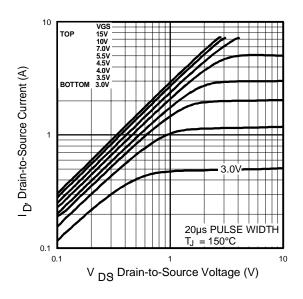


Fig 2. Typical Output Characteristics

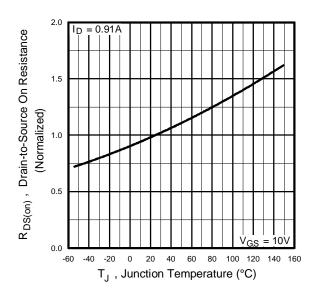


Fig 4. Normalized On-Resistance Vs. Temperature

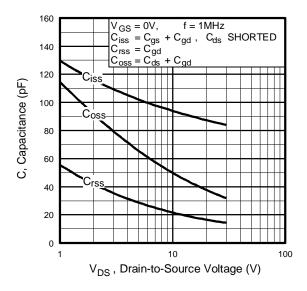


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

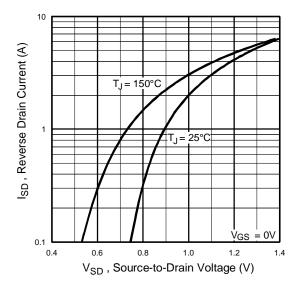


Fig 7. Typical Source-Drain Diode Forward Voltage

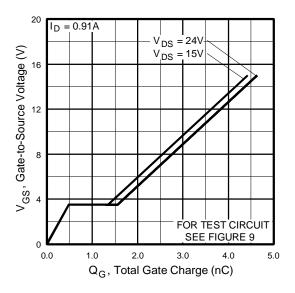


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

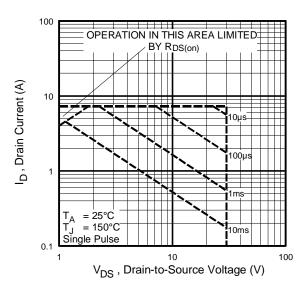


Fig 8. Maximum Safe Operating Area

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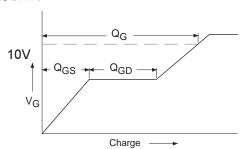


Fig 9a. Basic Gate Charge Waveform

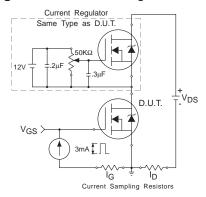


Fig 9b. Gate Charge Test Circuit

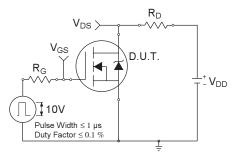


Fig 10a. Switching Time Test Circuit

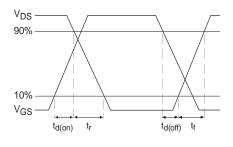


Fig 10b. Switching Time Waveforms

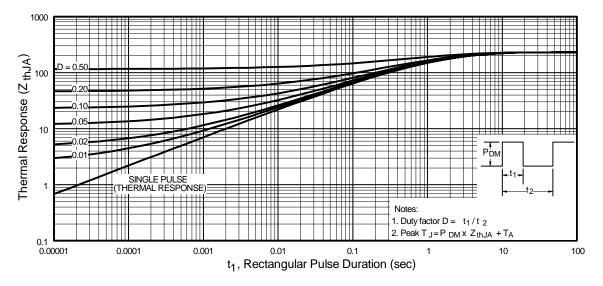
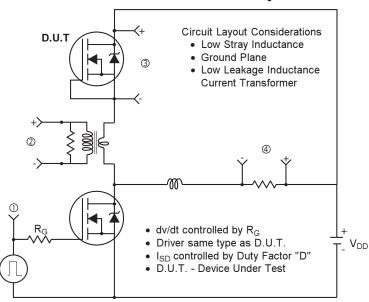


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

Peak Diode Recovery dv/dt Test Circuit



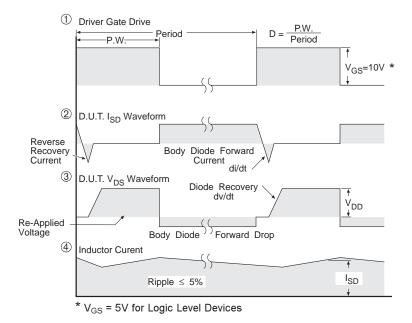


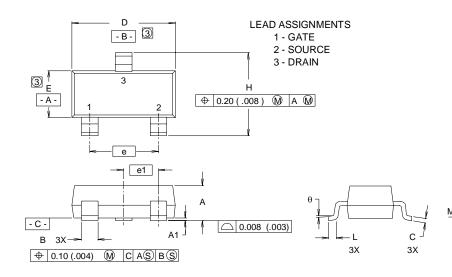
Fig 12. For N-Channel HEXFETS

International IOR Rectifier IRLML2803

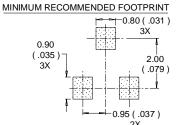
Package Outline

SOT-23 Outline

Dimensions are shown in millimeters (inches)



DIM	INC	HES	MILLIMETER			
DIM	MIN	MAX	MIN	MAX		
Α	.032	.044	0.82	1.11		
A1	.001	.004	0.02	0.10		
В	.015	.021	0.38	0.54		
С	.004	.006	0.10	0.15		
D	.105	.120	2.67	3.05		
е	.0750	.0750 BASIC		1.90 BASIC		
e1	.0375	BASIC	0.95 B	ASIC		
Е	.047	.055	1.20	1.40		
Н	.083	.098	2.10	2.50		
L	.005	.010	0.13	0.25		
θ	0°	8°	0°	8°		
L	.005	.010	0.13	0.25		



NOTES:

- 1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
 2. CONTROLLING DIMENSION : INCH.
 3 DIMENSIONS DO NOT INCLUDE MOLD FLASH.

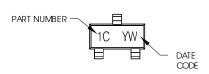
IRLML2803

International TOR Rectifier

Part Marking Information sot-23

Notes: This part marking information applies to devices produced before 02/26/2001

EXAMPLE: THIS IS AN IRLML6302 WW = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR



PART NUMBER CODE REFERENCE:

1A = IRLML2402 1B = IRLML2803 1C = IRLML6302 1D = IRLML5103 1E = IRLML6402 1F = IRLML6401 1G = IRLML2502

DATE CODE EXAMPLES:

1H = IRLML5203

YWW = 9503 = 5C YWW = 9532 = EF

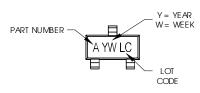
YEAR	Υ	WORK WEEK	W
2001	1	01	
2002	2	02	В
2003	3	03	С
1994	4	04	D
1995	5		
1996	6		
1997	7		
1998	8	1	1
1999	9	1	7
2000	0	24	Χ
		25	Υ
		26	Z

WW = (27-52) IF PRECEDED BY A LETTER

YEAR	Υ	WORK WEEK	W
2001	Α	27	Α
2002	В	28	В
2003	С	29	С
1994	D	30	D
1995	E		
1996	F		
1997	G		
1998	Н	1	1
1999	J	7	1
2000	K	50	X
		51	Υ
		52	Z

Notes: This part marking information applies to devices produced after 02/26/2001

W = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR



PART NUMBER CODE REFERENCE:

A = IRLML2402 B = IRLML2803 C = IRLML6302 D = IRLML5103 E = IRLML6402 F = IRLML6401 G = IRLML2502 H = IRLML5203

YEAR	Υ	WORK WEEK	W
2001	1	01	Α
2002	2	02	В
2003	3	03	С
1994	4	04	D
1995	5		
1996	6		
1997	7		
1998	8	1	1
1999	9	7	7
2000	0	24	Χ
		25	Υ
		26	Z

W = (27-52) IF PRECEDED BY A LETTER

YEAR	Υ	WORK WEEK	W
2001	Α	27	Α
2002	В	28	В
2003	С	29	С
1994	D	30	D
1995	Е		
1996	F		
1997	G		
1998	Н	1	1
1999	J	7	7
2000	K	50	X
		51	Υ
		52	Z

International

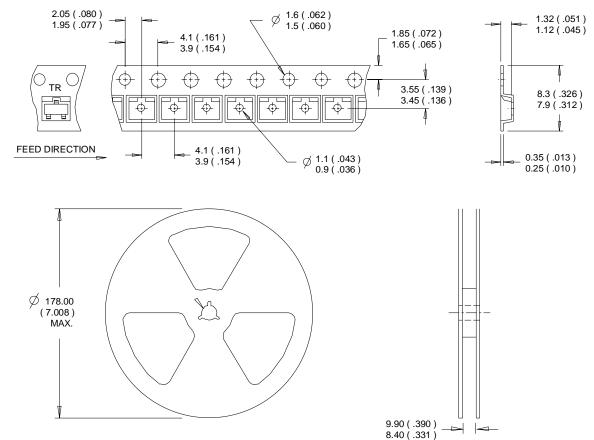
TOR Rectifier

IRLML2803

Tape & Reel Information

SOT-23

Dimensions are shown in millimeters (inches)



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.



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