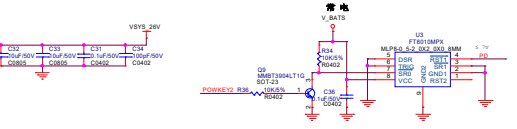
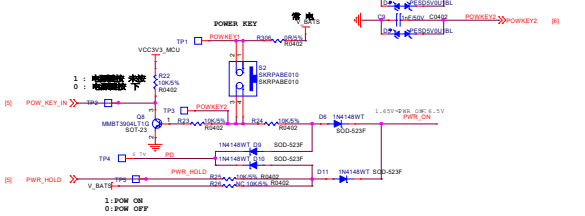
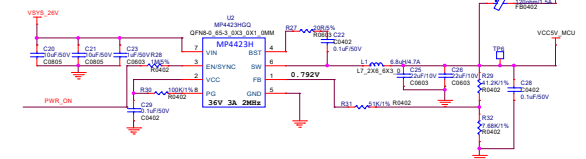
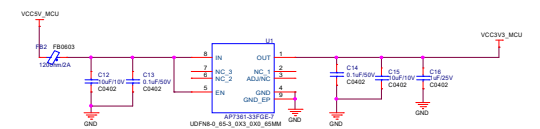
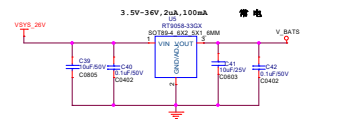


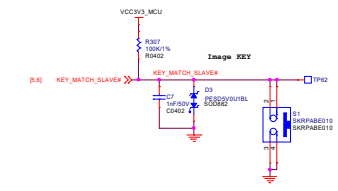
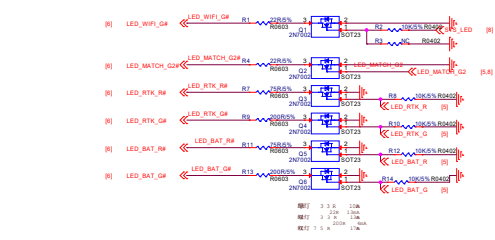
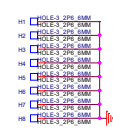
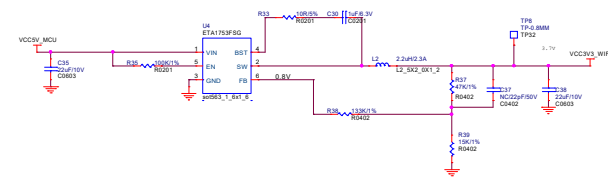
开关按键，位置有严格限制



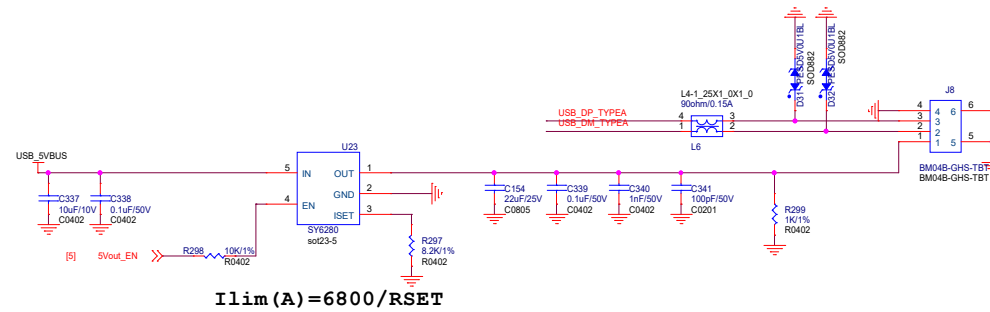
MCU 3.3V-3.6V, 2uA, 100mA



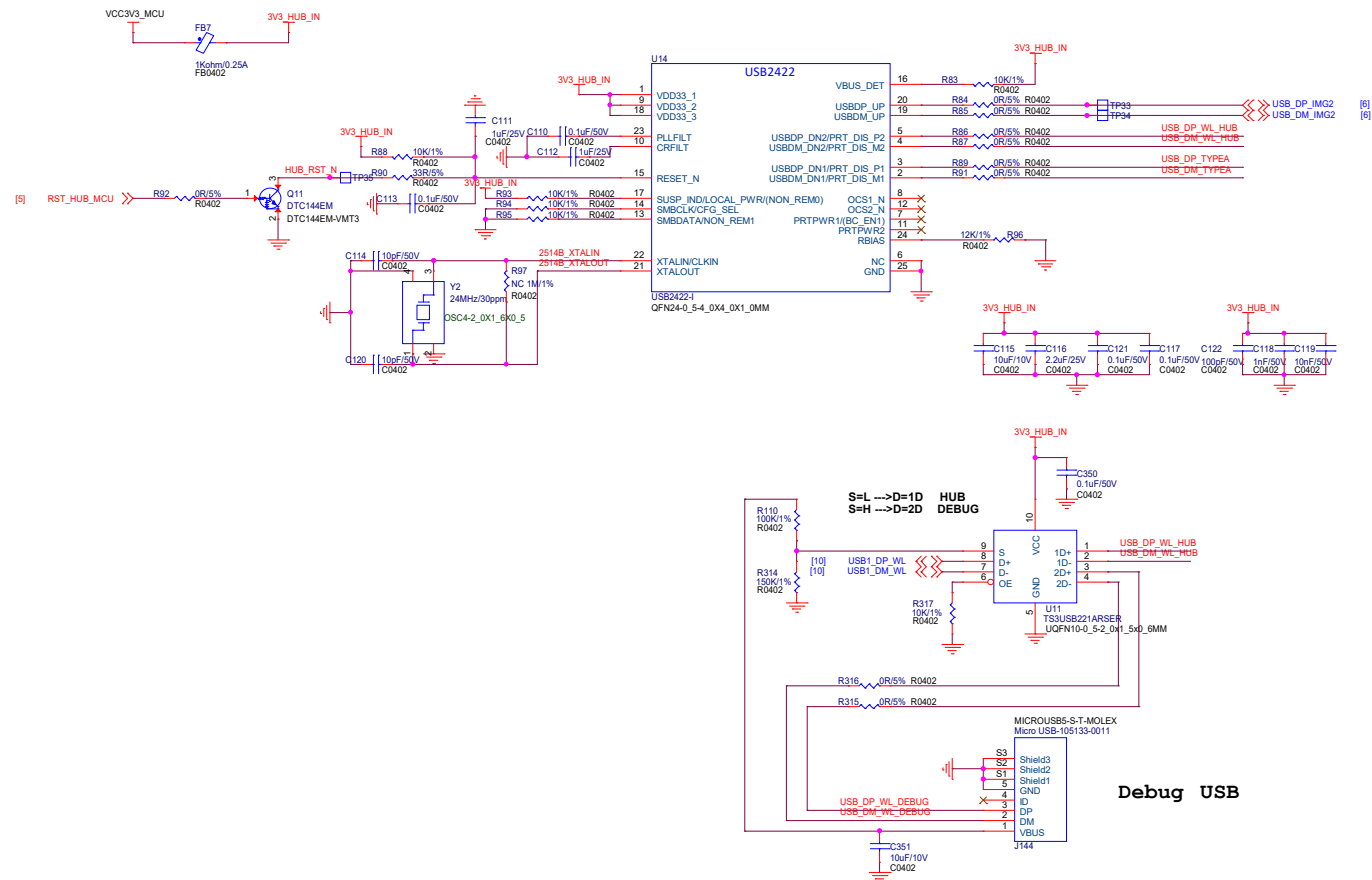
MCU 3.3V-3.6V, 2uA, 100mA



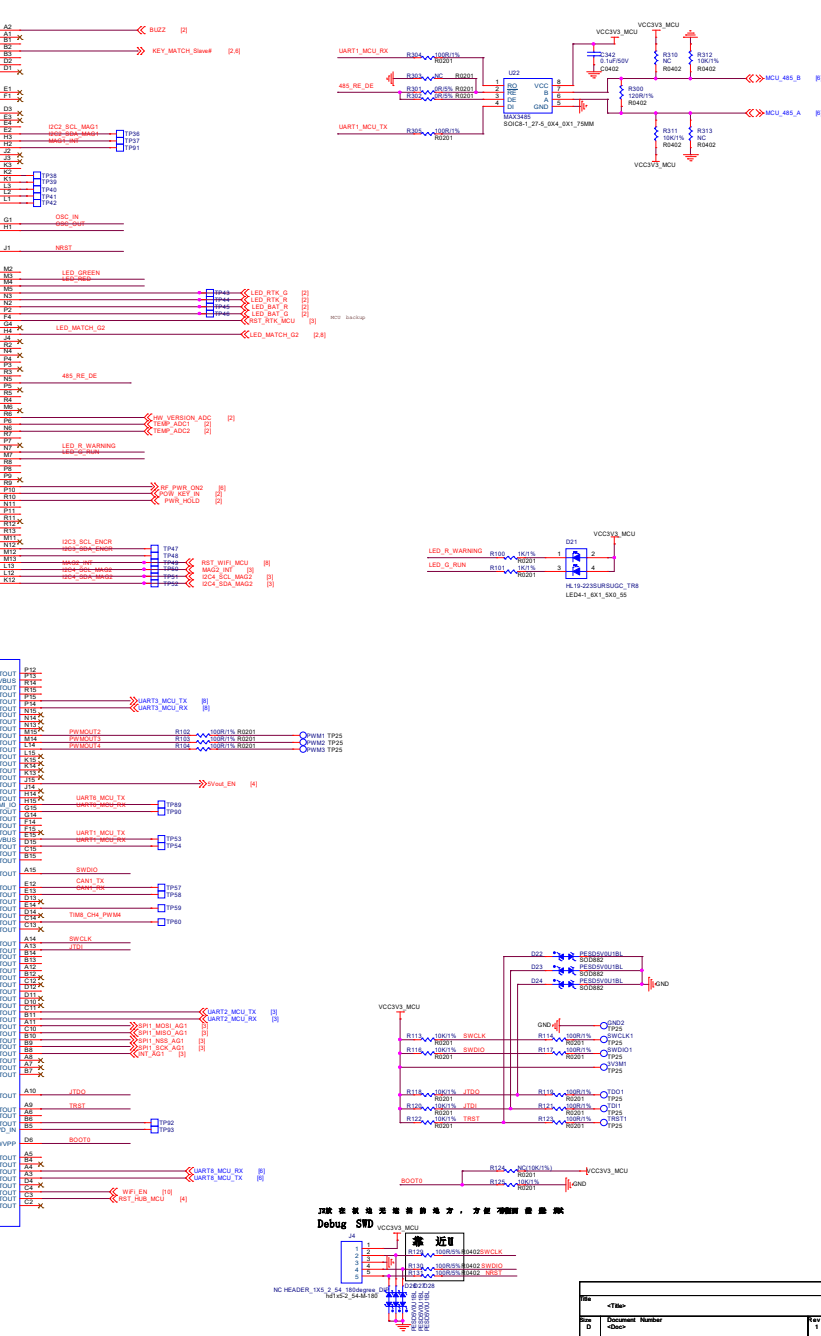
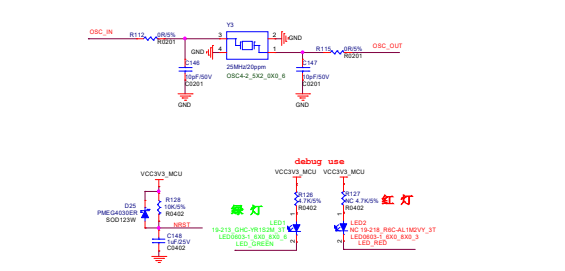
接对外 防 水USB接头



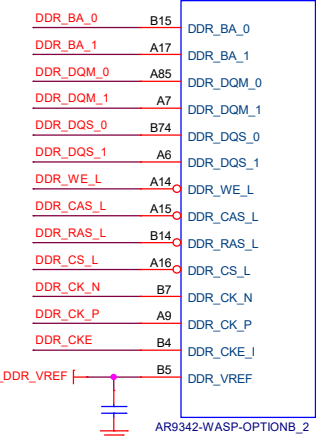
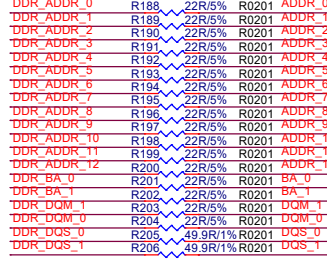
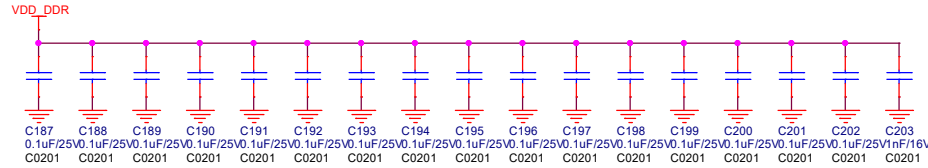
$$I_{lim}(A) = 6800 / R_{SET}$$



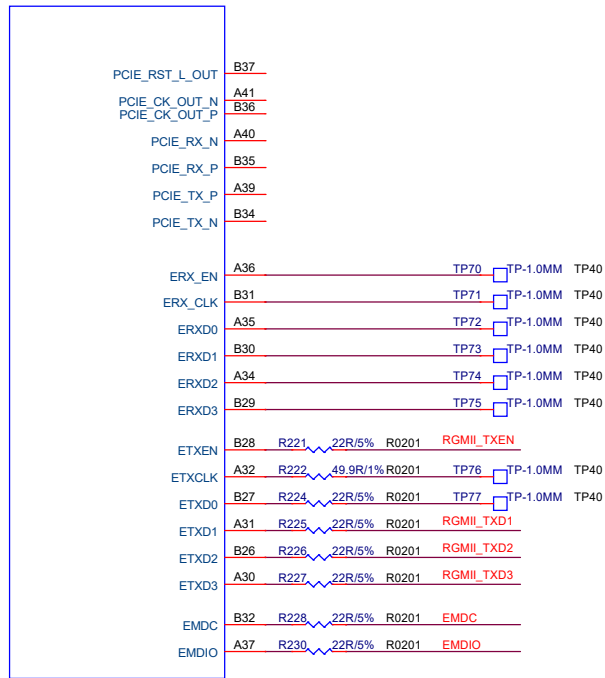
Title <Title>			
Size C	Document Number <Doc>		Rev 1
Date:	Monday, October 18, 2021	Sheet 4 of 10	



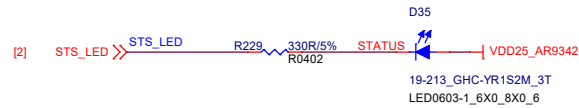
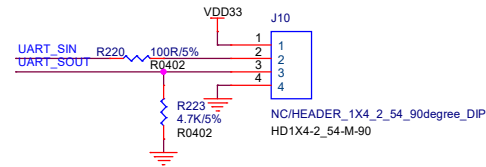
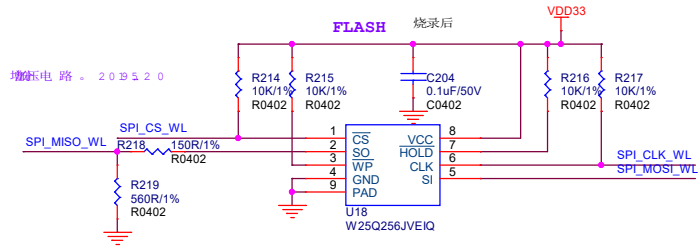
Title <Title>		
Size D	Document Number <Doc>	Rev 1
Date:	Wednesday, October 13, 2021	Sheet 5 of 10



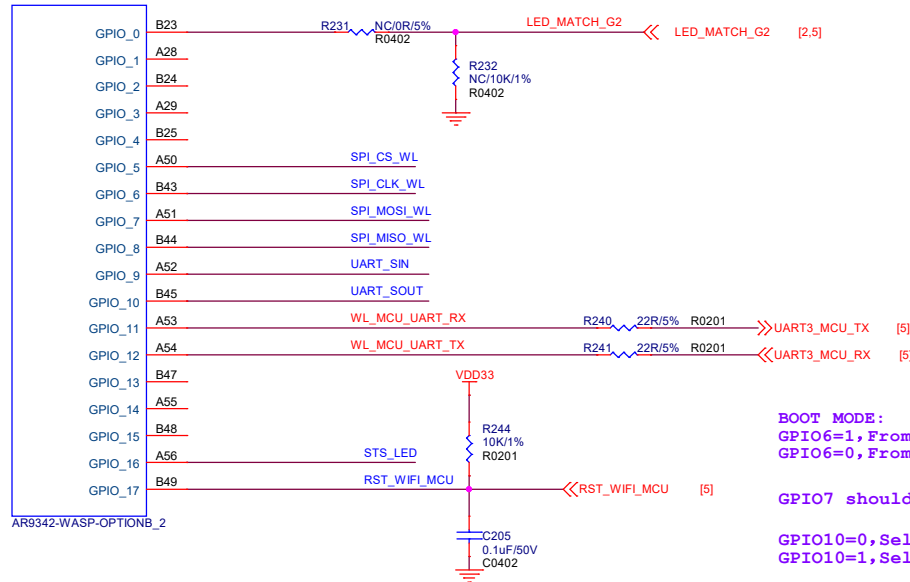
U17H



AR9342-WASP-OPTIONB_2



U17F



AR9342-WASP-OPTIONB_2

RGMIITXEN=0, JTAG Mode
RGMIITXEN=1, EJTAG Mode

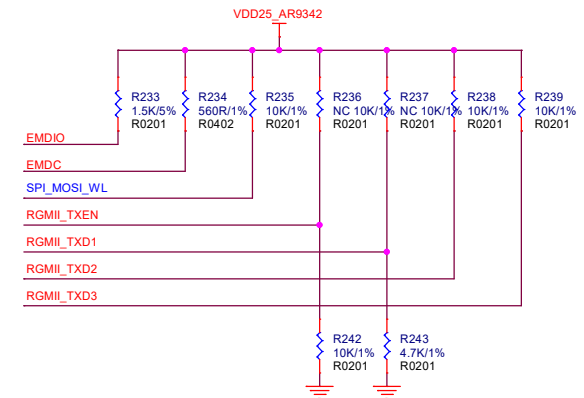
RGMIITXD1=0, USB Host Mode
RGMIITXD1=1, USB Device Mode

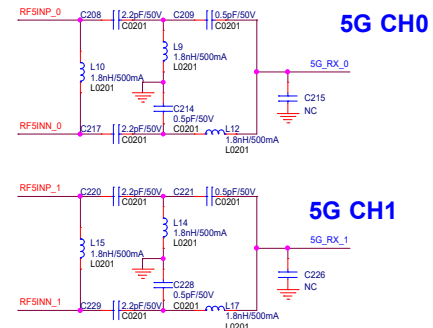
RGMIITXD2=0, PCIE_EP(default)
RGMIITXD2=1, PCIE_R#

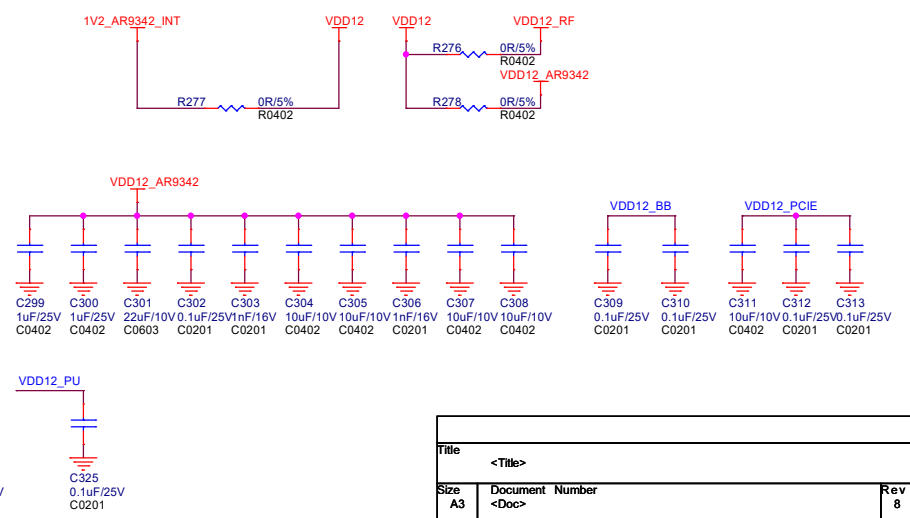
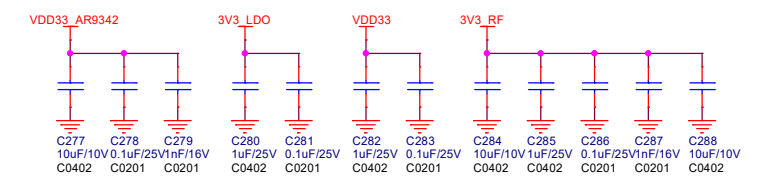
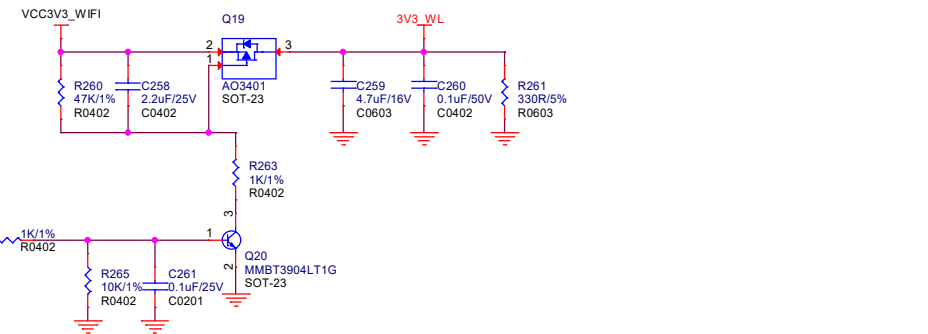
RGMIITXD3=1, REF_CLK#25MM ;
RGMIITXD3=0, REF_CLK#40MM ;

GPIO7 should be tied to 1;

GPIO10=0, Select DDR2
GPIO10=1, Select DDR1







Title			
<Title>			
Size A3	Document Number <Doc>		Rev 8
Date:	Wednesday, October 13, 2021	Sheet	10 of 10