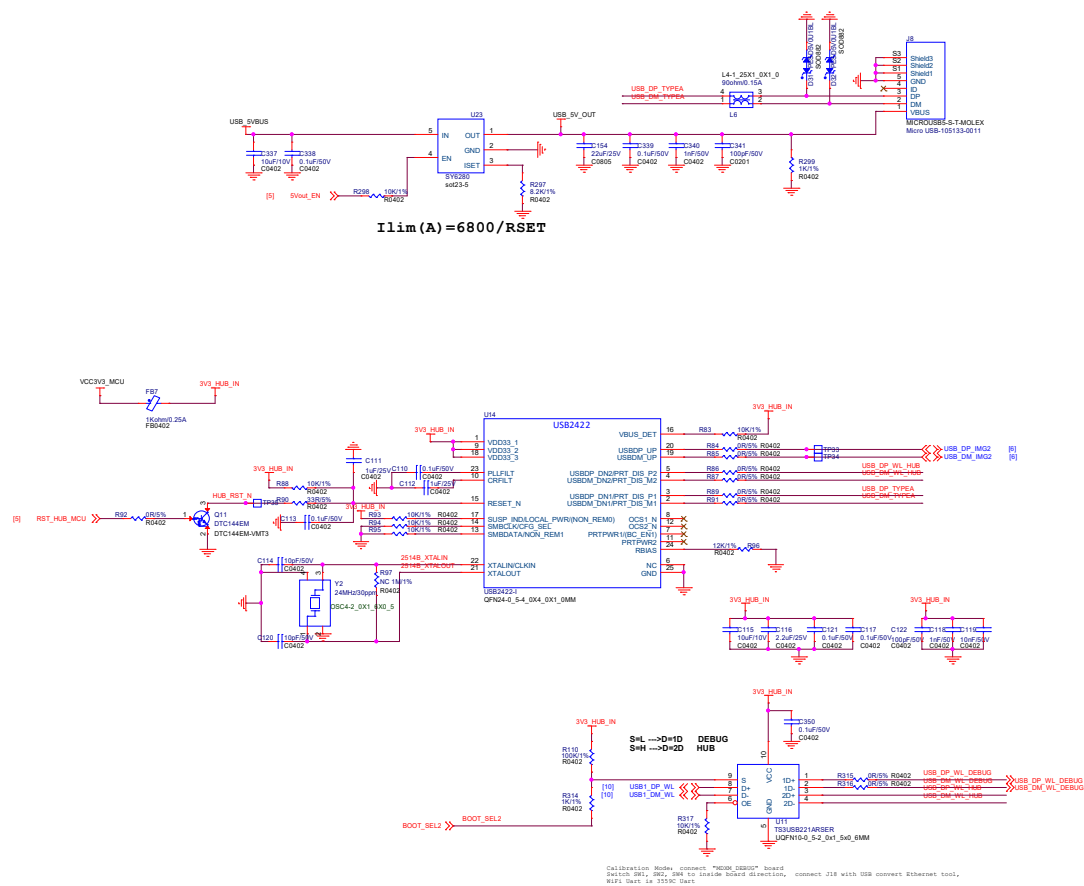
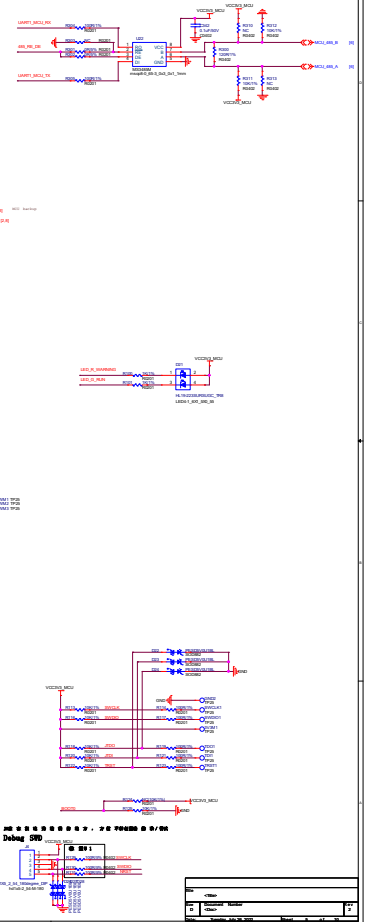
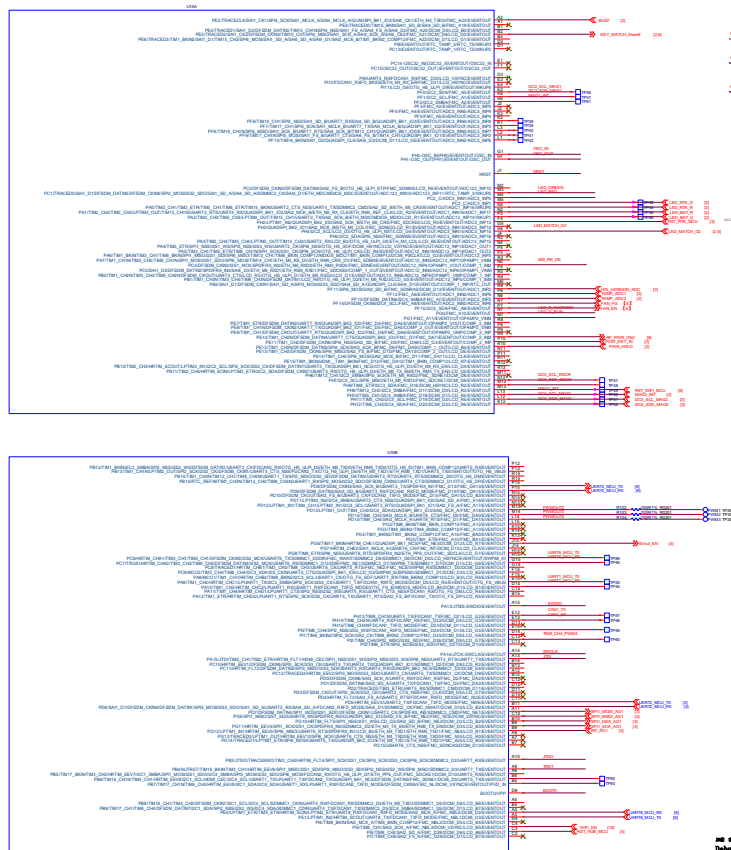
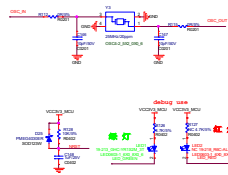


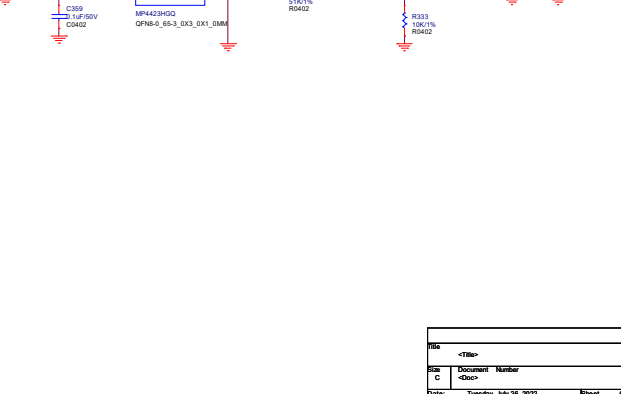
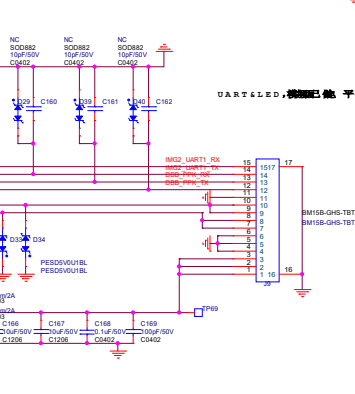
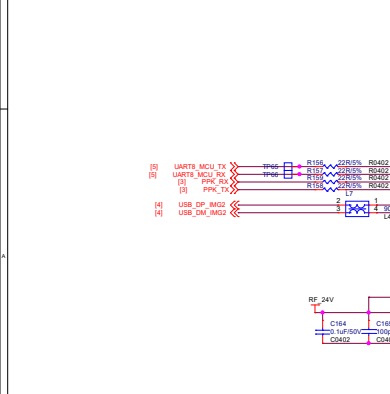
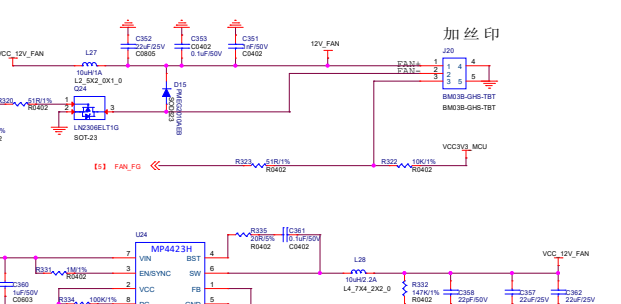
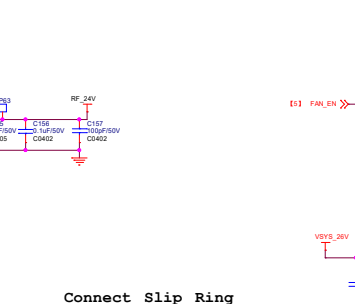
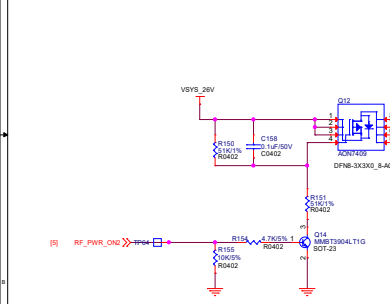
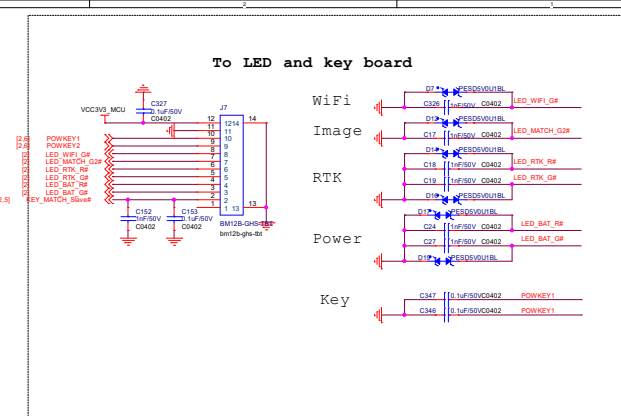
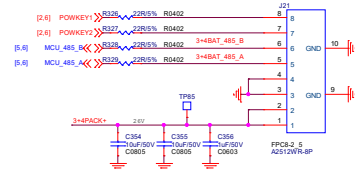
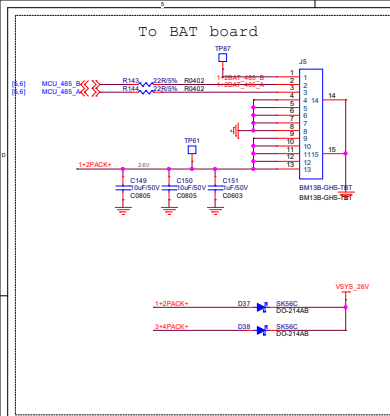
接对外 防 水USB接头



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Size	Document	Number	Rev
C	<Doc>		2
Date:	Wednesday, July 20, 2022		Sheet 4 of 10



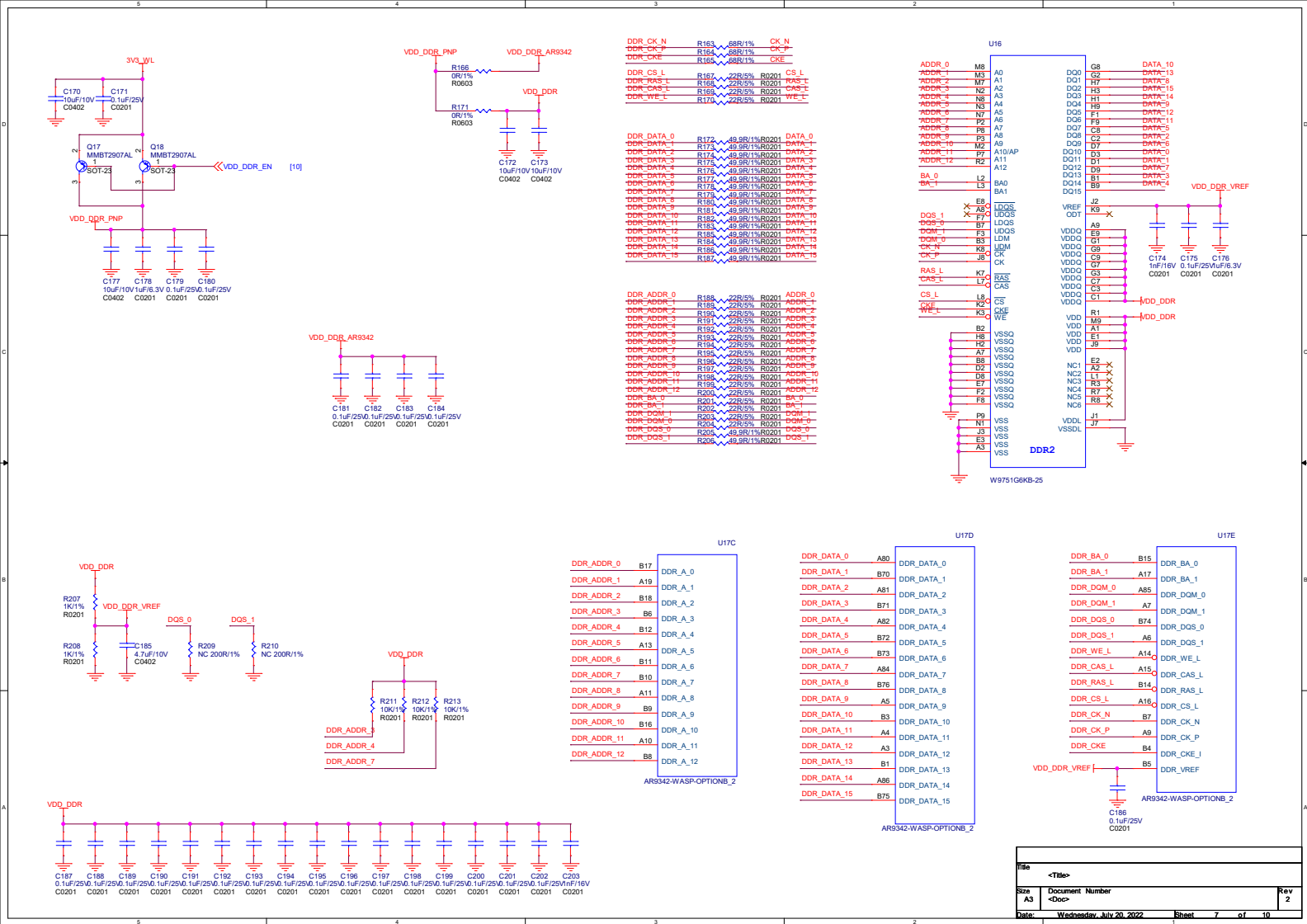
To EXT BAT BOX
Connect LP-12-J05SX-03-4XXA

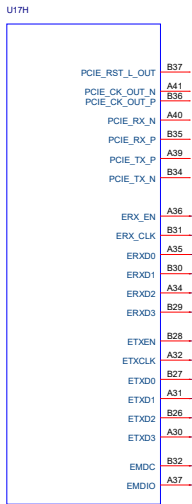


Connect Slip Ring

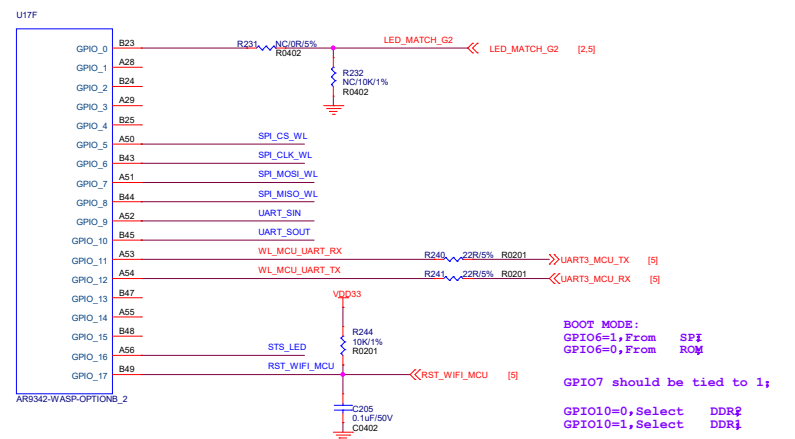
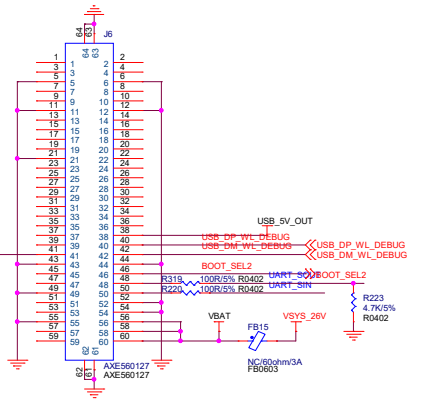
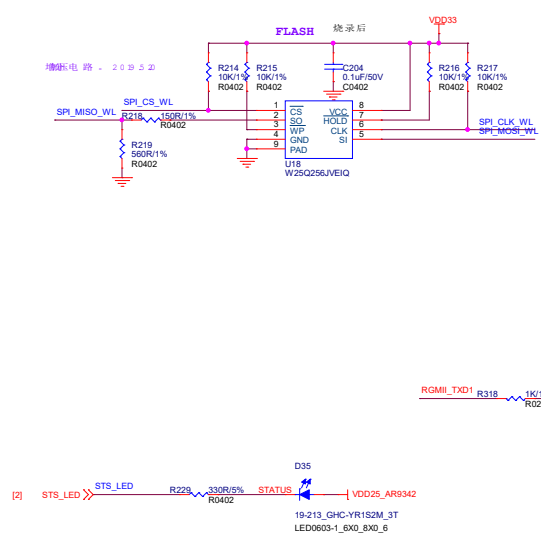
UART5LED, 数据已能正常转换

File	<File>	Rev
Doc	Document Number	2
Rev	<Rev>	
Date	2022.07.26.2022	Sheet 8 of 16





AR9342-WASP-OPTIONB_2



AR9342-WASP-OPTIONB_2

BOOT MODE:
GPIO6=1,From SPI
GPIO6=0,From ROM

GPIO7 should be tied to 1;

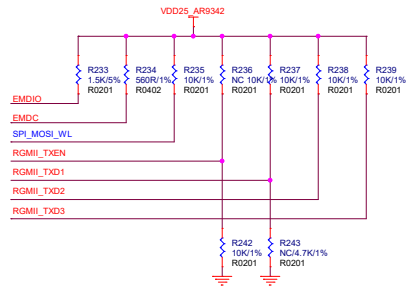
GPIO10=0,Select DDR2
GPIO10=1,Select DDR1

RGMII_TXEN=0,JTAG Mode
RGMII_TXEN=1,EJTAG Mode

RGMII_TXD1=0,USB Host Mode
RGMII_TXD1=1,USB Device Mode

RGMII_TXD2=0,PCIE EP(default)
RGMII_TXD2=1,PCIE_Rq

RGMII_TXD3=1,REF_CLK25MM ;
RGMII_TXD3=0,REF_CLK40MM ;



File			
<Title>			
Size	Document Number	Rev	
A3	<Doc>	2	
Date	Wednesday, July 20, 2022	Sheet	8 of 10

