

Report

- ☐ Create DRC markers for warnings
- ☐ Check hierarchical port connections
- ☐ Check off-page connector connection
- ☒ Report identical part references
- ☒ Report invalid packaging
- ☐ Report hierarchical ports and off-page
- ☒ Check unconnected nets
- ☐ Check SDT compatibility
- ☐ Report off-grid objects
- ☐ Report all net names

Report File: ☐ View Output

D:\NORCAD TRAINING\FLEX.APA206912-16.DRC

Browse

- 1.检查违背标准的错误,一般不选
- 2.检查分层式结构电路中上一层电路中子电路框图引出端与代表该框图结构的下一层电路中的端口是否匹配
- 3.检查页面之间的端口连接是否匹配
- 4.检查元件编号是否相同
- 5.检查元件封装是否匹配
- 6.输出报告时列出所有端口符号
- 7.检查节点连接情况
- 8.建议该项不选
- 9.是否存在元件引出点未在电路图纸网格点的情况
- 10.输出所有节点名
- 11.报告观看以及保存路径选择