



- Create Module...
- Padstack
- Pad
- Derive Connectivity...
- Reports...
- Quick Reports
- Technology File Compare...
- Setup Advisor...
- Database Check...
- Update DRC
- Design Compare...
- Utilities



- Assigned Function Report
- Bill of Material Report
- Bill of Material Report (Condensed)
- Cadence Schematic Feedback Report
- Component Pin Report
- Component Report
- Dangling Lines Report
- Design Partition Report
- Design Rules Check Report
- Etch Length by Layer Report
- Etch Length by Layer and Width Report
- Etch Length by Net Report
- Etch Length by Pin Pair Report
- Film Area Report
- Function Pin Report
- Function Report
- Missing Fillets Report
- Module Report
- Net List Report
- Net Single Pin and No Pin
- Netin (back anno.)
- Netin (non-back)
- Padstack Definition Report
- Padstack Usage Report
- Pin Swap Report
- Placed Component Report
- Properties on Nets Report
- Shape Dynamic State
- Shape Islands
- Shape No Net
- Shape Report
- Slot Hole Report
- Spare Function Report
- Summary Drawing Report
- Symbol Availability Check
- Symbol Library Path Report
- Symbol Pin Report
- Testprep Report
- Unassigned Functions Report
- Unconnected Pins Report
- Unplaced Components Report
- Unused Blind/Buried Via Report
- User Schedule (back anno.)
- Waived Design Rules Check Report

Command Line: Opening existing drawing... Opening existing drawing... Command >

Idle

Windows taskbar: 开始, W, 肩, 王, 河, 器, 毕, P, A, P, 技, O, p

System tray: GEN, DRC, 14:45