

DUAL SURFACE MOUNT SWITCHING DIODE

Features

Fast Switching Speed

Surface Mount Package Ideally Suited for

Automatic Insertion

For General Purpose Switching Applications

High Conductance

Mechanical Data

Case: SOT-23, Molded Plastic

Case material - UL Flammability Rating

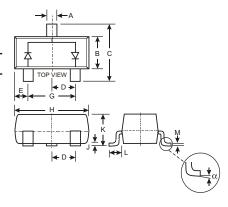
Classification 94V-0

Moisture sensitivity: Level 1 per J-STD-020A

Terminals: Solderable per MIL-STD-202,

Method 208

Polarity: See Diagram Marking: KJH (See Page 2) Weight: 0.008 grams (approx.)



SOT-23								
Dim	Min	Max						
Α	0.37	0.51						
В	1.20	1.40						
С	2.30	2.50						
D	0.89	1.03						
E	0.45	0.60						
G	1.78	2.05						
Н	2.80	3.00						
J	J 0.013 0.1							
K	0.903	1.10						
L	0.45	0.61						
М	0.85	0.80						
0 8								
All Dimensions in mm								

Maximum Ratings @ TA = 25 C unless otherwise specified

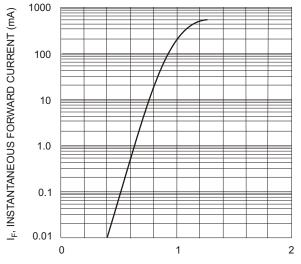
Characteristic	Symbol	MMBD7000	Unit		
Non-Repetitive Peak Reverse Voltage	V _{RM}	100	V		
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	75	V		
RMS Reverse Voltage	V _{R(RMS)}	53	V		
Forward Continuous Current (Note 1)	I _{FM}	300	mA		
Non-Repetitive Peak Forward Surge Current @ t = 1.0 s @ t = 1.0s	I _{FSM}	2.0 1.0	Α		
Power Dissipation (Note 1)	Pd	350	mW		
Thermal Resistance Junction to Ambient Air (Note 1)	R JA	357	C/W		
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +150	С		

Electrical Characteristics @ TA = 25 C unless otherwise specified

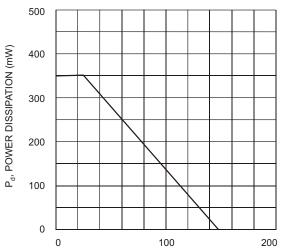
Characteristic	Symbol	Min	Max	Unit	Test Condition		
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	75		V	I _R = 100 A		
Forward Voltage (Note 2)	V _F	0.55 0.67 0.75	0.70 0.82 1.10 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA		
Reverse Current (Note 2)	I _R		1.0 3.0 100 25	A A A nA	$V_R = 50V$ $V_R = 100V$ $V_R = 50V$, $T_j = 125$ C $V_R = 20V$		
Total Capacitance	C _T		2.0	pF	V _R = 0, f = 1.0MHz		
Reverse Recovery Time	t _{rr}		4.0	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100$		

Notes:

- 1. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. Short duration test pulse used to minimize self-heating effect.







T_A, AMBIENT TEMPERATURE (°C) Fig. 3 Power Dissipation Derating

10,000 PARAGE CORRENT (u) 100 PARAGE CORRENT

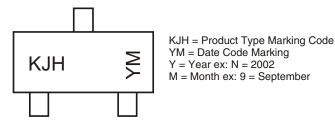
 $\label{eq:total_total_total} \textbf{T}_{j}, \text{JUNCTION TEMPERATURE (°C)} \\ \text{Fig. 2 Leakage Current vs Junction Temperature}$

Ordering Information (Note 3)

Device	Packaging	Shipping		
MMBD7000-7	SOT-23	3000/Tape & Reel		

Notes: 3. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



Date Code Key

Year	1999	2000	2001	2002	2003	2004	2005	
Code	K	L	М	N	Р	R	S	

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D