

VerifyIO: Verifying Adherence to Parallel I/O Consistency Semantics

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Abstract—High-performance computing (HPC) applications generate and consume substantial amounts of data, typically managed by parallel file systems. These applications access file systems either through the POSIX interface or by using high-level I/O libraries. While the POSIX consistency model remains dominant in HPC, emerging file systems and popular I/O libraries increasingly adopt alternative consistency models that relax semantics in various ways, creating significant challenges for correctness and portability. This paper addresses these challenges by proposing a trace-driven I/O consistency verification workflow, implemented in our open-source tool, VerifyIO. VerifyIO collects execution traces, detects data conflicts, and verifies proper synchronization against specified consistency models. Our extensive evaluation of 91 test case executions across three widely-used I/O libraries against four I/O consistency models reveals critical consistency issues at both the application and implementation levels.

Index Terms—Consistency Semantics, POSIX Semantics, MPI-IO, I/O Consistency, Parallel File System

I. INTRODUCTION

HPC applications, such as scientific simulations and AI training, generate and process vast amounts of data, imposing significant demands on I/O systems to ensure efficient and correct data handling. These I/O demands are typically supported by parallel file systems. To ensure portability and compatibility, most widely deployed parallel file systems, such as Lustre [1] and GPFS [2], conform to the POSIX [3] standard, providing both the POSIX interface and its associated consistency semantics. HPC applications access these parallel file systems either directly through POSIX APIs or via higher-level I/O libraries (e.g., PnetCDF [4] and HDF5 [5]). These libraries offer more user-friendly interfaces and implement various parallel I/O optimizations such as I/O aggregation, data sieving, and asynchronous I/O. While these libraries may eventually invoke POSIX APIs for portability, they often adopt relaxed consistency models (weaker than POSIX) for better performance. For instance, HDF5 and PnetCDF, both built on MPI-IO [6], use MPI-IO’s relaxed consistency model, which diverges from the strict guarantees of POSIX.

On the storage side, though many HPC systems continue to use POSIX-compliant file systems, recent trends show that emerging file systems such as BurstFS [7], UnifyFS [8], and GfarmBB [9]—are choosing to relax POSIX consistency semantics while maintaining the POSIX interface. By keeping

the POSIX interface, they support legacy applications while relaxing consistency semantics to improve performance. However, this trade-off introduces new risks related to portability and correctness, especially when applications assume stricter POSIX guarantees.

A key question emerges: How can we ensure that applications behave correctly on systems with different consistency models? Even if an application and its associated I/O libraries are programmed using POSIX APIs, how can we verify that they adhere to the specific consistency rules of the underlying system—especially when those rules deviate from POSIX? Furthermore, if a program violates these semantics, how can we diagnose the cause of a violation, e.g., whether it is caused by the application or the underlying I/O library? Answering these questions is crucial for ensuring both the portability and correctness of HPC applications, and also help pave the road for adopting future non-POSIX systems. To the best of our knowledge, no existing study directly addresses these questions.

There are several challenges in tackling these questions. First, verifying correctness requires formal specifications of the target consistency model, as well as a rigorously designed verification algorithm. Second, different I/O systems may relax POSIX semantics in different ways, making it difficult to design a generic verification algorithm that can account for all possible variations. Third, as the I/O software stack deepens, HPC applications may involve multiple libraries and middleware, making it difficult to trace the root cause of semantics violations. Lastly, the solution must be insightful, easy-to-use, and capable of helping both application developers and I/O system designers identify and resolve consistency issues. This requires significant designing and engineering efforts.

To tackle these challenges, we propose a trace-driven four-step verification workflow that systematically collects execution traces and verifies their adherence to specific consistency models. We use the framework from [10] to specify I/O consistency models in a unified way. This aids in the design of a generic verification algorithm that can handle different consistency models. Further, we extend the framework to define the concept of a *properly-synchronized execution*, where no data conflicts occur, or all conflicts are properly synchronized according to the specified consistency model.

For any execution, our algorithm examines the traces to verify whether it is properly synchronized, i.e., whether it follows the rules of the specified consistency model. Although a properly synchronized execution does not guarantee that the entire application is synchronized correctly, because applications may follow different I/O execution paths. In our experience, this is rare though, most HPC applications tend to have a few if not one I/O path. More importantly, when an execution is found to be improperly synchronized, it indicates the presence of data races, suggesting potential consistency issues or implementation bugs in either the application or the I/O library.

We present VerifyIO, an open-source project that implements the proposed verification workflow. VerifyIO contains four components, one for each step of the workflow: (1) *a tracing tool* that collects execution traces with sufficient information for later steps, (2) *a conflict detection tool* that identifies data conflicts within the execution traces, (3) *a MPI matching tool* that matches MPI calls to establish the temporal order between all conflicting operations, and (4) *a verification tool* that checks whether identified conflicts are properly synchronized according to the target consistency model. Additionally, VerifyIO reports call-chain information for improperly synchronized conflicts, offering insights into whether the application or its I/O libraries are responsible for the issue. This information helps pinpoint the root cause of data races and can even expose underlying consistency bugs.

Overall, this paper makes the following key contributions:

- We present a trace-driven workflow for verifying I/O consistency semantics in HPC applications, implemented in the VerifyIO project. VerifyIO supports common I/O consistency models and reports detailed call-chain information to aid in diagnosing semantics violations.
- We develop a tracing library within VerifyIO that captures all necessary information for verification, supporting a complete set of APIs for three popular I/O libraries—a feature existing tracing tools do not offer.
- We conduct an extensive study using 91 test case executions from three I/O libraries, verifying each against four I/O consistency models. Our results show that six tests are not properly synchronized even under POSIX, and 28 exhibit synchronization issues across all four models. We perform an in-depth analysis of the detected data races, identifying several application- and implementation-level consistency issues that could lead to incorrect result.

II. BACKGROUND

Before we dive into the proposed workflow and implementation, we use this section to give the background information on consistency models and I/O libraries, with a special focus on the ones that we will use in our evaluation.

A. Consistency Models

A consistency model defines the contract between the programmer and the system, specifying the rules under which

shared data remains consistent. Adherence to the model ensures that the outcomes of read, write, and update operations are predictable and correct. While POSIX consistency is the dominant model in HPC, other consistency models are also used in real-world I/O libraries and file systems. As mentioned earlier, despite their differences, these models can be specified uniformly [10], which makes designing a generic verification algorithm feasible.

1) *POSIX Consistency*: The POSIX standard [3] defines a strong consistency model that requires all writes to be immediately visible to all subsequent reads. While this model is simple to maintain in single-node environments, it is expensive to implement at scale [11], [12]. Nevertheless, major parallel file systems such as Lustre [1], GPFS [2], and BeeGFS [13] continue to support POSIX consistency due to its compatibility and widespread adoption.

2) *Commit Consistency*: Commit consistency offers a relaxed model often used in user-level parallel file systems, such as BSCFS [14], UnifyFS [8], and SymphonyFS [15]. Here, synchronization is explicitly performed by issuing “commit” operations, typically by writers, to ensure data becomes globally visible. The data written prior to a commit is only visible after the commit operation completes. In practice, file systems using commit consistency may map a commit to an existing POSIX call; for example, UnifyFS uses the `fsync` call to signal a commit.

3) *Session Consistency*: Session consistency, also known as close-to-open consistency, is another relaxed model that synchronizes data between processes when one process closes a file and another subsequently opens it. This model addresses cases where global visibility (ensured by commit consistency) is unnecessary, such as when only a subset of processes perform reads. Session consistency uses `close` and `open` operations to control visibility between processes.

4) *MPI-IO Consistency*: MPI-IO [6], a part of the MPI standard [16], specifies MPI’s I/O functionalities. MPI-IO’s relaxed model ensures sequential consistency for conflicting accesses through a *sync-barrier-sync* construct. In this pattern, `MPI_File_open`, `MPI_File_close`, and `MPI_File_sync` serve as synchronization points for flushing or retrieving data, while barriers (e.g., `MPI_Barrier`, or point-to-point communications like `MPI_Send/MPI_Recv`) ensure proper ordering.

B. I/O Libraries

I/O libraries provide portability, efficiency, and scalability at different layers of the I/O stack. This work focuses on HDF5 [5], NetCDF [17], and PnetCDF [4], which are widely used for managing large, multi-dimensional datasets in HPC. These libraries are designed to provide machine-independent data formats and high-level APIs for accessing scientific data. Ensuring their correctness, especially with respect to data consistency, is critical given their extensive use in real-world applications.

HDF5 [5] is a data model and library designed for the storage and management of complex data objects. It provides

advanced data structures, such as groups and datasets, to organize and store scientific data. HDF5 supports various optimizations like hyperslab selections for non-continuous access and asynchronous I/O, allowing applications to overlap I/O with computation to improve performance.

NetCDF [17] is designed for array-oriented scientific data, with a simple data model consisting of dimensions, variables, and attributes. It allows users to define and store multi-dimensional arrays, and is commonly used in climatology, meteorology and oceanography application. The latest version of NetCDF can use either HDF5 or PnetCDF as its backend.

PnetCDF (Parallel netCDF) [4] is a parallel I/O library tailored for high-performance applications that use the NetCDF format. Built on MPI-IO, PnetCDF offers over 900 APIs for handling files, variables, and attributes, with features like collective I/O and non-blocking operations to maximize performance in parallel environments.

III. METHODOLOGY

The proposed workflow is built around a generic semantics verification algorithm designed for commonly used I/O models in HPC systems. These models are specified using a unified framework proposed in [10]. We adopt the same terminology and define when an execution is considered properly-synchronized for a given consistency model, i.e., the execution abides the rules of the consistency model. Based on this definition, we then outline the overall verification workflow.

A. A Formal Definition

We define two types of I/O operations: An I/O operation is either a *data operation* or a *synchronization operation*. Data operations are operations that read or write storage, such as `fread` or `fwrite`. Data operations include the specification of the storage location to be read or written. Synchronization operations are special I/O operations that can be used to impose order on data operations, such as `fsync`, `fopen`, or `fclose`. Synchronization operations are model-specific.

We consider the execution of a multiprocess program, in an environment that provides well-defined mechanisms to synchronize concurrent processes, such as MPI message-passing. These mechanisms define a *program order* and *synchronization order* on the executed operations of the program:

Def. 1 [Program Order (\xrightarrow{po})]: The program order of a process is a total order on the execution of the process' operations as specified by the program text. To keep the discussion simple, we ignore the extensions needed to deal with multithreaded processes.

Def. 2 [Synchronization Order (\xrightarrow{so})]: A synchronization order is a partial order specified between operations executed by distinct processes. This partial order is consistent with the program order, and $\xrightarrow{po} \cup \xrightarrow{so}$ is acyclic.

A properly-synchronized execution is defined as follows.

Def. 3 [Happens-Before Order (\xrightarrow{hb})]: The happens-before order of an execution is the transitive closure of \xrightarrow{po}

$\cup \xrightarrow{so}$. The outcome of a parallel execution should be as if all instructions were executed in the order specified by \xrightarrow{hb} . Thus, if ow and or are, respectively, a write and a read to the same location, and $ow \xrightarrow{hb} or$, then or will return the value written by ow , unless there is another store ow' to the same location such that $ow \xrightarrow{hb} ow' \xrightarrow{hb} or$.

Def. 4 [Conflict]: Two data operations *conflict* iff (if and only if) their access ranges overlap, and at least one of them is a write.

Def. 5 [Minimum Synchronization Construct (MSC)]: An MSC specifies a minimum sequence of synchronization operations required to synchronize two conflicting data operations. An MSC consists of k synchronization operations and $k + 1$ edges, where $k \geq 0$:

$$MSC = \xrightarrow{r_0} S_1 \xrightarrow{r_1} S_2 \xrightarrow{r_2} \dots \xrightarrow{r_{k-1}} S_k \xrightarrow{r_k}$$

For each i , $1 \leq i \leq k$ and $S_i \in S$, where S is the set of synchronization operations to be defined by the specific consistency model. For each j , $0 \leq j \leq k$ and $\xrightarrow{r_j} \in \{\xrightarrow{po}, \xrightarrow{hb}\}$.

Def. 6 [Properly-Synchronized Relation (\xrightarrow{ps})]: Two conflicting data operations X and Y are properly synchronized, i.e., $X \xrightarrow{ps} Y$, iff one of the following holds:

- 1) X is a read operation and $X \xrightarrow{hb} Y$.
- 2) X is a write operation, and there exists an MSC between X and Y in the happens-before order.

Def. 7 [Data Race]: Two data operations X and Y in an execution form a *data race* iff they conflict and they are not properly synchronized.

Def. 8 [Properly-Synchronized Execution]: An execution is properly synchronized iff for every sequentially consistent execution of the program, all I/O operations can be distinguished by the system as either data or synchronization, and there are no data races in the execution.

B. Workflow and Challenges

The proposed workflow aims to verify whether a given execution is properly synchronized under a specific consistency model. It is structured into four steps, as illustrated in Fig. 1. Below, we outline each step and discuss its associated challenges.

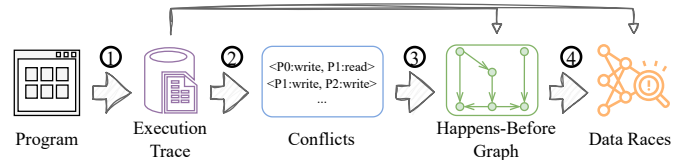


Fig. 1: Verification workflow

1) *Generating Execution Trace*: The first step involves running the target program and collecting an execution trace. This trace is used for later verification steps. The main challenge here is ensuring that the tracing tool collects all the necessary

function calls and their arguments, including the complete call chain for any potential violations. The trace needs to cover multiple I/O layers (e.g., NetCDF, PnetCDF, MPI-IO, and POSIX) to capture all relevant function calls. Further, the desired tracing tool must also handle additional complexities, such as maintaining mappings between file descriptors and file names, as well as distinguishing between nested open/close calls on the same file. While many tracing tools exist, none fully meet our requirements.

2) *Detecting Conflicts*: Once the execution trace is generated, the next step is to detect conflicts (Def. 4) between I/O operations. Conflicts occur when two or more operations access the same file location, and at least one is a write. Challenges arise when retrieving access details for operations that do not explicitly include file locations in their arguments (e.g., `fwrite`). In such cases, previous metadata calls like `lseek` or `fseek` must be examined to recover the location information. Additionally, the detection algorithm must handle corner cases, such as I/O operations using different file handles for the same file. The output of this step is a list of *conflict groups*. Each group contains a conflicting operation and a mapping of ranks (processes) to other conflicting operations. We denote a conflict group as (X, ζ) , where X is a data operation, ζ is the mapping, then for each $Y \in \zeta$, X and Y form a *conflict pair*.

3) *Establishing Happens-Before Order*: For each conflict, the happens-before order (Def. 3) must be established to determine whether the conflicting operations are properly synchronized. This order is derived from the program order and the synchronization order defined by the MPI calls. Thus, our tracing library also needs to intercept and store MPI calls. This step matches the captured MPI calls (essentially replaying them) to establish the synchronization order. To do so, we need to collect many extra information. For example, to match `MPI_Send` to a `MPI_Irecv`, we need to intercept the `MPI_Wait` or `MPI_Test` on the receive side. Additionally, we need to keep track of the request id of each MPI request.

4) *Verifying Consistency Semantics*: In the final step, the verification algorithm examines these conflicts to determine whether they are properly synchronized (Def. 6) according to the target consistency model. One challenge in this step is the potentially large number of conflict pairs to verify. In some cases, even small programs can produce millions of conflicts (e.g., *pmulti_dset* in Section V), making it essential to optimize the verification process for efficiency. In particular, pruning strategies are necessary to reduce the number of conflicts to examine. Finally, when a data race (Def. 7) is detected, the algorithm should report the complete call chain involved, helping users pinpoint the root cause.

IV. VERIFYIO

VerifyIO is an open-source project¹ that implements the entire workflow and addresses the challenges outlined in Section III-B. Its primary goal is to provide the I/O community

with a tool for verifying and ensuring the correctness of I/O semantics, especially as we move away from POSIX. We aim to make VerifyIO as user-friendly, generic, and flexible as possible. Specifically, the tracing library (step one) is designed for easy extension to support additional I/O libraries, while the verification algorithm (step four) is capable of working with various consistency models.

This section follows the four steps of the proposed workflow described earlier, with each subsection detailing the implementation of one workflow step. An illustrative example is provided in Fig. 2.

A. Generating Execution Trace

The tracing tool is a crucial component of VerifyIO, as it collects the data necessary for subsequent steps. In particular, it captures I/O calls for conflict detection, MPI calls for establishing synchronization order, and both I/O and MPI calls for the final semantics verification and call-chain reporting.

While many tracing tools exist, none meet all the requirements of VerifyIO as discussed in Section III-B. Among the tools we studied, Recorder [18] came closest to fulfilling our needs. Recorder intercepts POSIX, MPI, MPI-IO, and HDF5 functions, capturing all arguments of each intercepted call. However, it only supports a single I/O library (HDF5) and provides incomplete coverage—supporting 84 HDF5 functions, while the latest HDF5 version includes over 700 APIs.

To address these gaps, we extended Recorder, creating a new version called Recorder⁺, which can be easily adapted to support additional functions and libraries. Recorder⁺ intercepts I/O calls through `LD_PRELOAD` and redirects them to the corresponding wrapper functions. The wrapper function records pre-invocation information (e.g., entry timestamp and thread id), invokes the original function, stores all runtime arguments, then returns to the original caller. In contrast to the original Recorder, where these wrappers were manually written (making it difficult and error-prone to extend), Recorder⁺ uses a redesigned and automated approach, as shown below.

```

wrapper(func, ret_type, n_args, args) {
    prologue();
    ret_type ret = func(args);
    epilogue(n_args, args);
    return ret;
}

```

The *prologue* and *epilogue* handle the storage of pre- and post-invocation arguments and are implemented using macros to support arbitrary argument types. The wrapper function is also implemented as a macro, removing dependencies on function signatures. In companion, we developed a code-generation tool that takes a *function signature file* (think it as a header file) as input and automatically generates wrapper functions for each function in the file. The generated code can be easily compiled as a plugin to extend Recorder⁺ to support new functions. In this work, Recorder⁺ covers the complete function sets of three I/O libraries, as shown in Table I.

The remaining components of Recorder, such as compression and caching, are remain unchanged. Recorder typically

¹The link is hidden for double-blind review purposes.

Program

```

MPI_Comm comm = MPI_COMM_WORLD;
MPI_Info info = MPI_INFO_NULL;
MPI_File fh;
MPI_Status status;
MPI_File_open(comm, "./test",
MPI_MODE_RDWR, info, &fh);
if (rank == 0) {
    int data = 7;
    MPI_File_write_at(fh, 0, &data, 1,
MPI_INT, &status);
    MPI_File_sync(fh);
    MPI_Barrier(comm);
}
if (rank == 1) {
    int data;
    MPI_File_sync(fh);
    MPI_Barrier(comm);
    MPI_File_read_at(fh, 0, &data, 1,
MPI_INT, &status);
}
MPI_File_close(&fh);
  
```

Step 1: Generating Execution Trace

Trace File - Rank 0

```

MPI_File_open(MPI_COMM_WORLD, ...);
int fd = open("./test", O_RDWR);
MPI_File_write_at(fh, 0, &data, ...);
pwrite(fd, buf, 4, 0);
MPI_File_sync(fh);
fsync(fd);
MPI_Barrier(MPI_COMM_WORLD);
MPI_File_close(&fh);
close(fd);
  
```

Trace File - Rank 1

```

MPI_File_open(MPI_COMM_WORLD, ...);
int fd = open("./test", O_RDWR);
MPI_File_sync(fh);
fsync(fd);
MPI_Barrier(MPI_COMM_WORLD);
MPI_File_read_at(fh, 0, &data, ...);
pread(fd, buf, 4, 0);
MPI_File_close(&fh);
close(fd);
  
```

Step 2: Detecting Conflicts

Conflicts

```

<Rank 0: pwrite("./test", [0-3],
Rank 1: pread("./test", [0-3])>
  
```

Step 3: Establishing Happens-before Order

The diagram shows the execution flow for Rank 0 and Rank 1. Rank 0's operations are: MPI_File_open, open, MPI_File_write_at, pwrite, MPI_File_sync, fsync, MPI_Barrier, MPI_File_close, close. Rank 1's operations are: MPI_File_open, open, MPI_File_sync, fsync, MPI_Barrier, MPI_File_read_at, pread, MPI_File_close, close. A red dashed arrow indicates the happens-before relationship from Rank 0's MPI_Barrier to Rank 1's MPI_Barrier.

Step 4: Verifying Consistency Semantics

| Consistency Semantics | Properly Synchronized |
|-----------------------|-----------------------|
| POSIX | ✓ |
| Commit | ✓ |
| Session | X |
| MPI-IO | X |

Explanation:
The execution is properly synchronized under POSIX because there exists a path between the two conflicting operations, suggesting write happens-before read, which is sufficient for POSIX consistency. Additionally, this path contains a commit (fsync) operation, which satisfies the Commit semantics requirement. However, there is no close-to-open pair and sync-barrier-sync construct in this path, thus it is not properly synchronized under Session consistency and MPI-IO consistency.


```

// not conflict with I
break;
if (I.type == WRITE || J.type == WRITE)
    conflicts[i, j] = TRUE;
}

```

C. Establishing Happens-before Order

In this step, we match all MPI calls recorded in the execution trace to establish synchronization orders between I/O operations. MPI communications are categorized into two types: point-to-point and collective. Point-to-point calls involve two processes, while collective calls involve multiple processes within the same communicator. The synchronization imposed by these MPI calls form the happens-before order among other I/O operations. For instance, in Fig. 2, the MPI_Barrier calls impose an order between the pwrite operation from rank 0 and the pread operation from rank 1.

Point-to-point calls are matched by comparing the destination, source, and tag arguments. For calls with wildcard arguments (i.e., MPI_ANY_TAG and MPI_ANY_SRC), we recover and compare the actual tag and source information from the MPI_Status argument returned by the receiver.

Collective calls are matched based on their communicator and in program order, e.g., two MPI_Barrier calls on the same communicator will never be matched out of order. One complexity arises with user-created communicators (e.g., not the builtin ones like MPI_COMM_WORLD). To handle this, we track communicator creation and duplication operations at tracing time and assign each communicator a globally unique identifier. When matching collective calls, we simply compare the communicator identifier passed by each process.

Handling non-blocking calls, such as MPI_Isend and MPI_Iallreduce, is especially tricky. These calls must be matched with the corresponding MPI_Wait* or MPI_Test* calls. It can be particularly difficult when they are paired with MPI_Testsome calls, since the associated MPI_Request may not yet be complete. We must first determine which requests have completed, then retrieve and compare them against the request generated by the original non-blocking call. While we cannot cover all corner cases due to space constraints, our implementation handles the most common scenarios, and we manually confirmed that it correctly matches all MPI calls used in our evaluations.

Once all MPI calls are matched, we construct a *happens-before graph*, denoted as $G = (V, E)$. Here, V represents the vertices of G , each corresponding to either a conflicting I/O operation or a synchronization event. The edges, E , capture the happens-before relationships between operations, derived from program order and synchronization order. For two vertices v_1 and v_2 in G , $v_1 \xrightarrow{hb} v_2$ if there is a path from v_1 to v_2 .

D. Verifying Consistency Semantics

In the final step, VerifyIO examines the detected conflicts and determines whether they are properly synchronized. If a data race is found, the algorithm reports the entire call-chain to help identify the root cause.

A naive implementation would exhaustively check each conflicting pair, which can be expensive in the case of large numbers of conflicts. To optimize this, VerifyIO applies runtime pruning techniques to reduce unnecessary checks. As described earlier, we use *conflict groups* to organize conflicts based on process ranks and program order. The algorithm iterates over each conflict group, with four runtime pruning opportunities, as illustrated in Fig. 3. Here, X represents a data operation invoked by process P_a , and Y_1, Y_2, \dots, Y_n represent conflicting data operations (sorted by program order) invoked by process P_b . The four pruning scenarios are:

- 1) If $X \xrightarrow{ps} Y_1$, then $X \xrightarrow{ps} Y_i$ for all $i \in [2, n]$.
- 2) Conversely, if $Y_n \xrightarrow{ps} X$, then $Y_i \xrightarrow{ps} X$ for all $i \in [1, n-1]$.
- 3) If X does not $\xrightarrow{ps} Y_n$, then it does not $\xrightarrow{ps} Y_i$ for any earlier Y_i , because if it did, it would also have to $\xrightarrow{ps} Y_n$.
- 4) Similarly, if Y_n does not $\xrightarrow{ps} X$, then none of the earlier Y_i will $\xrightarrow{ps} X$.

Each pruning opportunity reduces the number of checks from n to 1. In practice, most conflict groups fall into one of these scenarios, meaning verification needs to be performed only once per conflict group. If a conflict group does not fit these scenarios, we fall back to checking every conflict pair within the group.

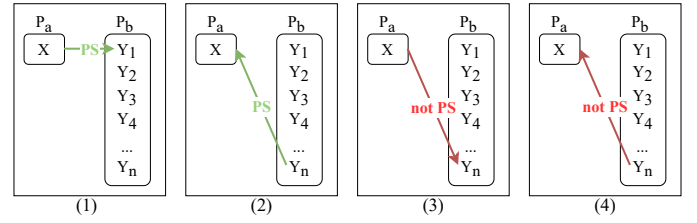


Fig. 3: Four runtime pruning scenarios, each reducing the number of check from n to 1.

As defined in Def. 6, the pair-wise verification of proper synchronization involves checking the existence of a happens-before order and verifying the *MSC* (Def. 5) between two I/O operations. *MSC* is determined by examining trace records, while the happens-before order can be determined using one of four approaches:

1) *Vector Clock*: Vector clock tracks event order across distributed processes and helps determine the partial order between two events. In our case, each event is a vertex in the happens-before graph. Since we already constructed the graph, computing vector clocks is straightforward. We perform a topological sort on the graph and propagate vector clocks through nodes in that order. This takes $\mathcal{O}(V + E)$ time, and once the clocks are computed, determining the happens-before order between any two operations takes $\mathcal{O}(1)$ time.

2) *Graph Reachability*: Graph reachability queries whether there exists a path from vertex v_1 to vertex v_2 in a directed acyclic graph. To determine if $X \xrightarrow{hb} Y$, we treat X and Y as vertices in the happens-before graph and use reachability algorithms. VerifyIO employs the reachability method from

the NetworkX [19] package, which operates with a time complexity of $\mathcal{O}(V + E)$ per query.

3) *Transitive Closure*: Transitive closure captures all pairs of vertices (v_1, v_2) in a directed acyclic graph such that a path exists from v_1 to v_2 . Computing the transitive closure takes $\mathcal{O}(V^3)$ in the worst case, but once done, it allows pair-wise verification in $\mathcal{O}(1)$ time.

4) *On-the-fly*: This algorithm determines the happens-before order at the time of verification, without the need to pre-build the happens-before graph. It matches MPI communications during each verification step to establish the order, checking for matching MPI events occurred in between the two conflicting I/O operations. In the worst case, when no match is found, the algorithm must go through all MPI calls of the involved processes.

After analyzing the complexity of each algorithm, it's clear that transitive closure and graph reachability are generally slower than vector clocks. Depending on the number of conflicts and the size of the graph, the on-the-fly algorithm may prove beneficial as it does not require pre-building the happens-before graph. Nevertheless, performance evaluation is not the primary focus of this work, and we leave it for future work. For validation purposes, we implemented all four approaches, using at least two in our experiments to ensure consistent results.

V. EVALUATION

We selected 91 built-in tests from three widely-used I/O libraries as our target applications. These tests were chosen because they are self-contained and typically focus only on I/O operations. Additionally, these tests are usually created by the library developers to validate implementation correctness and system compatibility. As a result, they are more likely to adhere strictly to system rules and are less prone to library-usage errors.

We used the latest stable versions of the libraries available at the time of writing: HDF5 1.14.4.3, PnetCDF 1.13.0, and NetCDF 4.9.2. Our test cases include: 15 from HDF5, 17 from NetCDF, and 59 from PnetCDF. We verified each test against four I/O consistency models: POSIX, Commit, Session, and MPI-IO. All the tests were parallel in nature, and we executed them using the test scripts provided by the respective libraries. Initially, we ran the tests without VerifyIO to ensure they passed on our system, before rerunning them with VerifyIO to collect execution traces and verify their consistency semantics.

All experiments were conducted on X (name anonymized for review), a system with 795 nodes, each equipped with an IBM Power9 CPU and 256 GB of memory. The I/O libraries were compiled using GCC and Spectrum MPI. The verification process was performed on a single node, with most runs completing within minutes, while a few larger tests took under an hour. To promote reproducibility and further research, we have made the traces and execution scripts available to the community (link hidden for double-blind review).

A. Summary

Fig. 4 illustrates the number of data races detected for each test across the four consistency models. Each row represents a test execution, and the columns display the number of data races detected for the POSIX, Commit, Session, and MPI-IO models. HDF5 tests are the largest among the three libraries, with many containing thousands of lines of code. In our experiments, over 800 million conflicts were detected across 7 of the 15 HDF5 test executions. In some tests, such as *shapesame* and *testphdf5*, hundreds of thousands of data races were reported under the weaker consistency models. In contrast, NetCDF tests are fewer in number and generally smaller in size. VerifyIO identified 53,793 conflicts across 9 of the 17 test executions, with over 9,000 data races detected under the relaxed models. For PnetCDF, VerifyIO identified a total of 107,185 conflicts across 12 out of 59 test executions, with 35,048 data races found under the relaxed consistency models. Additionally, three tests (marked in gray) were unable to complete the verification process due to unmatched MPI calls, which we will further discuss in Section V-D.

Table II summarizes the number of test executions that were not properly synchronized. Interestingly, we found that 6 out of 91 tests from all three libraries were not properly synchronized under POSIX semantics. This was unexpected, as data races under POSIX suggest that some read or write operations return undefined results. Notably, these 6 tests do not validate data integrity after each I/O operation, since they all pass on GPFS, a POSIX-compliant system. Section V-B will investigate the underlying causes.

| Semantics | HDF5 (15) | NetCDF (17) | PnetCDF (59) | Total (91) |
|-----------|-----------|-------------|--------------|------------|
| POSIX | 3 | 1 | 2 | 6 |
| Commit | 7 | 9 | 12 | 28 |
| Session | 7 | 9 | 12 | 28 |
| MPI-IO | 7 | 9 | 12 | 28 |

TABLE II: Test executions that are not properly synchronized.

Another unexpected finding relates to the behavior of the libraries under MPI-IO semantics. Since all three libraries use MPI-IO under the hood, we expected no additional data races when moving from POSIX to MPI-IO. However, the results in Fig. 4 and Table II show more data races detected under MPI-IO semantics. This suggests that although these libraries are developed using MPI-IO, they still assume a POSIX file system as the backend, potentially skipping some MPI synchronizations and relying solely on POSIX guarantees. This assumption is risky and can lead to significant correctness issues, which we will explore in Section V-C.

Finally, as we can see from Table II, all three libraries exhibited more synchronization issues under weaker consistency models. Interestingly, VerifyIO reported identical results for the Commit, Session, and MPI-IO models. This suggests that while Session and MPI-IO provide weaker guarantees than Commit, they do not break more applications. This insight is valuable when selecting a weaker consistency model, as it suggests that adopting the weakest model can improve performance without sacrificing application correctness. VerifyIO

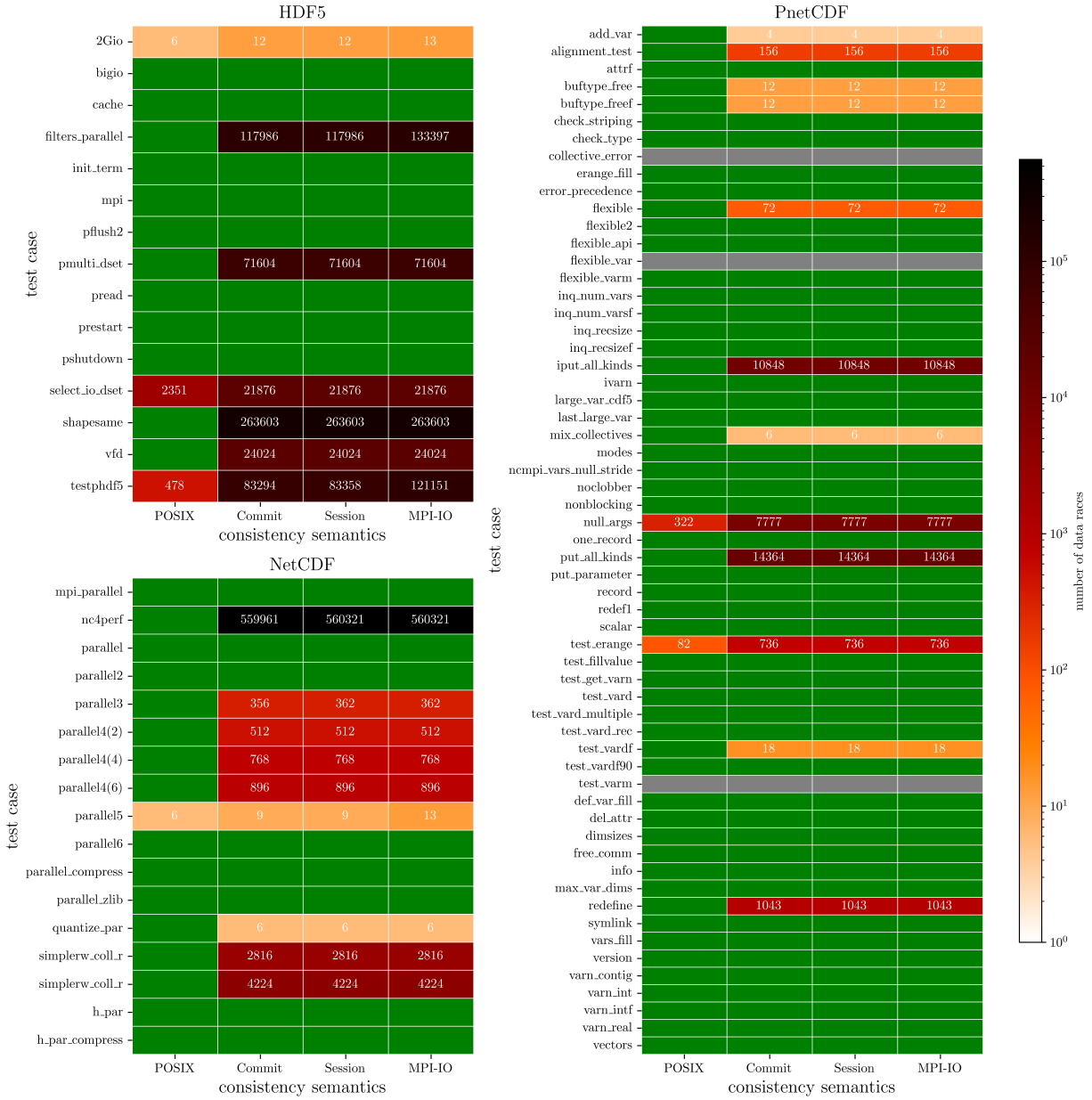


Fig. 4: Number of data races detected. Each row represents a test execution, and each column shows the number of data races under a specific consistency model. Green boxes indicate properly synchronized executions (i.e., zero data races). Gray rows indicate executions where VerifyIO detected unmatched MPI calls, due to potential library implementation issues.

proves useful in this context, helping to determine the extent to which consistency models can be relaxed while maintaining correctness.

B. POSIX Data Races

Data races are almost always problematic, especially in HPC applications, which are typically deterministic and assume the use of a POSIX-compliant file system. Consequently, such applications should not exhibit data races when running on POSIX systems. Due to space constraints, we focus here on the NetCDF and PnetCDF tests that exhibited POSIX data races. We discuss not only the causes but also whether the

responsibility to fix these issues lies with the users or the library developers.

1) *NetCDF*: The test *parallel5* from NetCDF is the only one that exhibited POSIX data races. These races are largely triggered by the high-level function `nc_put_var_schar`, which writes an entire variable in a single operation. Analyzing the call chain reveals that NetCDF implements this function using a sequence of HDF5 APIs. Specifically, `H5Dwrite` calls `MPI_File_write_at`, which writes the same offset and number of bytes to the same file from different processes, leading to a data race. A review of the test source code shows that it creates a variable with the `NC_BYTE` type and

then writes to it concurrently from multiple processes. This is an incorrect use of `nc_put_var_schar` and should be addressed at the application level, by correcting the test source code itself.

2) *PnetCDF*: The tests `null_args` and `test_erange` both exhibited POSIX data races due to similar issues. These races occur when multiple processes concurrently invoke `pwrite` to write to the same location in the same file. Tracing the call chain reveals that `pwrite` is called by `MPI_File_write_at_all`, which, in turn, is invoked by `ncmpi_put_var1_text_all` in `null_args.c` and `ncmpi_put_var_uchar_all` in `test_erange.c`. Upon reviewing the source code, we determined that the root cause is that multiple processes attempt to write to the *same variable* without proper synchronization. As with the NetCDF issue, this is not the intended use of these PnetCDF functions. They are designed to be collective calls for writing to *distinct variables*. Therefore, the data races in these cases are also caused by improper usage at the application level and should be corrected by the user.

C. MPI-IO Semantics Violations

All three libraries in our study use MPI-IO internally, so they are expected to adhere to MPI-IO semantics. However, as briefly mentioned earlier, this is not always the case. In this subsection, we provide a deeper examination of how PnetCDF and HDF5 violate MPI-IO semantics.

1) *PnetCDF*: To illustrate the MPI-IO violation in PnetCDF, we analyze the test case *flexible*. Fig. 5 shows a code snippet from *flexible.c* (with some details omitted for readability). The code defines a two-dimensional array variable, initializes it to NULL using `ncmpi_set_fill`, and later populates it with actual values through `ncmpi_put_vara_all`. By examining the call chain, we identified that `MPI_File_write_at_all` is invoked twice internally: first by `ncmpi_enddef` and then by `ncmpi_put_vara_all`. In the first MPI write, each rank writes NULLs to distinct areas of the file. However, before the second write, PnetCDF internally modifies the MPI file view, which triggers MPI-IO's aggregation optimization. As a result, rank 0 performs the entire second write, causing data races between rank 0 and the other ranks.

For typical users, it is difficult to discern which PnetCDF functions are responsible for the actual write or whether they access overlapping file regions. Therefore, expecting users to handle this with appropriate synchronizations is impractical. The issue should instead be resolved at the library level, where there is a complete understanding of when and where actual I/O occurs and how file access patterns change.

During a review of recent code changes in the PnetCDF tests, we discovered that a workaround had been introduced: `ncmpi_sync/MPI_Barrier/ncmpi_sync` calls were added between potentially conflicting PnetCDF operations. However, these safeguards are only applied on non-POSIX systems and are not enabled by default. This supports our analysis and indicates that the library developers are

aware of the issue. Nonetheless, addressing the problem at the application level does not fully resolve the underlying MPI-IO semantics violations within the library's implementation.

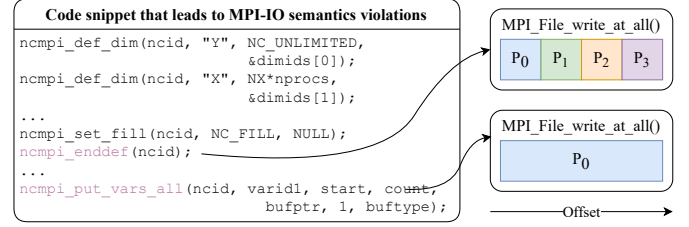


Fig. 5: Code snippet from *flexible.c*. In this execution, `MPI_File_write_at_all` is invoked twice, causing a conflict that is not properly synchronized according to MPI-IO semantics.

2) *HDF5*: We identified a recurring pattern in HDF5 tests (e.g., *shapesame*) that leads to MPI-IO data races. This pattern involves three core functions, shown on the left side of Fig. 6. Specifically, the functions `H5Dwrite` and `H5Dread` access the same dataset, with only an MPI barrier between them. Variations of this pattern exist in other codes, for instance, with `H5Dwrite` and `H5Dread` replaced by other calls, such as `H5Awrite` and `H5Aread`. However, regardless of the specific functions, accessing the same dataset (or attribute) with this pattern consistently leads to a synchronization issue.

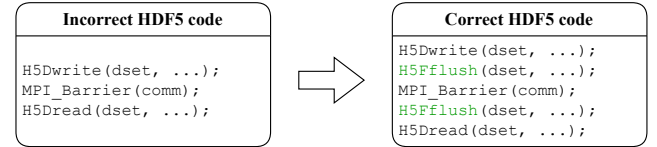


Fig. 6: Improperly vs. properly synchronized HDF5 code

The problem arises because the data returned by `H5Dread` is undefined according to the MPI standard, due to the lack of proper synchronization with the preceding `H5Dwrite`. While the MPI barrier enforces a temporal order, this is only sufficient on systems that provide POSIX consistency. According to MPI-IO semantics, two additional synchronization operations are required, as shown on the right side of Fig. 6. These two `H5Fflush` calls will invoke `MPI_File_sync` to ensure consistency.

However, these flush calls introduce significant overhead, as they force cached data to be written to disk. Since this is unnecessary on POSIX systems, the HDF5 developers (as confirmed in discussions with them) intentionally omit these calls to optimize performance. The assumption is that users will mostly run their code on POSIX systems, where this omission has no impact. Nevertheless, this incorrect synchronization pattern can lead to silent data corruptions when executed on non-POSIX systems, making the issue extremely difficult to detect and resolve.

3) *Discussion*: At first glance, the MPI-IO violations appear to stem from insufficient MPI synchronizations. However, a deeper examination reveals that this is sometimes an intentional trade-off for performance. In essence, correctness

on non-POSIX systems is sacrificed to gain performance on POSIX systems. This reluctance to add MPI synchronizations, in particular `MPI_File_sync`, is primarily due to the high cost of this function. `MPI_File_sync` is designed to ensure both persistency and consistency, though many high-level I/O libraries require only consistency, not persistency. To address this, we have begun an initiative to revise the MPI standard to include a new MPI-IO API that guarantees consistency without forcing a flush to disk. Once this new API is available, high-level libraries will be able to achieve both consistency and improved performance.

D. Potential Implementation Bugs

An additional benefit of VerifyIO is its ability to detect unmatched MPI calls. During its second phase, VerifyIO matches all stored MPI operations, flagging any mismatches or unmatched calls. For example, a collective MPI function must be invoked by all processes within the same communicator; otherwise, VerifyIO will report an error.

In our analysis, we identified three PnetCDF test executions (represented by gray rows in Figure 4) with unmatched MPI calls. The `collective_error` test, as the name suggests, is designed to trigger such errors, and VerifyIO successfully detected this intentional behavior. After manually inspecting the other two tests, we found the mismatch was caused by the `ncmpi_wait` call. Specifically, during execution, this function splits into two paths: rank 0 calls `MPI_File_write_at_all`, while the other ranks call `MPI_File_write_all`, which appears to be an implementation bug.

VI. RELATED WORK

In the context of shared-memory systems, consistency models [20], [21], [22], [23], along with the verification of semantics and program correctness [24], [25], [26], [27], have been extensively studied. However, similar studies in the realm of HPC I/O systems remain sparse. This gap exists despite the increasing complexity and importance of storage consistency in HPC environments.

One reason for this disparity is that HPC I/O stacks lack the compiler layer present in shared-memory systems, which helps programs achieving portability regardless of the underlying consistency models provided by the hardware. As a result, many applications rely on the POSIX [3] interface to ensure portability and compatibility across systems. Given POSIX’s dominance in HPC environments, much of the existing work on semantics verification [28], [29], [30] has focused on ensuring program correctness under POSIX semantics.

On the other hand, POSIX enforces strict consistency by ensuring that operations are serialized and immediately visible, but this comes at the cost of performance, especially in distributed environments [11], [31]. To overcome this, modern I/O systems, including parallel file systems [7], [8], [9], [32], [33] and I/O libraries [5], [6], [34], are increasingly adopting weaker consistency models. These models, such as

commit consistency, session consistency, and MPI-IO consistency, relax the strict guarantees of POSIX in favor of higher performance and scalability. However, they increase the risk of data races or incorrect behavior in applications that assume POSIX consistency.

Consequently, recent efforts have attempted to address these risks by expanding the scope of consistency checks. AtomFS [35] introduced a formal framework for building verified concurrent file systems, while other work [36] developed a formal model for the Google File System, describing its read/write behaviors and encoding the model in a way that allows automatic verification. Despite these advances, such efforts often focus on a single file system or a specific consistency model, limiting their generality and applicability to other I/O systems and libraries. In parallel, a few projects have explored verification of parallel I/O codes against multiple models. For example, in [37], the authors studied the requirements of consistency semantics of HPC applications and proposed a method for detecting data races for three I/O consistency models. However, the proposed method is based on examining the timestamp of each I/O operation, which is only a proximity of the happens-before order and may result in incorrect results. A follow-up work [38] addresses this shortcoming by examining synchronization events when detecting data races. However, the method remained limited to specific file systems and consistency models, and it only reported the existence of data races without providing detailed information to help developers resolve the issues.

In contrast, VerifyIO addresses these limitations by providing a generic verification framework that supports widely used HPC I/O models. VerifyIO detects data races, identifies mismatched MPI calls, and additionally reports the full call chain associated with detected violations. These features help answer the key questions posed in Section I, offering a more comprehensive solution than existing tools for detecting and diagnosing I/O consistency issues in HPC environments.

VII. CONCLUSION

In this work, we present a trace-driven verification workflow, implemented in VerifyIO, for determining I/O consistency issues in parallel programs. VerifyIO’s tracing tool supports complete API sets of three widely-used I/O libraries, and is easy to extend to support more. The verification tool is capable of handling various consistency models for any given execution trace. Through an extensive study of 91 tests across these I/O libraries, VerifyIO successfully identified consistency issues, providing valuable insights into both application and library-level issues. Finally, all tools in VerifyIO and all collected data and results are made publicly accessible for further study.

To enhance the usefulness of VerifyIO, one area of future work is improving the reported call chain. In many applications, the same function may be called multiple times from different locations, requiring manual inspection during data race analysis. To reduce this manual effort, we plan to explore

the integration of a backtrace feature to complement the call chain.

Another planned feature is dynamic selection of the verification algorithm. Since VerifyIO includes four different algorithms, it could dynamically choose the most efficient one based on factors such as the number of conflicts, the size of the happens-before graph, and other relevant metrics.

ACKNOWLEDGMENT

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