Performance Portability through Abstraction



Data Structures

Parallel Execution

Memory Spaces ("Where")

- Multiple-Levels

- Logical Space (think UVM vs explicit)

Execution Spaces ("Where")

- N-Level

- Support Heterogeneous Execution

- Memory Layouts ("How")
- Architecture dependent index-maps
- Also needed for subviews

Memory Traits

- Access Intent: Stream, Random, ...
- Access Behavior: Atomic
- Enables special load paths: i.e. texture

- parallel_for/reduce/scan, task spawn
- Enable nesting

Execution Policies

- Range, Team, Task-Dag
- Dynamic / Static Scheduling

Execution Patterns ("How")

Support non-persistent scratch-pads