**Source codes:** ...\PNC8.srcs\sources\_1\imports\source\_PNC4

**Top Module:** TOP\_PN.vhd

 $\textbf{Libraries:} \ \texttt{C28SOI\_SC\_8\_COREPB4\_LL} \ (1.00 \text{V}, \, \text{m40}), \, ...$ 

No wire load

Libraries	Clock Period (ns)	Slack	Comb. Area	Buf/Inv Area	Non-Comb. Area	Total Area
	4	2.57	72006	2580	6118	78124
C28SOI_SC_8_COREPB4_LL	2	0.55	71946	2520	6118	78064
	1.6	0.17	72020	2598	6118	78138
	1.4	0.00	72003	2584	6118	78121
	2	0.79	144547	5908	6512	151059
C28SOI_SC_12_COREPB4_LL	1.4	0.19	144796	6123	6512	151309