

a) Ideal Diode Model

I= 136ppA, V=OV is q-point our initial b) Constant Voltage Drop model

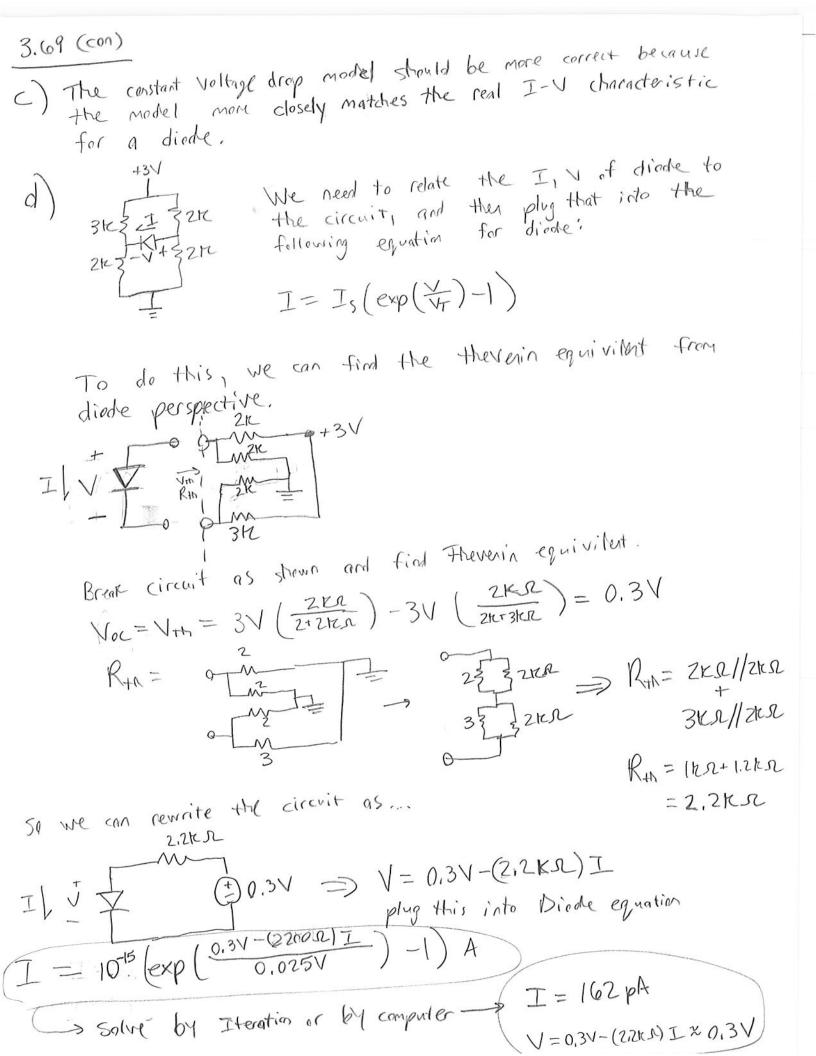
Assume Dirole off.

$$3V$$
 $J = 3V \cdot \frac{1}{2}$
 $2K = 3/(\frac{1}{2})$
 $= 3(\frac{1}{10})$

Assume Dirole oft.

$$3V$$
 $V = 3V \cdot \frac{2kR}{2k+2kR} - 3V \cdot \frac{2kR}{3kR+2kR}$
 $= 3I(\frac{1}{2} - \frac{2}{5})V$
 $= 3(\frac{1}{10})V$
 $= 0.3$

Because V=0.31 < Von=0.60 , our assumption that the



3.72

· Ideal Diade Model

- B) Diode is forward Biased, so assume to sheet circuit.

 Therefore V=-5V. I= +5V-(-5V) = 625 MA
 - b) Diode is forward Biased, So assume short circuit.

 Therefore V= +3V. I = +3V-(-7V) = COZ5UA
 - c) Diode reverse biased, assume state open circuit.
 Therefore V=+7V, I=OA
 - d) Diade reverse biased, assume open circuit. => V=-5V , I=OA

Von= 0.7 V Mode)

a) Assume Diable off. Opposition then there is 10V across the diable which is a greater than Von. This is inconsistent w/ assumption that diade is off, so that must be false. Therefor diade must be on.

V=-5V+ Von=-4.3V, I= 45V-(-4.3V)= 581MA

.b) similar logic to a) indicates dinde is again on.

V= +3V-Von= 2.3V, I= 2000 +2.3V-(-7V) - 58/UA

c) Assume Diode off. Then there is -10V across the diode, which is less than Von= +0.7V. This is consistent with our assumption that the Diode is off, so the diade is indeed off.

V=+7V , I=QA

Some logic as in e) allows us to conclude that the diale is off. Herefore

V= -5V, I=OA

3.74

Ideal Diode Model

$$I_1 = 000 - I_2 + 00 \left(\frac{9V}{32kR}\right) = -140\mu A + \frac{9V}{22kR} = 270\mu A$$

 $V_1 = 0V$

b)
$$D_2$$
 off, D_1 on
$$I_1 = \frac{GV}{43KS2} - \frac{140\mu A}{V_1 = 0V}$$

$$I_2 = 0A + V_2 = -9V$$

Assume: D, off, Dzen- (we see later this assumption is wrong)

$$J_2 = \frac{15V}{22 + 43KJR} = 230\mu A$$
 $V_2 = 0V$

This is inconsistent w/ ow original assumption, so an Di is on, Not off.

Assure: Dion, Dzon.

$$I_2 = \frac{1}{22\kappa^2} = \frac{409\mu A}{1200} V_2 = 0V$$

These are consistent

 $I_1 = I_2 - \frac{GV}{43\kappa^2} = 2.70\mu A$, $V_2 = 0V$

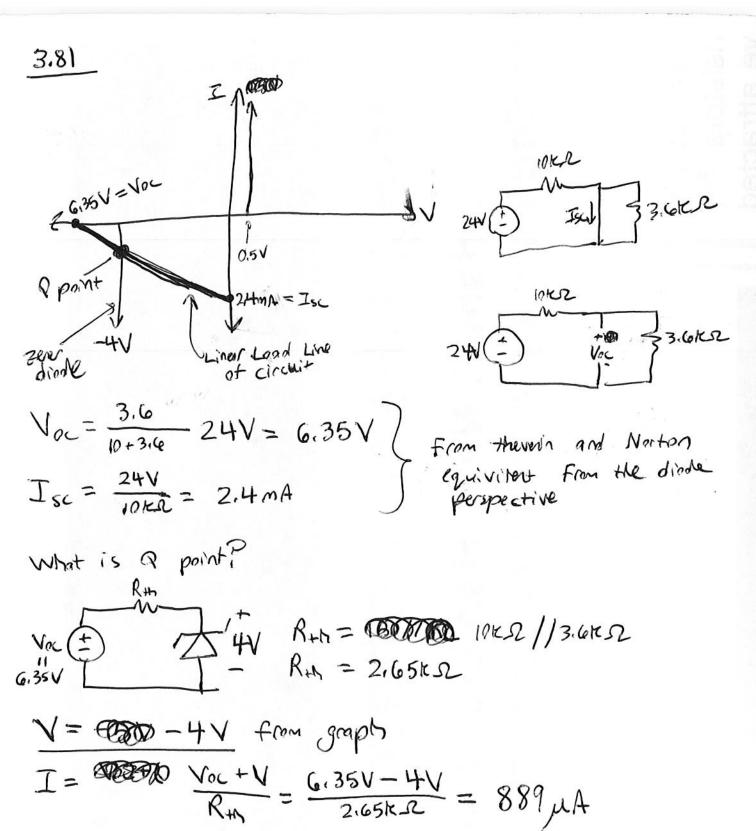
Both Diodes on, so

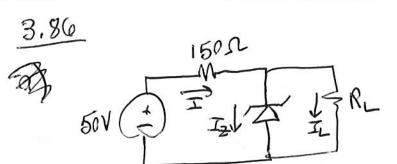
Both Diode's on, so these should be correct.

$$I_1 = 0A$$
, $V_2 = (-9V + (\frac{22}{43+22})(15V)) = 000 - 3.92V$

$$I_1 = \frac{6V - V_{00}}{43k\Omega} = \frac{6V - 0.65V}{43k\Omega} = 124\mu A$$
 $V_1 = V_{00} = 0.65V$

$$I_2 = \frac{-Von - Von - (-9V)}{22kR} = \frac{-0.65 - 0.65 + 9V}{22kR} = 350\mu A \quad V_2 = Von = 0.65V$$





$$I = I_2 + I_L \Rightarrow 0 \qquad I_2 = I - I_L = I - \frac{V_2}{R_L} = \frac{50V - V_2}{150 \Omega} - \frac{V_3}{R_L} = I_2$$

$$I_L = \frac{V_2}{R_L}$$

$$I = \frac{50V - 00 V_2}{150 \Omega}$$

a) for
$$R_{L}=100 \Omega$$
 $I_{Z}=\frac{50V-15V}{150}-\frac{15V}{100\Omega}=83.3mA$
Power = $V_{Z}I_{Z}=(15V)(83.3mA)=1.25W$

3.104 - The production of the minimum capacitent of the same for the full wave rectifier. Therefore use eq. 3.63 to 3.67

$$3.104 - The production of the problem of the same rectifier. Therefore use eq. 3.63 to 3.67

a) Color of the production of the problem of the same for the full wave rectifier as the half wave rectifier. Therefore use eq. 3.57.

 $3.104 - The production of the product$$$

IDC = VOC -24.5V = -49.0A, Ip=(49.0A) - 1/60HB = 2190A