

# FPGA

Producer  
kernel

Consumer  
kernel

Global memory

```
graph TD; subgraph FPGA; PK[Producer kernel]; CK[Consumer kernel]; end; PK --> GM[Global memory]; GM --> CK;
```

The diagram illustrates the architecture of an FPGA. It features a large white rectangle at the top labeled 'FPGA' with a horizontal underline. Inside this rectangle are two cyan-colored squares. The left square is labeled 'Producer kernel' and the right square is labeled 'Consumer kernel'. Below the 'FPGA' rectangle is a gray rectangle labeled 'Global memory'. A thick black arrow points from the bottom of the 'Producer kernel' square down to the 'Global memory' rectangle. Another thick black arrow points from the bottom of the 'Global memory' rectangle up to the bottom of the 'Consumer kernel' square.