

provided with one or more vertical arrows in Fig. 5.1. For example, on the VME backplane.

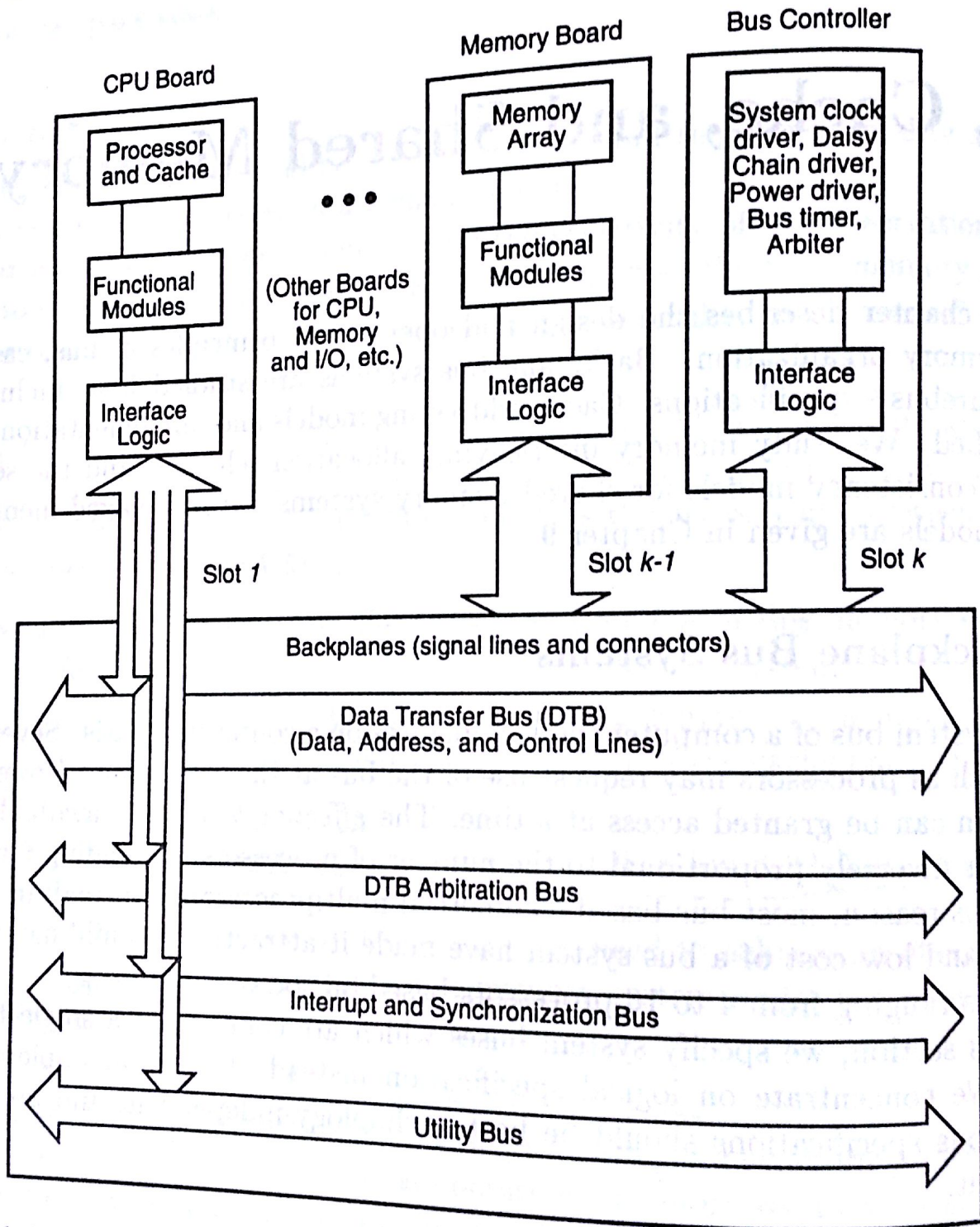


Figure 5.1 Backplane buses, system interfaces, and slot connections to various functional boards in a multiprocessor system.

Data Transfer