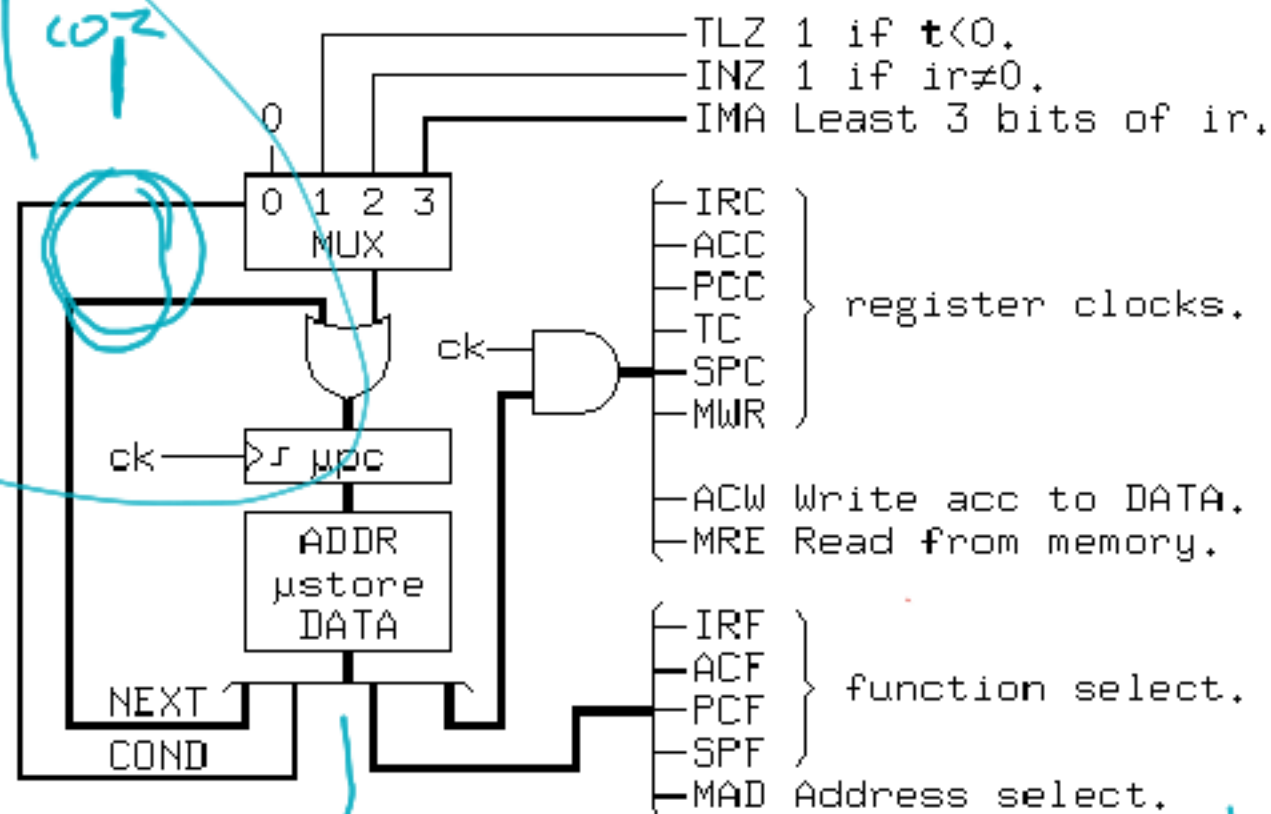
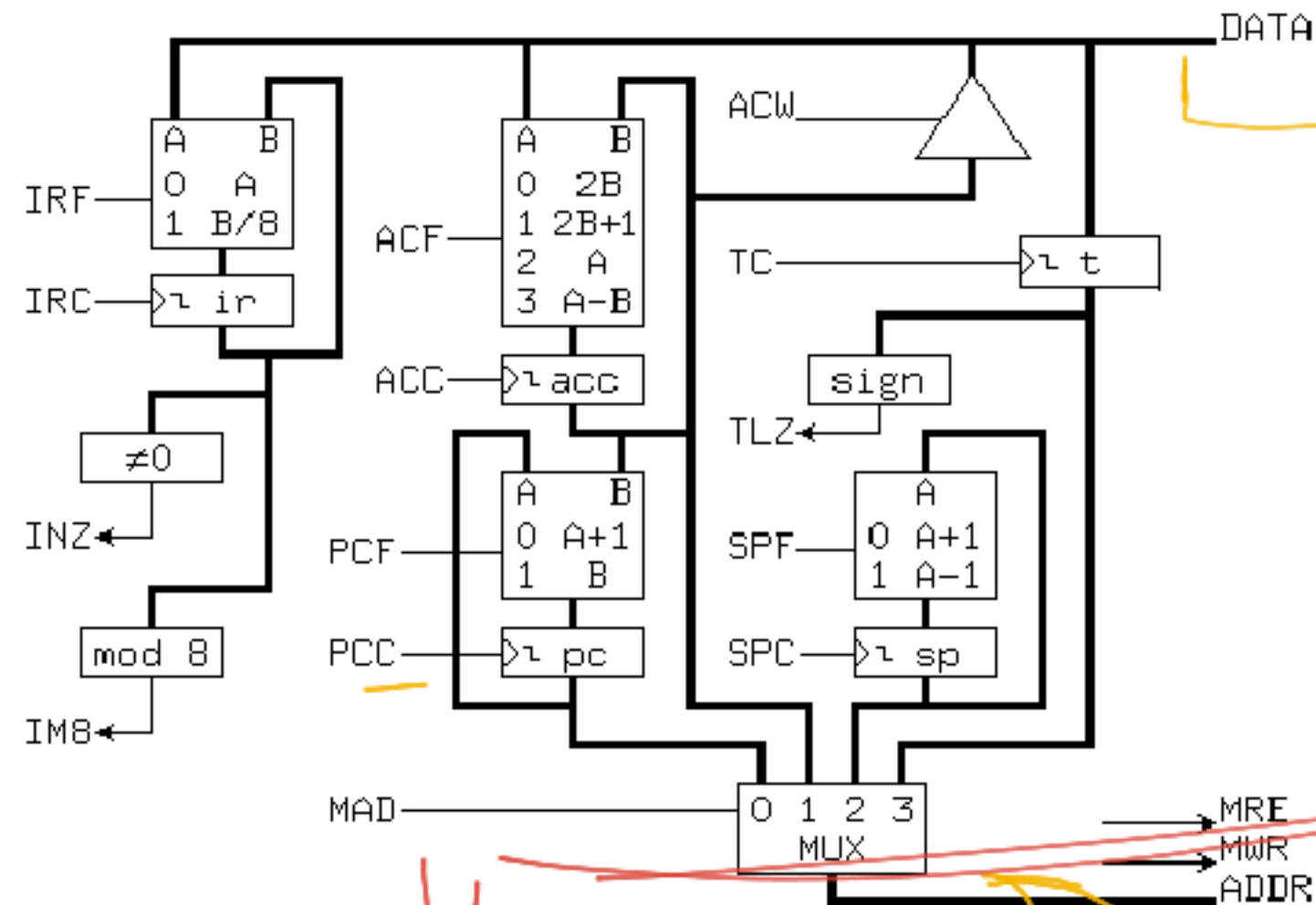


3:30PM presentation. INTRO TO EXMCISC

EXMCISC
extensions:
Privileged memory
access. Privileged
Instructions.
Writable ustore



NP1 = non priv rd

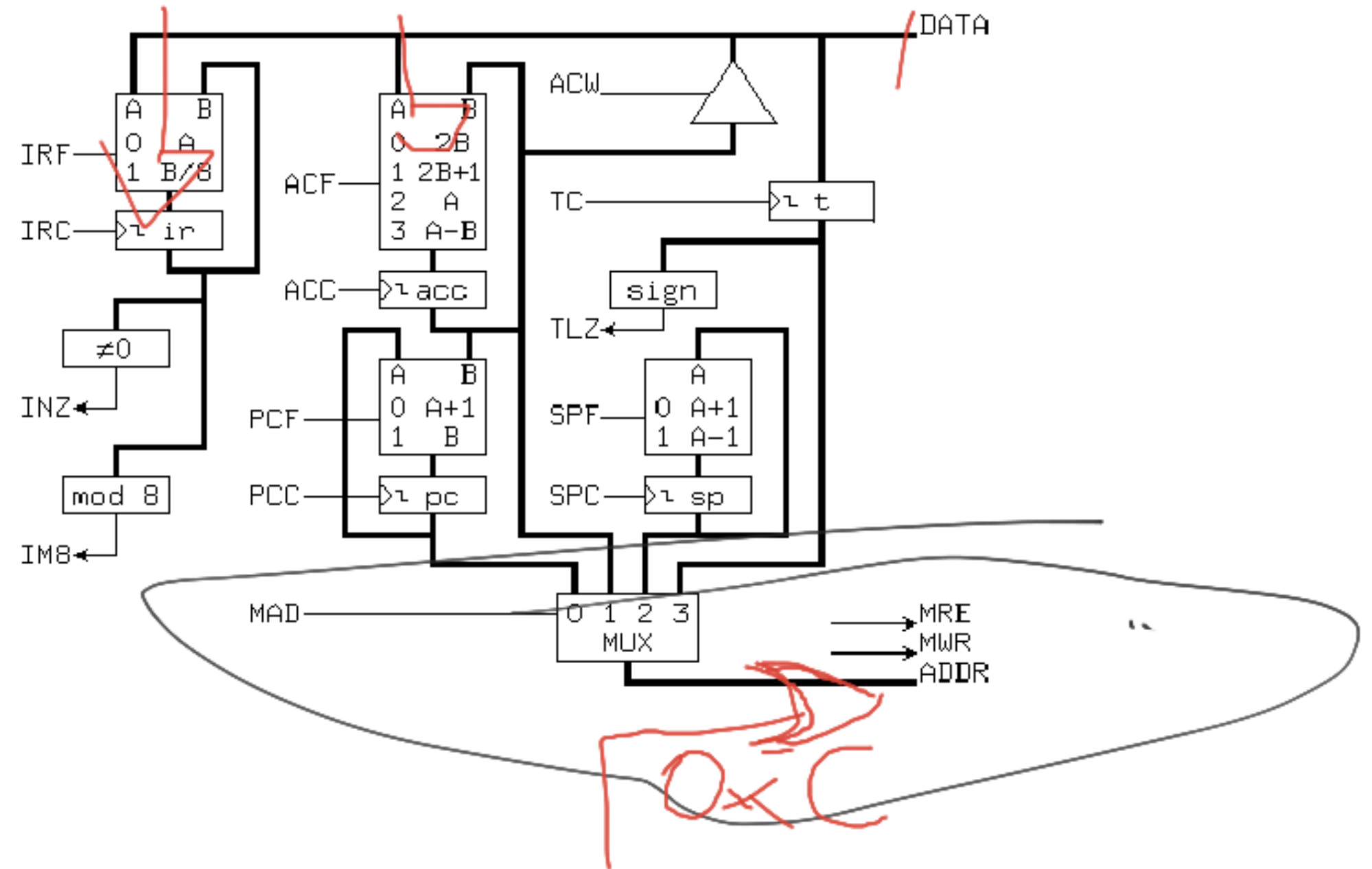
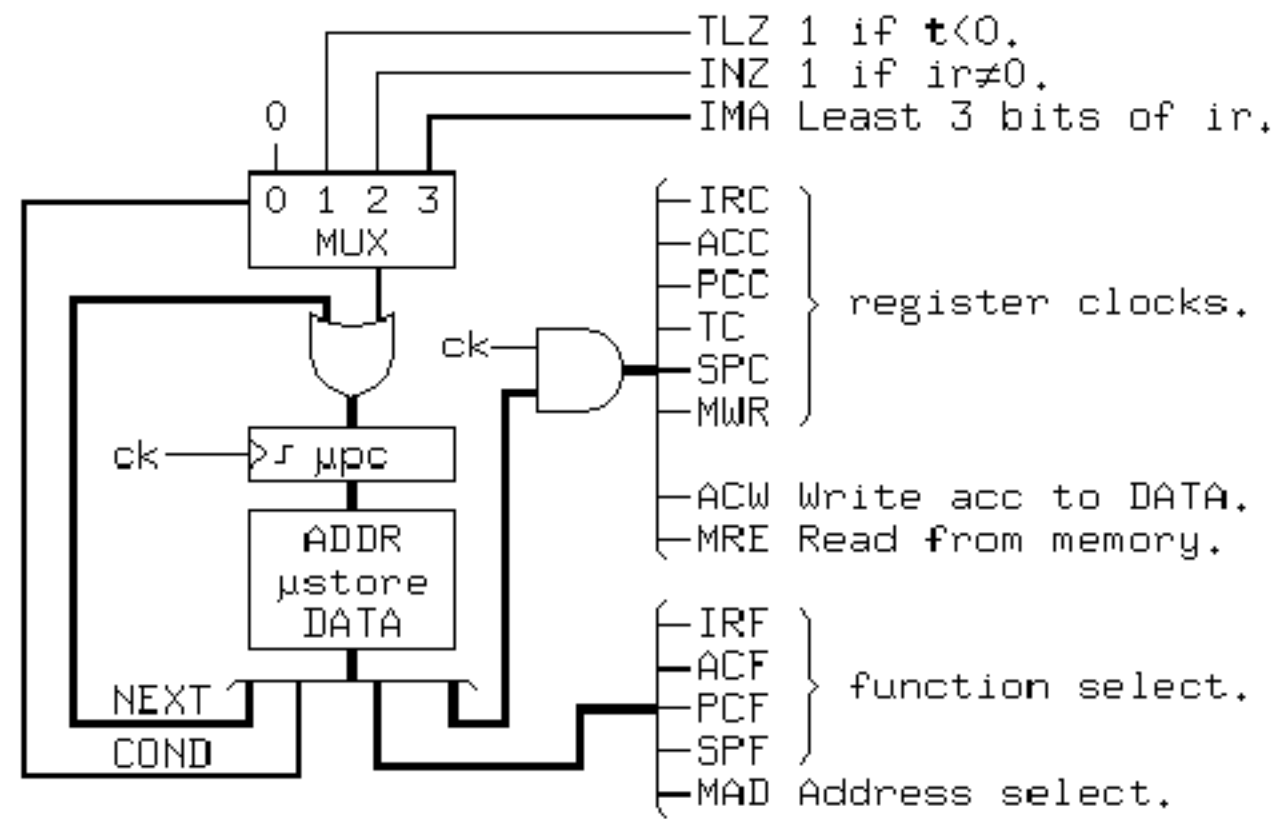


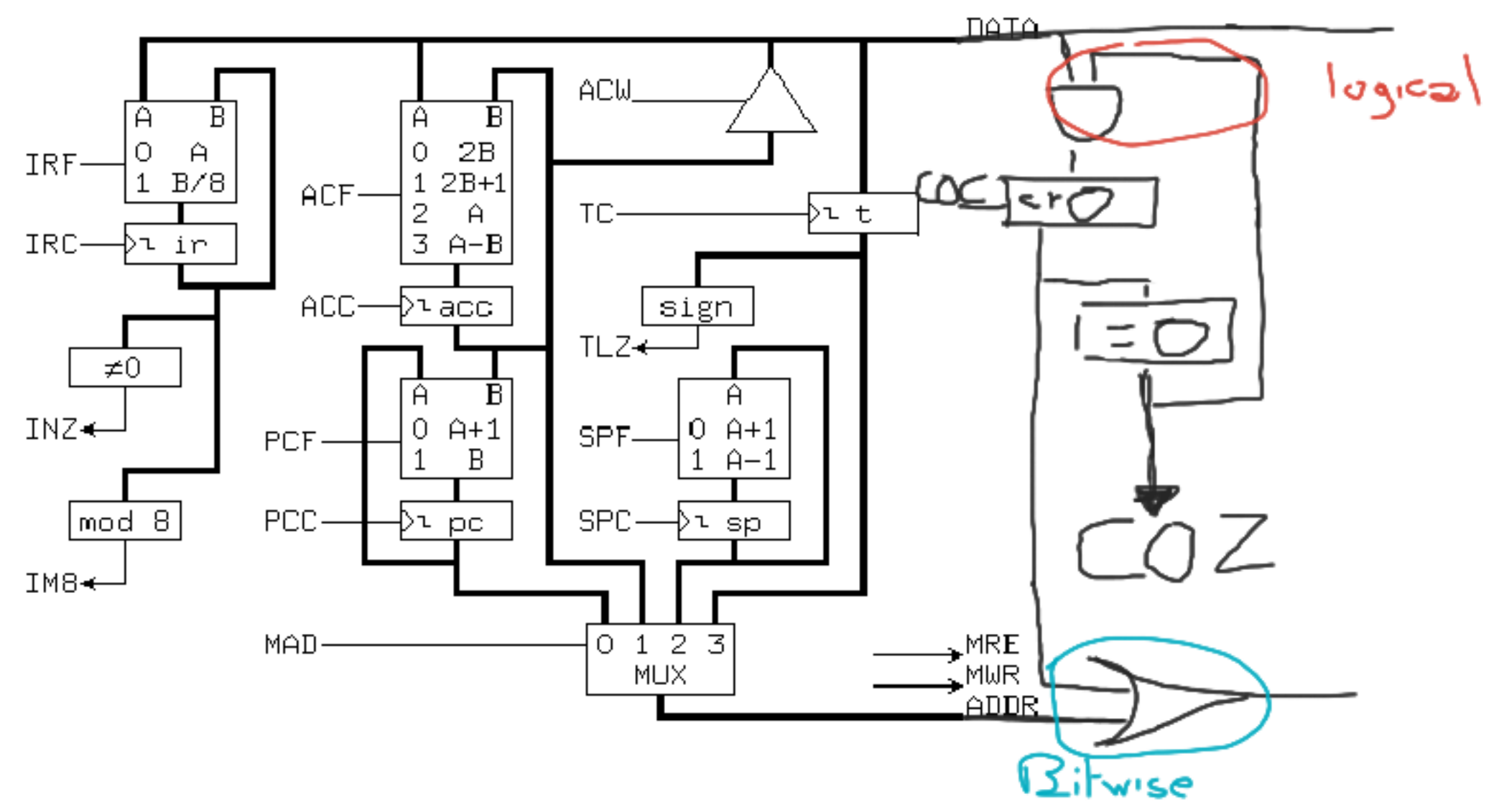
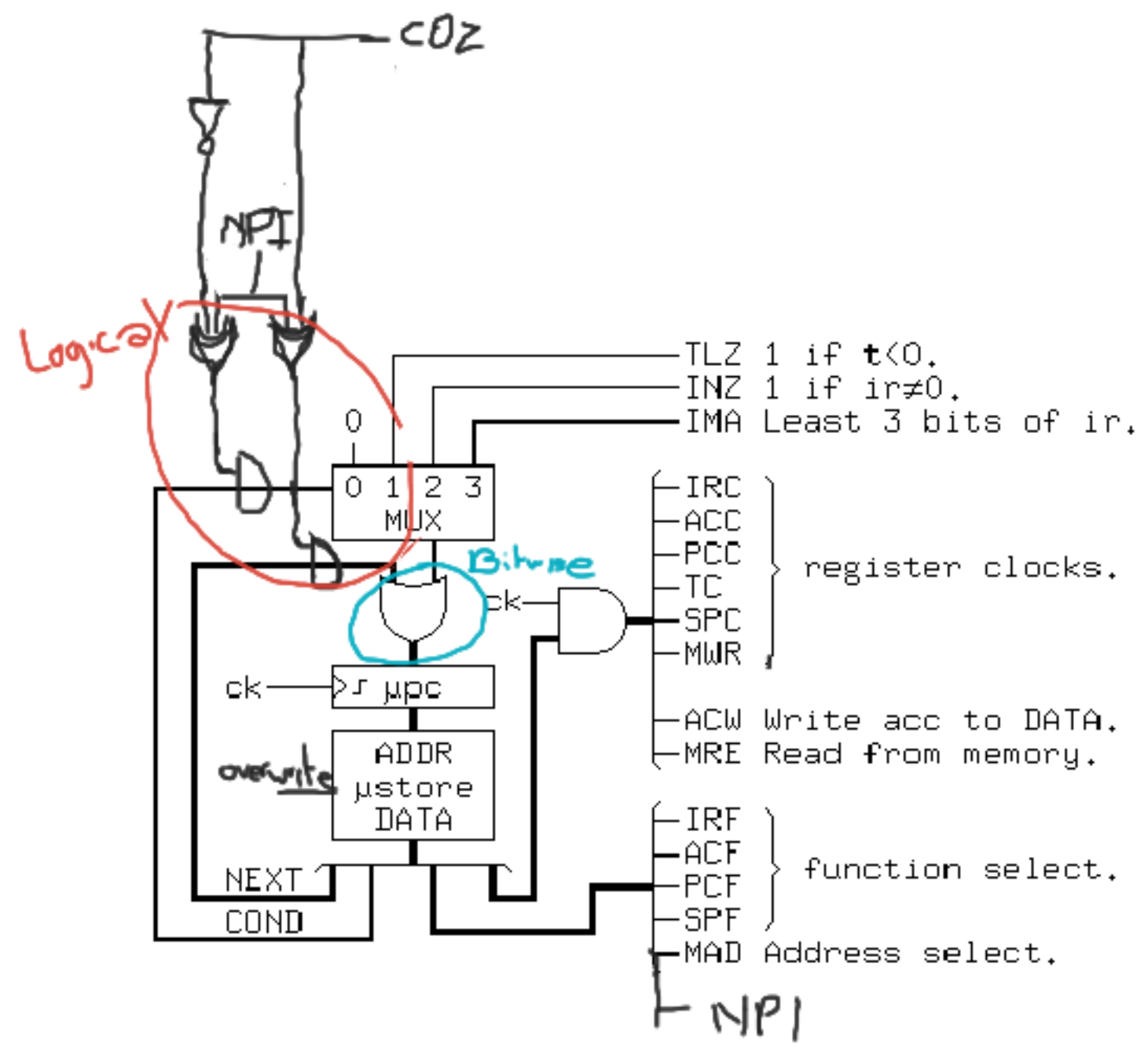
int

LD jmp 0

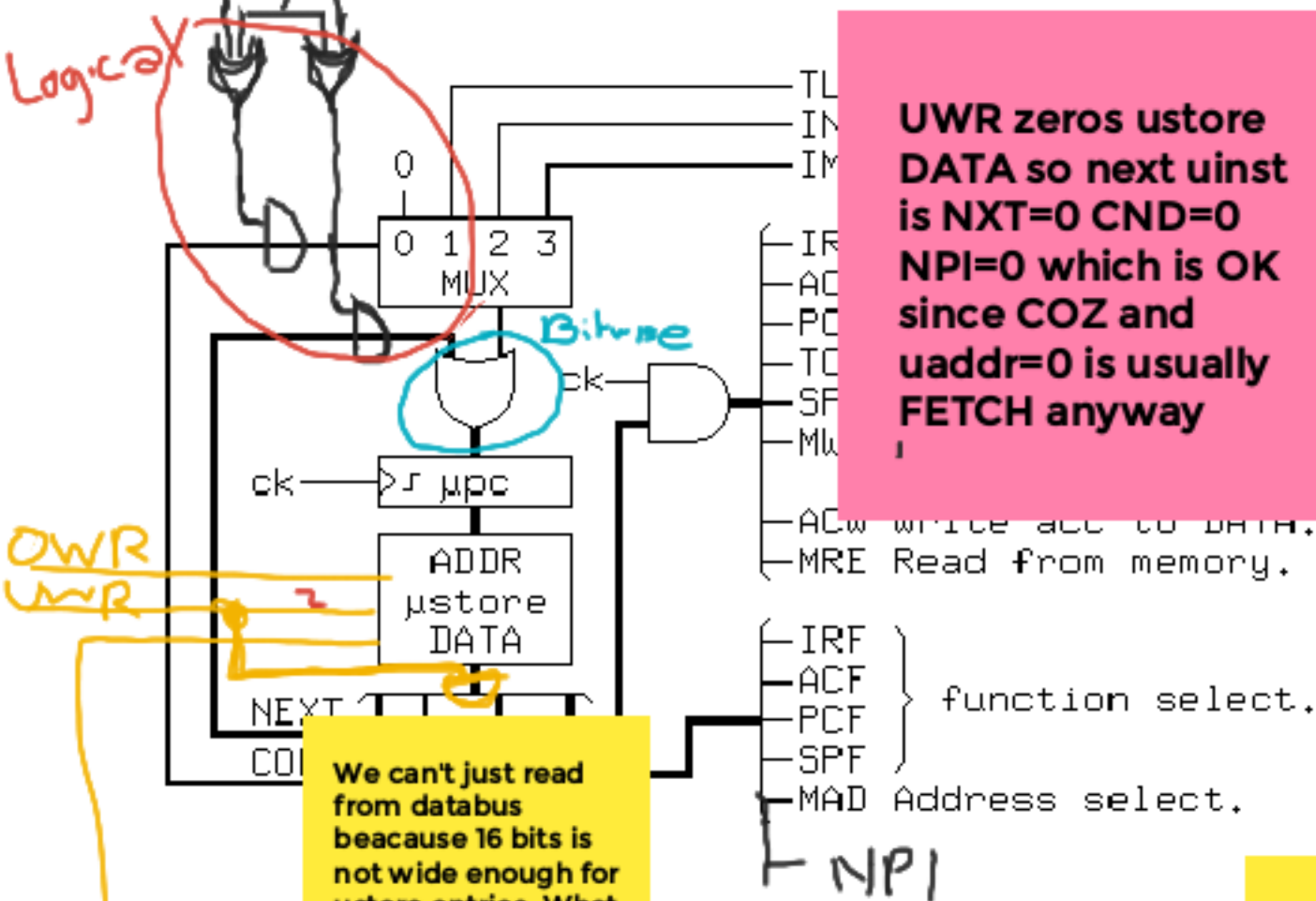
COZ

=0





And more usable regs?



We can't just read from databus because 16 bits is not wide enough for ustore entries. What about setting up DATA AND ADDR to read 32 bits?

DATA } 32 bit?
— ADDR }

2019

