ECE411
PEER DESIGN REVIEW
TEAM 8

TEAM 8:
- Tamarr Stigler-Flores
- Anthony Warner
- Hamed AlSaeghi
- Josh Perdera

TEAM 16:
- Ahmad Alothaimin
- Nathan Paul
- Jakob White
- James Ross

SCHEMATIC NOTES:

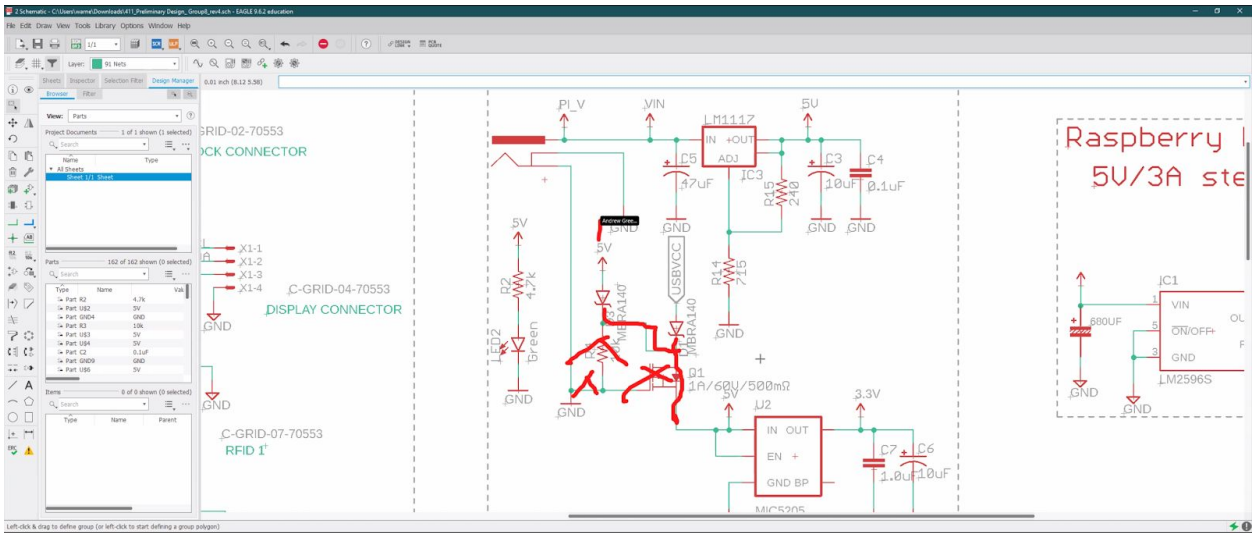
- Remove supply diode.
- Remove 3.3v supply for raspberry pi
- Usbvcc will explode usb chip
- Diode OR connections (5v supply and usbvcc)
- DC plug connect to ground pins 2 and 3
- 4.7k pull-up 5v for LCD I2C
- 1k resistor for LEDs
- 100nF capacitor connection to gnd from USB shield
- Remove Q1 and R4

- S1 Switch:
  Having (1 and 2) connecting in parallel and (3 and 4) in parallel. Isn't this supposed to cause some issues ?

- Solenoid lock and other sensors:
  Better to have them in separate blocks

- Having labels
  Better to connect with wires rather than labels

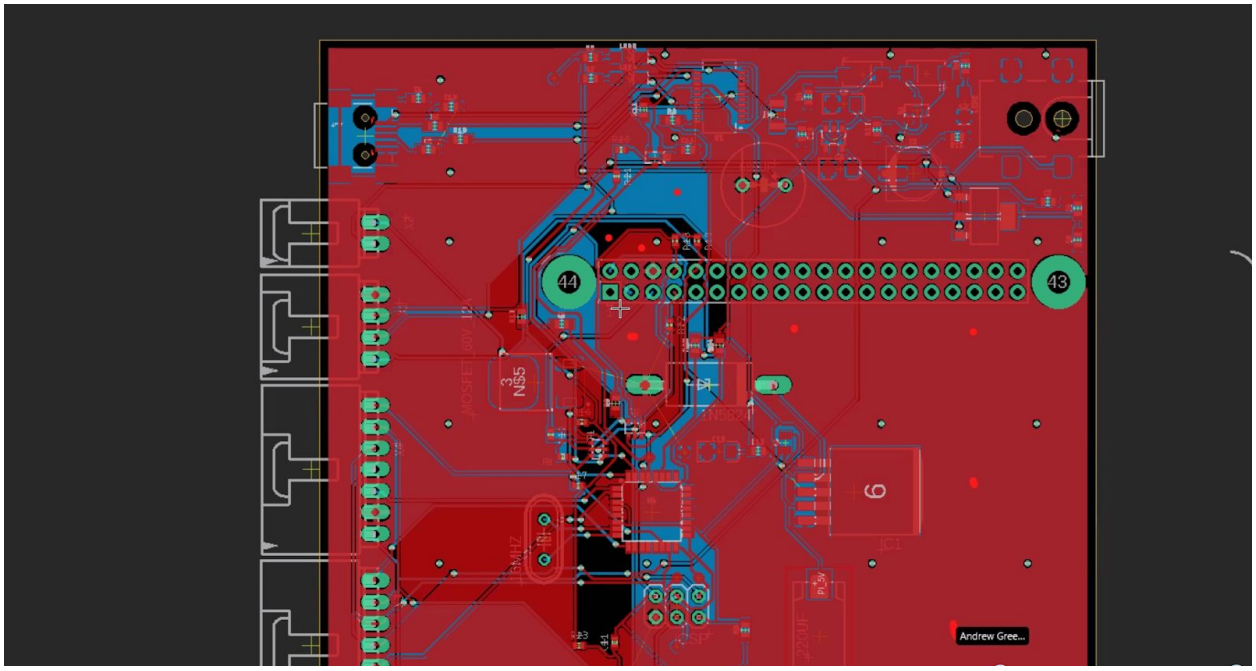
- AREF
  What is it? What does it do?



BOARD NOTES:

- More test points
- Gnd plane
- Test points
- Wider traces
- Board outline skewed
- Flood the gnd plane first
- Group switching power supply on board
- Thick lines on switching power supply
- Placement of mini usb
- Make the silk screen larger print
- Minimum trace size is 12mil for data lines and 50mil for power lines
- Add labels to test points

- Add more ground vias in open spaces



- Bigger SMD inductor