**Plan:**

**Week 1 (05/4-8):**

* Read documents regarding AXI specifications and AXI-SPI.
* Write down requirements for the AXI-SPI IP Core.

**Week 2 (05/11-15):**

* **Waseem:** 
  + Monday: Modify requirements and set up version control.
  + Tuesday: Work on AXI write side.
  + Thursday: TP read side.
  + Friday: Combine read and write for AXI.
* **Colin:**
  + Monday: Modify requirements and set up version control.
  + Tuesday: Work on AXI read side.
  + Thursday: TP write side.
  + Friday: Combine AXI code.

**Week 3 (05/18-22):**

* **Waseem:**
  + Monday: TP AXI Module.
  + Tuesday: Write SPI BRG.
  + Wednesday: Write SPI slave interface.
  + Thursday: combine code with the master part.
  + Friday: Write top entity code.
* **Colin:**
  + Monday: TP AXI Module.
  + Tuesday: Write SPI master interface.
  + Wednesday: Write shift register.
  + Thursday: Combine code with the slave part.
  + Friday: TP SPI component.

**Week 4 (05/25-29):**

* **Waseem:**
  + Monday: Write top entity code.
  + Tuesday: Write interrupt controller code.
* **Colin:**
  + Monday: Write register module code.
  + Tuesday: Complete project TP.