**Plan:**

**Week 1 (05/4-8):**

* Read documents regarding AXI specifications and AXI-SPI.
* Write down requirements for the AXI-SPI IP Core.

**Week 2 (05/11-15):**

* **Waseem:** 
  + Monday: Modify requirements and set up version control.
  + Tuesday: Work on AXI write side.
  + Thursday: TP read side.
  + Friday: Combine read and write for AXI.
* **Colin:**
  + Monday: Modify requirements and set up version control.
  + Tuesday: Work on AXI read side.
  + Thursday: TP write side.
  + Friday: Combine AXI code.

**Week 3 (05/18-22):**

* **Waseem:**
  + Monday: TP AXI Module.
  + Wednesday: code SPI slave operation.
  + Thursday: TP Colin’s SPI components.
  + Friday: Combine SPI code.
* **Colin:**
  + Monday: TP AXI Module.
  + Wednesday: Code SPI master operation.
  + Thursday: TP Waseem’s SPI components.
  + Friday: Combine SPI code.

**Week 4 (05/25-29):**

* **Waseem:**
  + Monday: TP SPI Module.
  + Tuesday: Develop top entity.
  + Thursday: TP top entity.
* **Colin:**
  + Monday: TP SPI Module.
  + Tuesday: Develop top entity.
  + Thursday: TP top entity.

**Week 5 (06/1-5):**

* **Waseem:** 
  + Monday: Integrate all code.
  + Wednesday: TP core.
* **Colin:**
  + Monday: Integrate all code.
  + Wednesday: TP Core.