**ECE 491 Lab 1 – Requirements Checklist**

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| **Description** | **Test Method** | **Detailed Results** |
| 1. Module Interface | Code Inspection |  |
| 2. Module function: accepts four 4-bit inputs and four 1-bit “decimal point” inputs and displays 4 7-segment outputs without noticeable flicker. | Demonstration in hardware using Nexys 4 DDR board:  Proper display of all 8 digits and 16 digit symbols.  Proper display of 4 decimal points  Free of noticeable flicker |  |
| 3. Uses Nexys4 board 100Mhz clock; all flip-flop clock inputs tied directly to this signal | Code inspection  *(fill in to describe how you will verify that this requirement is met)* |  |
| 4. Contains no latches | Inspection of Synthesis Report |  |
| 5. Test circuit – show test that test circuit functions properly to exercises circuit. | Demonstration in hardware  *(fill in to describe the test method – in particular, how will you verify that the test circuit does not have an error?)* |  |
| In submitting this checklist as part of our report, I/We certify that the tests described above were conducted and that the results of these tests are accurately described and represented. I/We understand that any misrepresentation of the tests or the results constitutes a violation of the College policy on academic dishonesty. | | |
| *Name(s): Date:* | | |