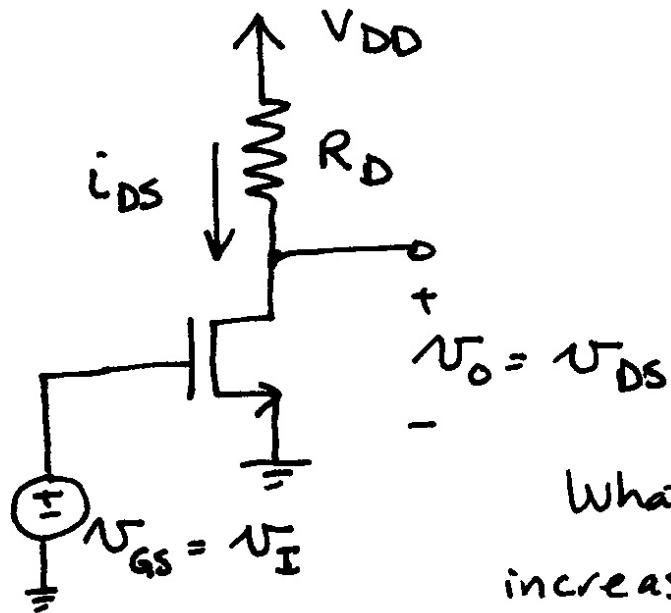


MOSFET in an Amplifier S7.1

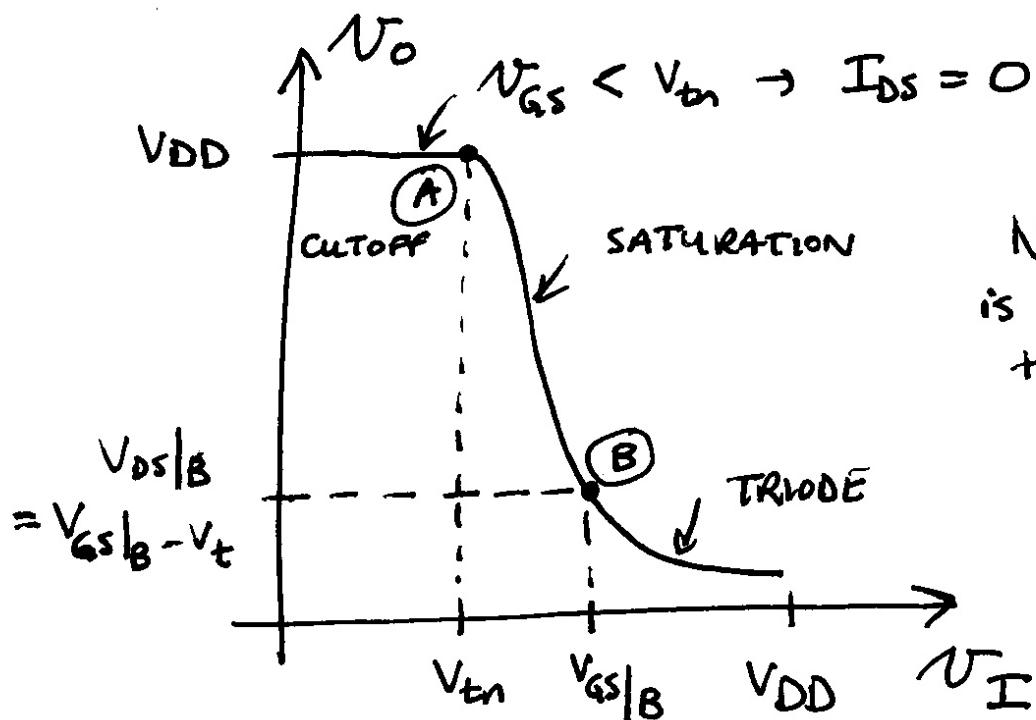
100

Consider the following circuit (known as a common-source amplifier), where we apply an input signal to the gate of a MOS transistor, and where the transistor's drain current, i_{DS} , flows across a load resistor R_D , to produce an output voltage V_o :



Clearly, $V_o = V_{DD} - R_D i_{DS}$, since the transistor's current must flow onto R_D .

What happens as V_I is increased from 0V to V_{DD} ?



NOTE: the slope is steepest as the transistor is in the saturation region

101

$$V_I < V_{tn} \rightarrow \text{transistor CUTOFF}$$

$$I_{DS} = 0 \rightarrow V_o = V_{DD}$$

$$V_{tn} \leq V_I \leq V_{GS}|_B \rightarrow V_{DS} = V_o > V_I - V_{tn}$$

→ transistor in SATURATION

$$V_o = V_{DD} - R_D \frac{\mu_n C_{ox} \frac{W}{L}}{2} (V_I - V_{tn})^2$$

$$V_I > V_{GS}|_B \rightarrow V_{DS} = V_o < V_I - V_{tn}$$

→ transistor in TRIODE

$$V_o = V_{DD} - R_D \mu_n C_{ox} \frac{W}{L} \left((V_I - V_{tn})V_o - \frac{V_o^2}{2} \right)$$

(quadratic equation)

Question: where is point B?

$V_{DS}|_B = V_{GS}|_B - V_{tn}$, i.e. transition between
SAT. & TRIODE

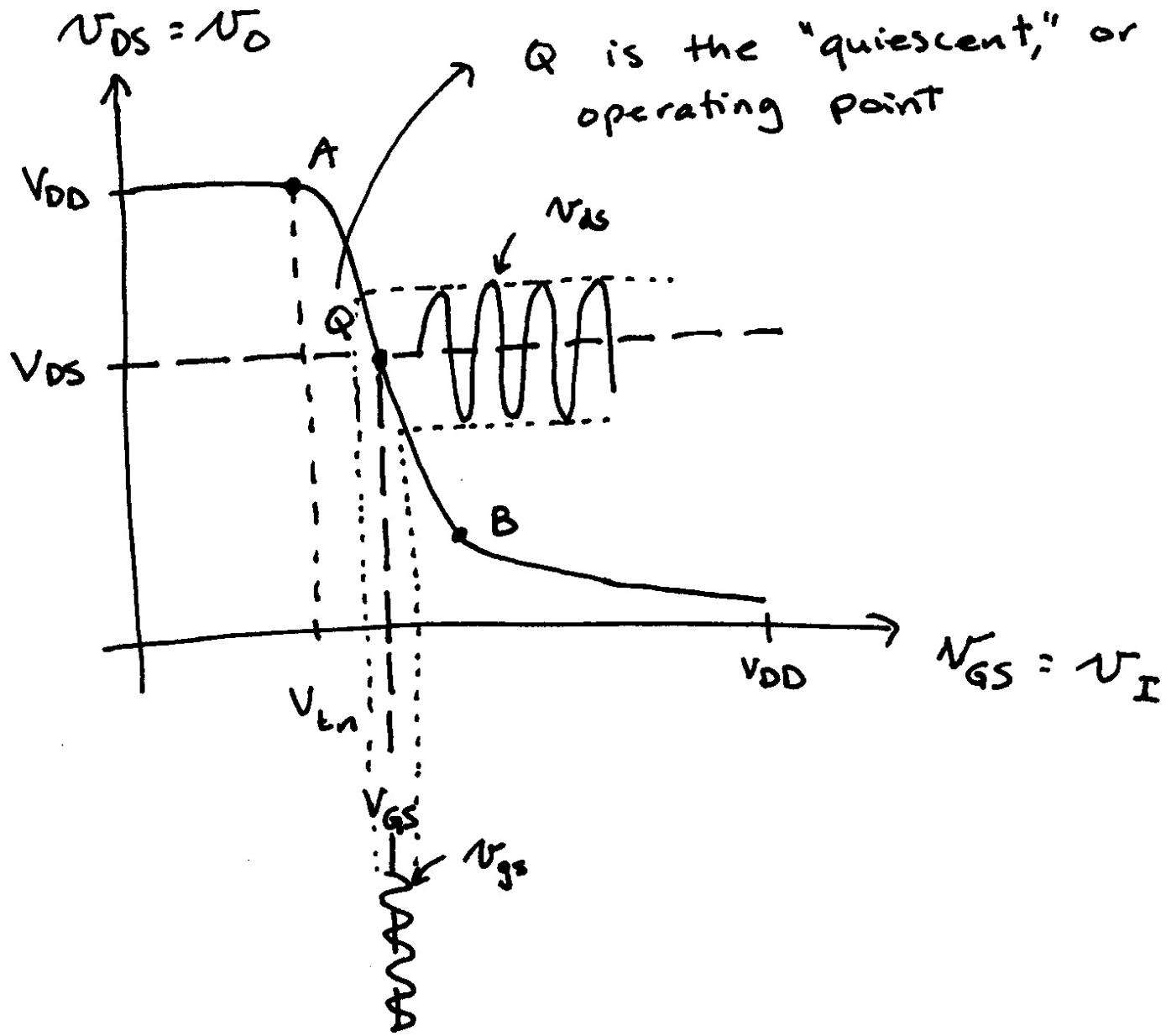
$$V_{DS}|_B = V_{DD} - R_D \frac{\mu_n C_{ox} \frac{W}{L}}{2} (V_{GS}|_B - V_{tn})^2$$

↓

$$V_{GS}|_B - V_{tn}$$

$$\text{Solve for } V_{GS}|_B = V_{tn} + \frac{\sqrt{2k_n R_D V_{DD} + 1} - 1}{k_n R_D}$$

Let's use the circuit as an amplifier 102



At the input, we have a signal

$$V_{GS}(+) = V_{GS} + v_{gs}(+)$$

↑
whole
signal

↑
DC
"biasing"
component

↑
small-
signal
to be
amplified

Question: how much "bigger" is
 $N_{ds}(t)$ than $N_{gs}(t)$?

(103)

We could solve for $V_{DS}(t)$ exactly,

$$\text{i.e. } V_{DS}(t) = V_{DD} - R_D \frac{\mu_n C_{ox}}{2} \frac{W}{L} (V_{GS} + N_{gs}(t) - V_{tn})^2$$

$$= V_{DD} - R_D \underbrace{\frac{\mu_n C_{ox} W}{2 L}}_{k_n/2} \left[(V_{GS} - V_{tn})^2 + 2(V_{GS} - V_{tn})N_{gs}(t) + N_{gs}(t)^2 \right]$$

constant,
 doesn't
 vary with t
 $\rightarrow V_{DS}$

$+ N_{gs}(t)^2$

tiny.
 $N_{gs} \ll V_{GS} - V_{tn}$
 \rightarrow ignore!
 varies with t
 $\rightarrow N_{ds}(t)$

$$N_{DS}(t) = \left[V_{DD} - R_D \frac{k_n}{2} (V_{GS} - V_{tn})^2 \right]$$

$$= V_{DS} - R_D \times k_n (V_{GS} - V_{tn}) N_{gs}(t) + N_{ds}(t)$$

Note:

the slope, $\frac{\partial V_{DS}}{\partial V_{GS}}$ at point Q is:

$$A_V = -R_D \times k_n (V_{GS} - V_{tn})$$

$$\text{so } V_{DS}(+) = (A_V) V_{GS}(+) \\ \uparrow$$

the voltage gain of
the circuit.

We can rewrite this as:

$$A_V = -k_n V_{DS} R_D$$

$$= -\underbrace{\frac{2I_D}{V_{DS}}}_{\text{V}_{DS}} R_D$$

note: there is always one term dependent
on the transistor, times R_D .

Summary:

(105)

Step 1 Find Q , i.e. the DC operating point \rightarrow find V_{GS} and V_{DS} .

Step 2 Find the gain, by finding the slope at point Q .

$$V_{DS}(+) \approx V_{DS} + (A_V) \times v_{gs}(+)$$

$v_{gs}(+)$ small so we ignore square term.

Notation:

$\overset{\leftarrow}{V}_{DS}(+)$: total signal
 \nwarrow caps

$\overset{\leftarrow}{V}_{DS}$: dc component
 \nwarrow caps

$\overset{\leftarrow}{v}_{ds}(+)$: Small-signal (ac)
 \nwarrow small ds component.

Example

part (a) (Based on Example 7.1)

(106)

In our circuit, $V_{tn} = 0.4V$, $k_n' = 0.4mA/V^2$,
 $W/L = 10$, $\lambda = 0$, $V_{DD} = 1.8V$, $R_D = 17.5k\Omega$
 $V_{GS} = 0.6V$.

What are: V_{ov} , I_D , V_{DS} , A_v
 $\underbrace{\hspace{10em}}$
dc values.

$$\textcircled{1} \quad V_{ov} = V_{GS} - V_{tn} = 0.6V - 0.4V = 0.2V$$

$$\textcircled{2} \quad I_D = \frac{k_n' \frac{w}{L}}{2} V_{ov}^2 = 0.08mA$$

$$\textcircled{3} \quad V_{DS} = V_{DD} - I_D R_D = 0.4V$$

$$\textcircled{4} \quad A_v = -k_n V_{ov} R_D = \left(\frac{0.4mA}{V^2} \right) (10) (0.2V) (17.5k\Omega)$$
$$= -14 \quad \underline{V/V}$$

to clearly indicate
that this is
voltage gain

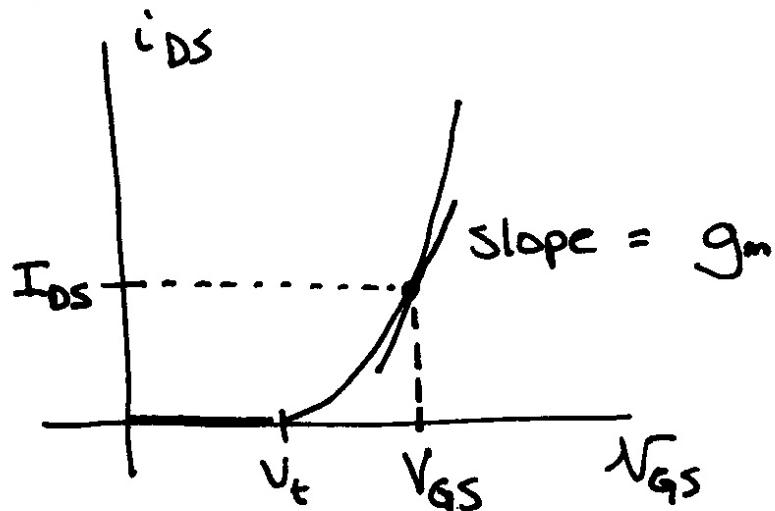
Recall that $A_v = - \underbrace{k_n V_{ov}}_{\text{dependent on transistor}} \times R_D \quad \text{V/V}$

$$= \left. \frac{\partial I_{DS}}{\partial V_{GS}} \right|_{V_{GS} = V_{GS}}$$

$$k_n V_{ov} = \mu_n C_{ox} (V_{GS} - V_t) = \left. \frac{\partial I_{DS}}{\partial V_{GS}} \right|_{V_{GS}},$$

a parameter with units of conductance (Ω^{-1}), that relates how much the output current changes in relation to a change in the gate voltage.

We call this parameter the transconductance g_m of the transistor:

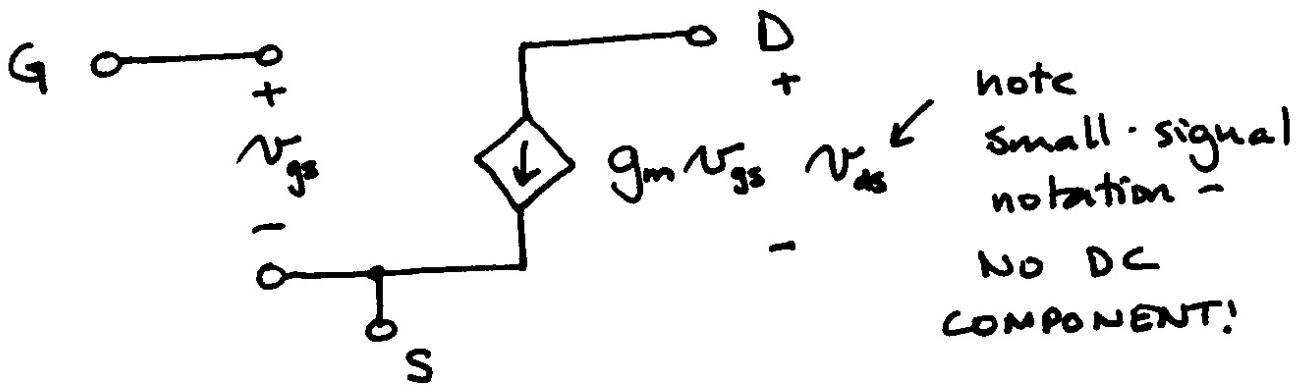


We can therefore write

(108)

$A_v = -g_m R_D$, the small-signal voltage gain of the common-source amplifier.

In small-signal analysis, we can replace the transistor with its small-signal equivalent circuit:



So far, we have implicitly assumed that $\lambda = 0$. In a real transistor, i_{DS} also varies with v_{DS} . Recall,

$$r_o = \left[\frac{\partial i_{DS}}{\partial v_{DS}} \Big|_{V_{DS}, V_{GS}} \right]^{-1} = \frac{|V_A|}{I_{DS}} = \frac{1}{\lambda I_{DS}}$$

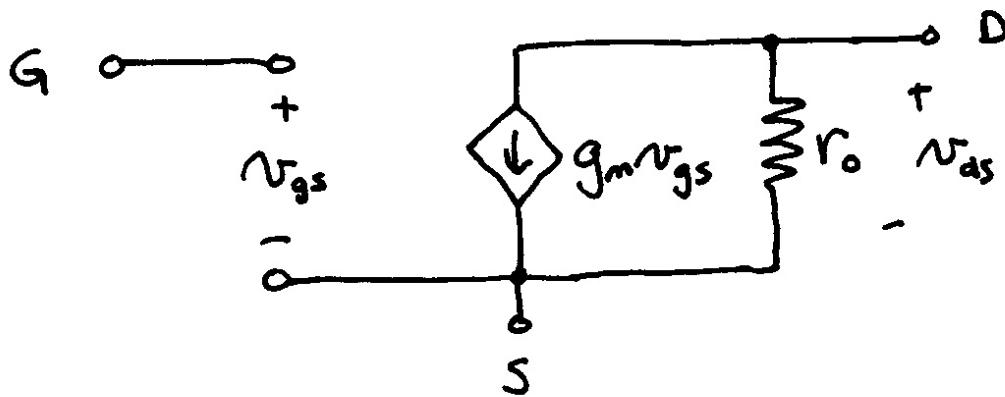
DC bias point

Typically, r_o is in the range of

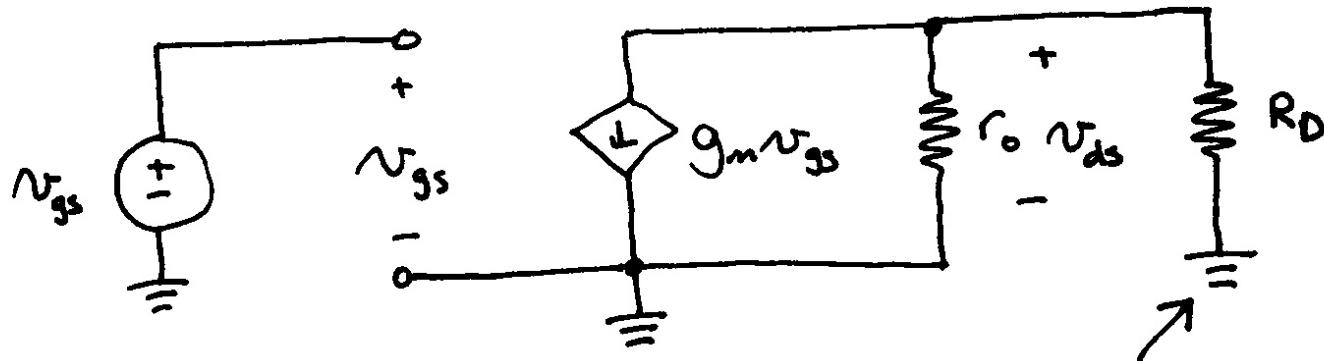
(109)

$10\text{k}\Omega$ to $1\text{M}\Omega$.

In a transistor's small-signal model, we add this as a resistor r_o between D and S:



In the common-source amplifier, we replace the transistor with its small-signal equivalent:



$$\frac{\partial V_{DD}}{\partial \Delta v_{gs}} = 0$$

small-signal ground

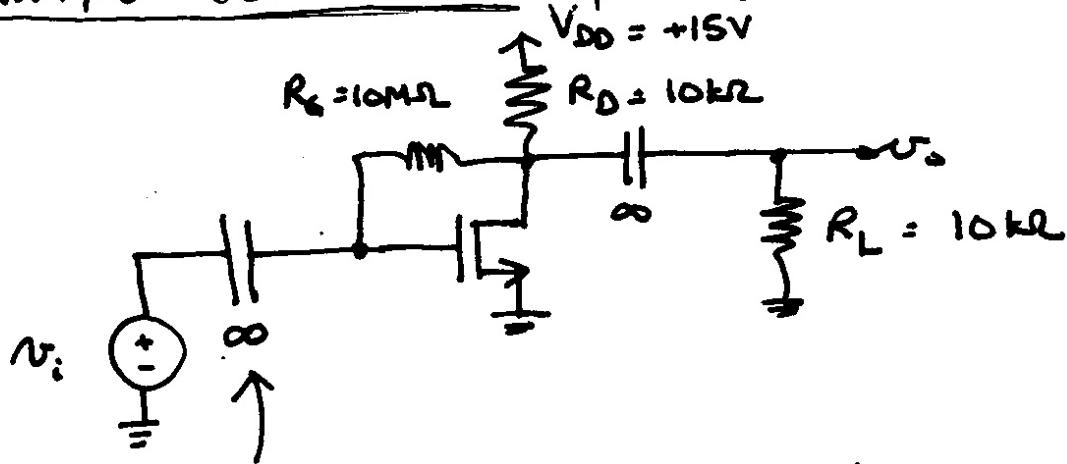
Analyzing this linear circuit, we

get $\frac{V_{ds}}{V_{gs}} = A_v = -g_m (R_D \parallel r_o)$

r_o acts as if
it's in parallel
with R_D .

Problems 7.1, 3, 8, 24, 26, 30, 31, 33

Example 7.1, 3

Example (Based on Example 7.3)

means open circuit at dc
short circuit at ac frequencies

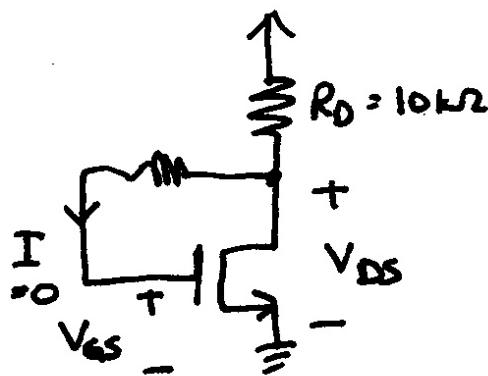
$$V_t = 1.5V$$

$$k_n = 0.25 \text{ mA/V}^2$$

$$V_A = 50V$$

$$\text{What is } A_v = N_o / N_i ?$$

Step 1 Find the dc operating point. (V_{GS} , V_{DS} , I_{DS})



Since $I = 0$,

$$V_{DS} = V_{GS} \quad (\text{saturation})$$

$$= V_{DD} - I_{DS} R_D$$

$$I_{DS} = \frac{k_n}{2} (V_{GS} - V_t)^2 \quad (\text{we usually ignore } \lambda \text{ at this stage})$$

$$\rightarrow V_{GS} = V_{DD} - \frac{R_D k_n}{2} (V_{GS} - V_t)^2$$

\rightarrow quadratic equation, with meaningful solution $V_{GS} = V_{DS} = 4.4V$

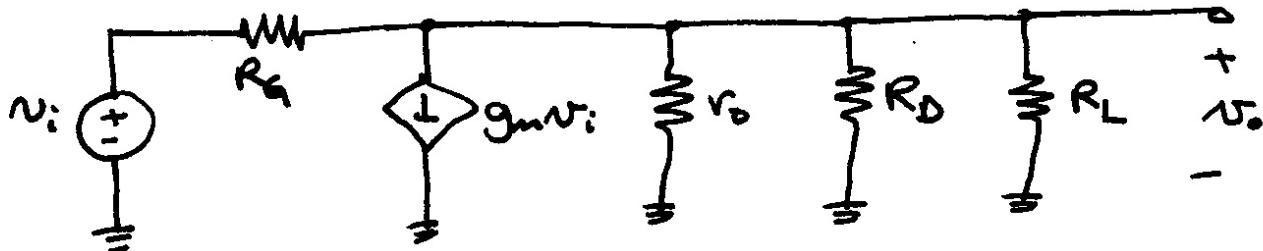
$$I_{DS} = 1.06 \text{ mA}$$

$$\rightarrow V_{ov} = V_{GS} - V_t = 2.9V$$

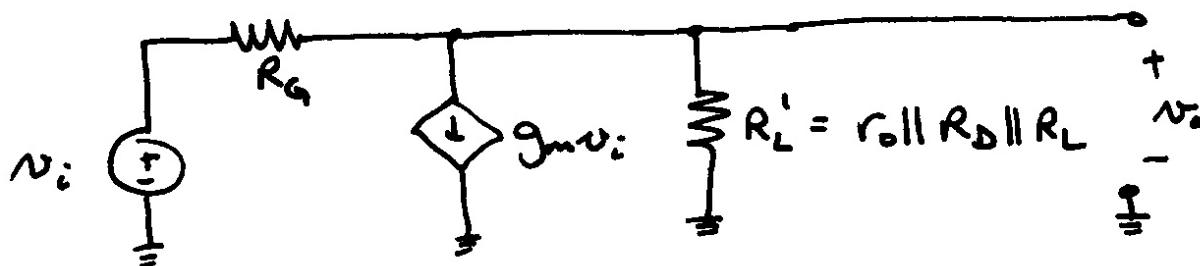
Step 2 Small-Signal analysis

(112)

Small-signal model:



Simplifies to:



$$g_m = k_n V_{DS} = \left(\frac{0.25 \text{ mA}}{\text{V}^2} \right) (2.9 \text{ V}) = 0.725 \text{ mA/V}$$

$$r_o = \frac{V_A}{I_D} = \frac{50 \text{ V}}{1.06 \text{ mA}} = 47 \text{ k}\Omega$$

$$R_L' = (47 \text{ k}\Omega \parallel 10 \text{ k}\Omega \parallel 10 \text{ k}\Omega) = 4.52 \text{ k}\Omega$$

→ Doing all the math we get

$$A_v = -g_m R_L' \left(\frac{1 - g_m R_G}{1 + R_L' R_G} \right)$$

but notice \$R_G\$ is big
(it was only used to
set the dc operating
point...)

$$\text{so } A_v \approx -g_m R_L' = -0.725 \frac{\text{mA}}{\text{V}} \times 4.52 \text{ k}\Omega = -3.3 \frac{\text{V}}{\text{V}}$$

what
we had
before