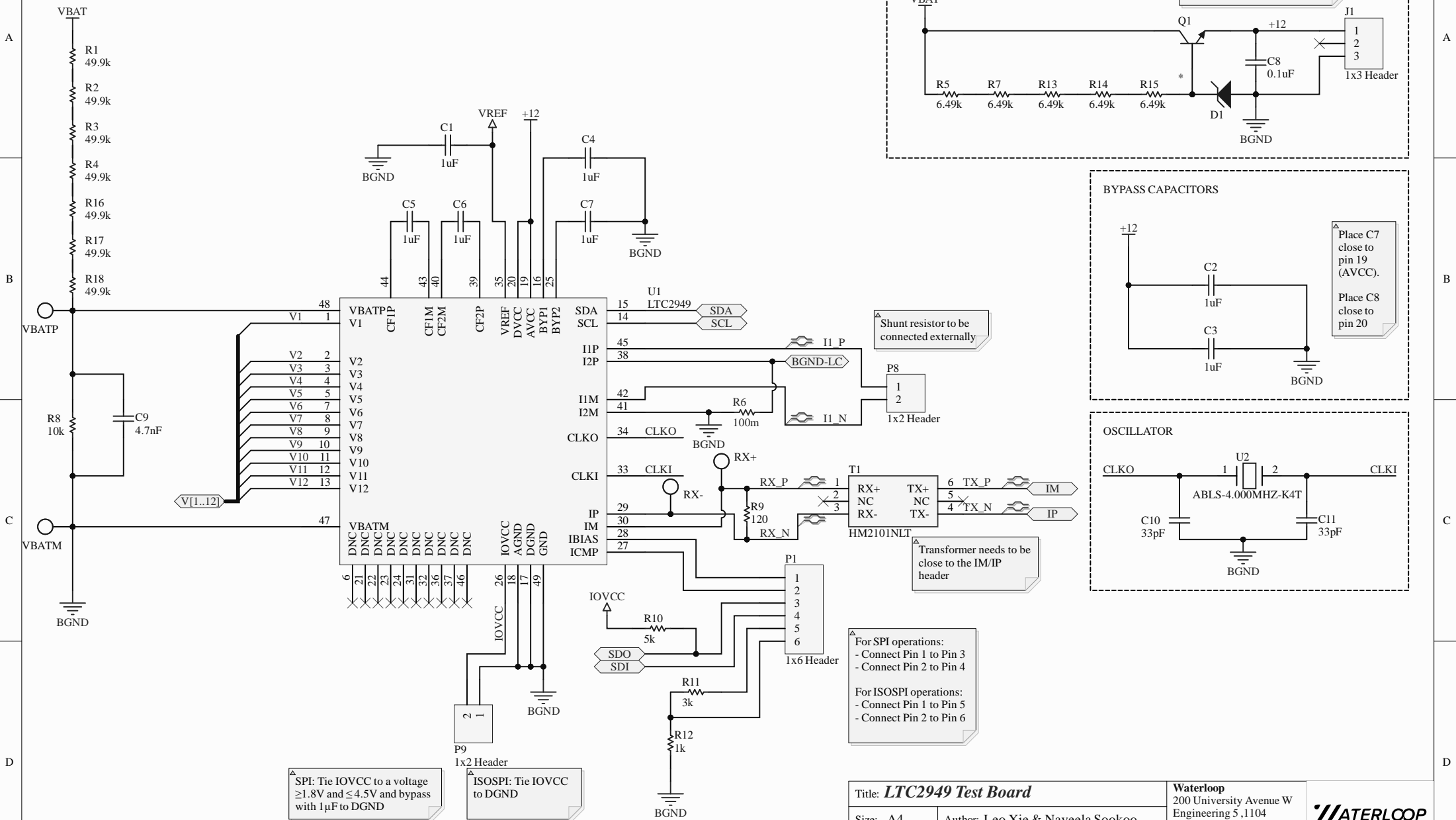


LTC2949 VOLTAGE AND CURRENT SENSING



▲ SPI: Tie IOVCC to a voltage $\geq 1.8V$ and $\leq 4.5V$ and bypass with 1uF to DGND

▲ ISOSPI: Tie IOVCC to DGND

▲ For SPI operations:
- Connect Pin 1 to Pin 3
- Connect Pin 2 to Pin 4

For ISOSPI operations:
- Connect Pin 1 to Pin 5
- Connect Pin 2 to Pin 6

Title: **LTC2949 Test Board**

Size: A4 Author: Leo Xie & Naveela Sookoo

Date: 2022-04-02 Revision: 1 Sheet 2 of 3

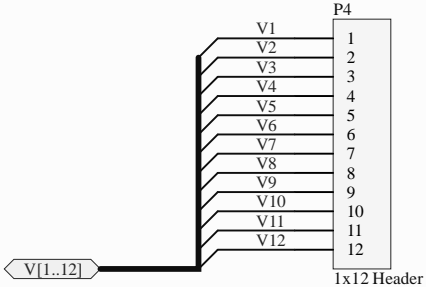
File: LTC2949.SchDoc

Waterloop
200 University Avenue W
Engineering 5, 1104
Waterloo, Ontario
N2L 3G1

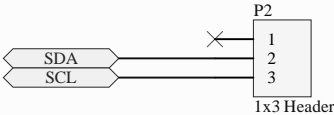


CONNECTORS

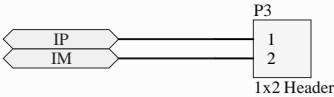
LTC2949 Voltage Inputs and GPIO Pins



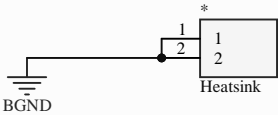
LTC2949 I/O Pins



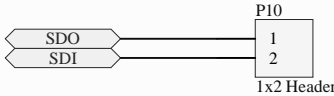
Isolated Interface Positive I/O



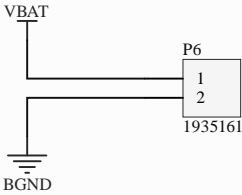
Heatsink



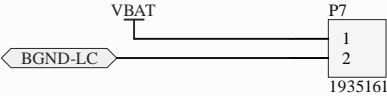
Serial Peripheral Interface



Battery Connector



Motor Connector



VREF 3.3V Header

