


For SPI operations:

- Connect Pin 1 to Pin 3
- Connect Pin 2 to Pin 4

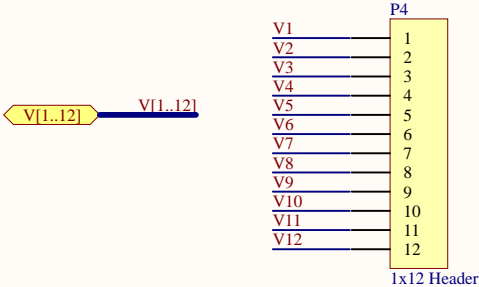
For ISOSPI operations:

- Connect Pin 1 to Pin 5
- Connect Pin 1 to Pin 6

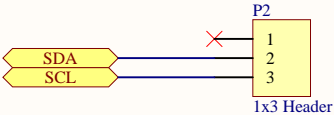
Title: <b><i>LTC2949 Test Board</i></b>			<b>Waterloop</b> 200 University Avenue W Engineering 5,1104 Waterloo, Ontario N2L 3G1	
Size: <b>A4</b>	Author: <b>Leo Xie &amp; Naveela Sookoo</b>			
Date: <b>2022-02-21</b>	Revision: <b>1</b>	Sheet <b>2</b> of <b>3</b>		
File: <b>LTC2949.SchDoc</b>				

CONNECTORS

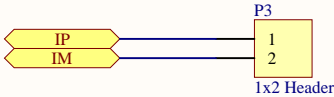
LTC2949 Voltage Inputs and GPIO Pins



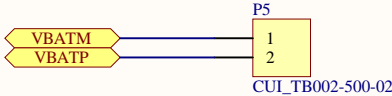
LTC2949 I/O Pins



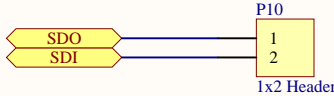
Isolated Interface Positive I/O



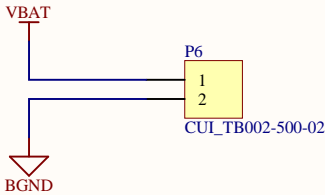
Battery Voltage Measurement



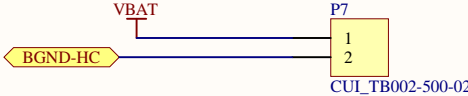
Serial Peripheral Interface



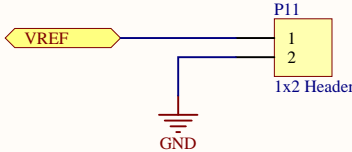
Battery Connector

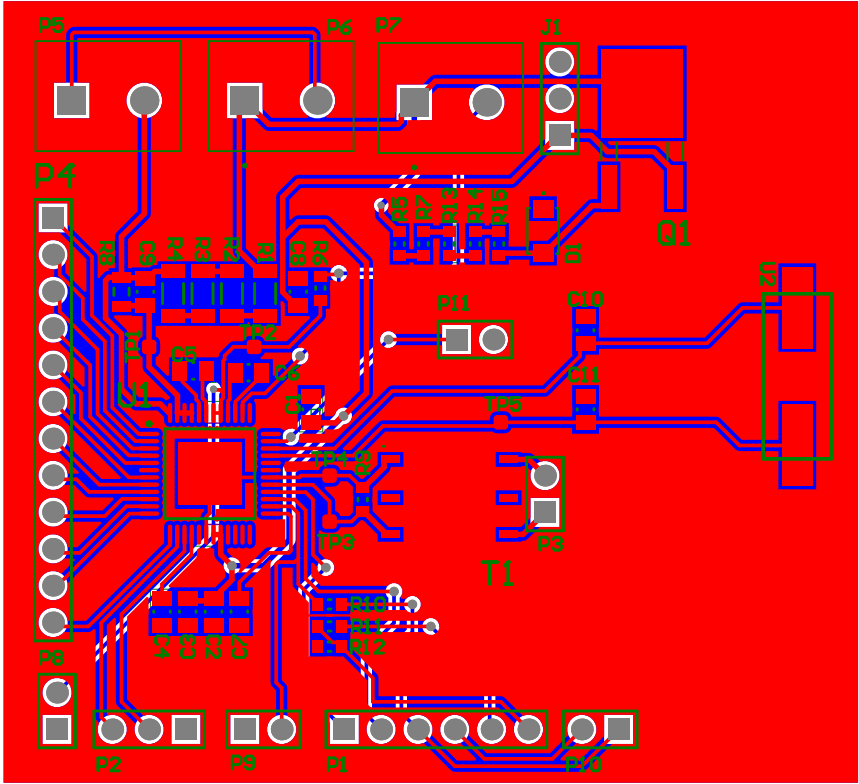


Motor Connector



VREF 3.3V Header





# Board Stack Report