

A

B

C

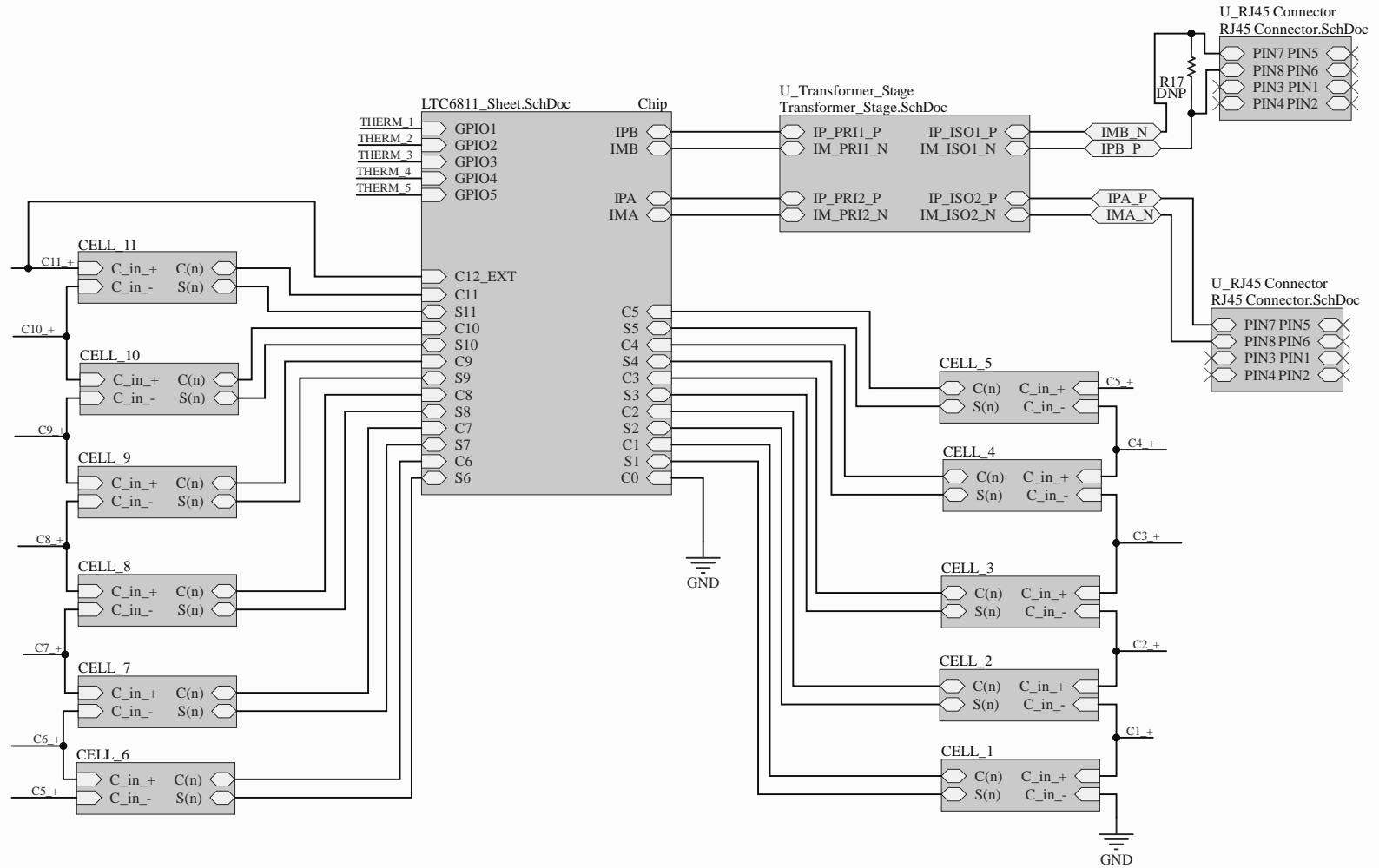
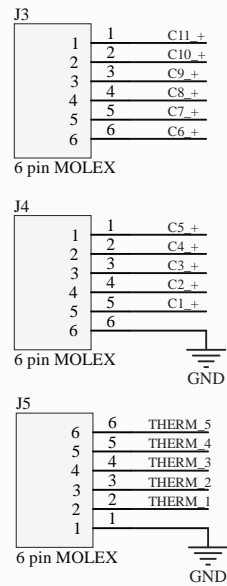
D


A

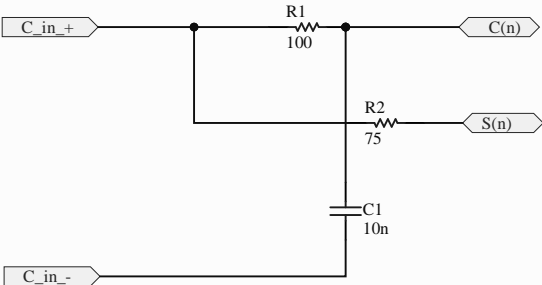
B

C

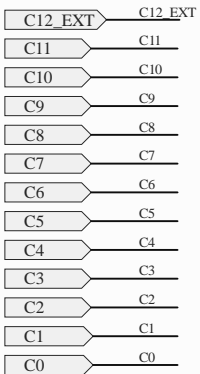
D



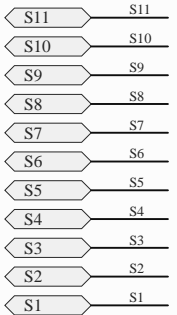
Title: *			Waterloop 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *			
Date: 2022-12-06	Revision: *	Sheet 1 of 5		
File: Monitoring IC.SchDoc				



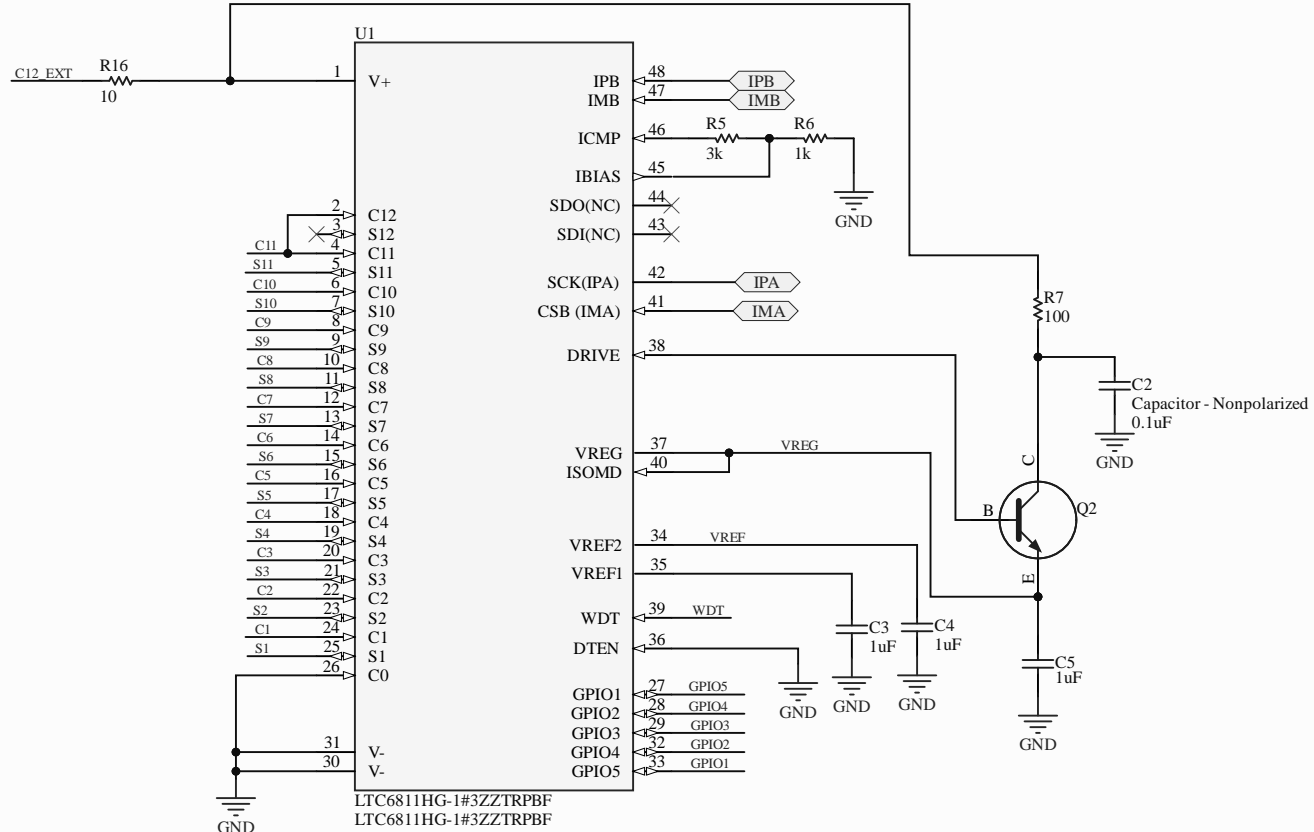
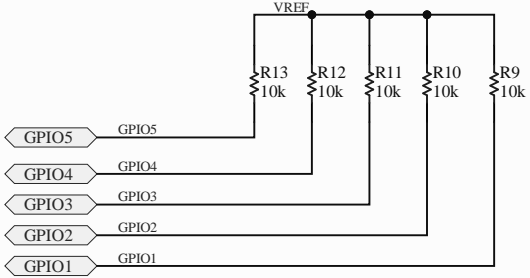
Cell Inputs



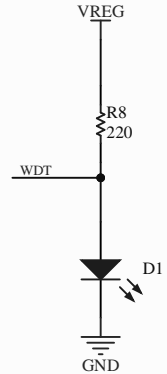
S Pin



GPIO INPUTS

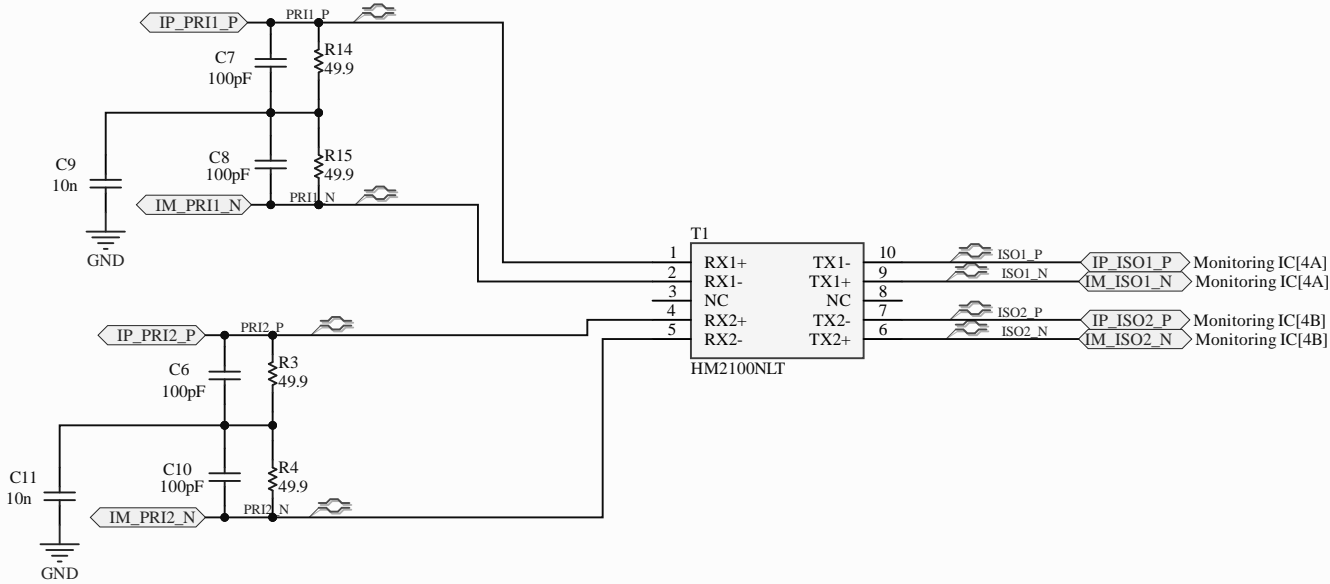


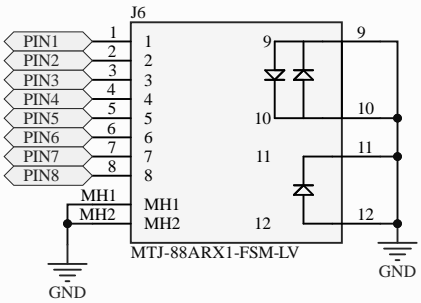
WDT LED




Title: *		Waterloop 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *	Sheet 3 of 5	
Date: 2022-12-06	Revision: *		
File: LTC6811_Sheet.SchDoc			







Title: *			Waterloop 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *			
Date: 2022-12-06	Revision: *	Sheet 5 of 5		
File: RJ45 Connector.SchDoc				

J3

