
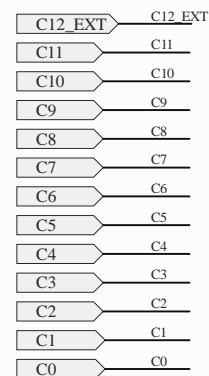
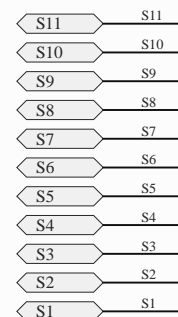


Title: *			<b>Waterloop</b> 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *			
Date: 2023-02-07	Revision: *	Sheet 2 of 5		
File: Cell Balancing.SchDoc				

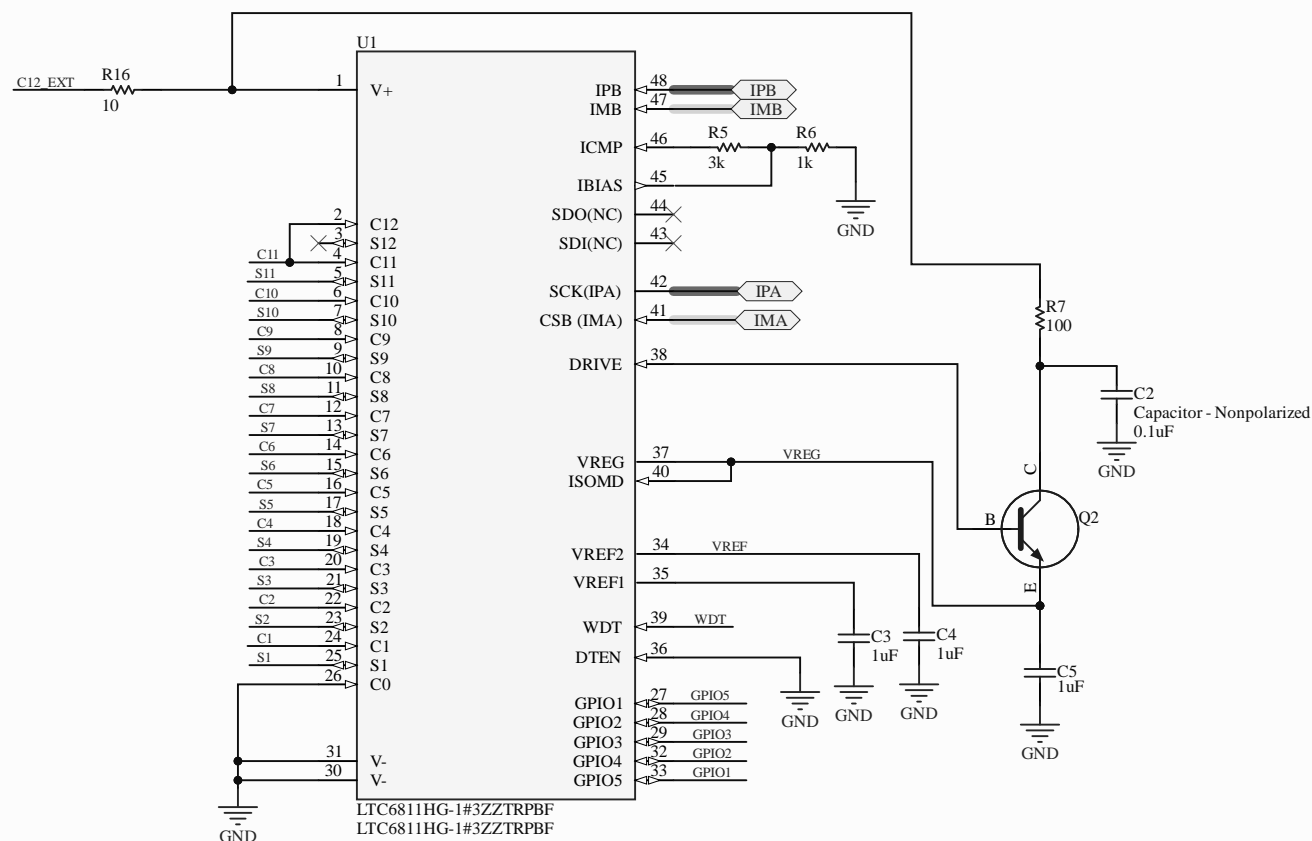
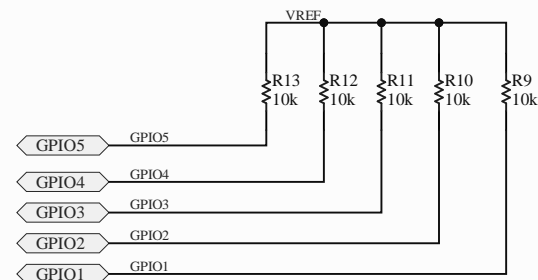
## Cell Inputs



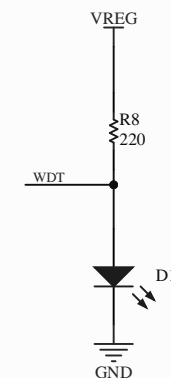
S Pin




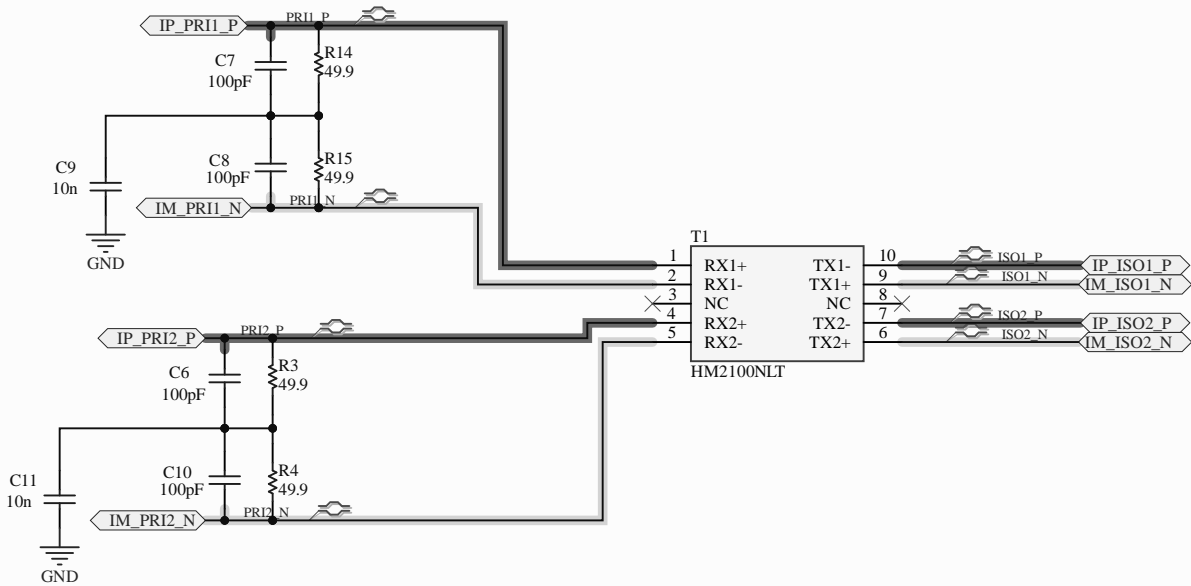
## GPIO INPUTS




# WDT LED

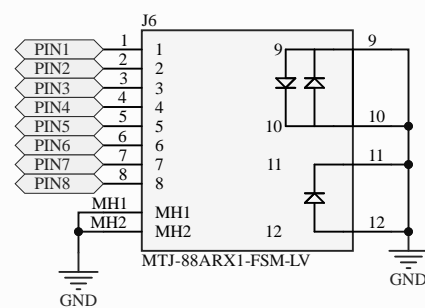



Title: *			<b>Waterloop</b> 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *			
Date: 2023-02-07	Revision: *	Sheet 3 of 5		
File: LTC6811 Sheet.SchDoc				



Title: *			<b>Waterloop</b> 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *			
Date: 2023-02-07	Revision: *	Sheet 4 of 5		
File: Transformer_Stage.SchDoc				





Title: *			<b>Waterloop</b> 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1	
Size: A4	Author: *			
Date: 2023-02-07	Revision: *	Sheet 5 of 5		
File: RJ45 Connector.SchDoc				

# Waterloop – BMS DISCHARGING BOARD

Akshat Doctor

