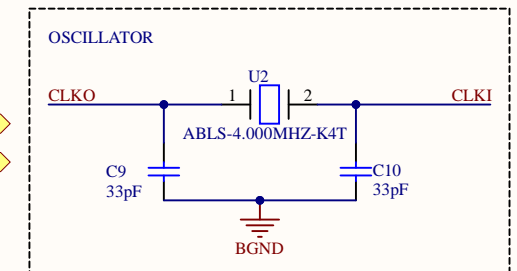
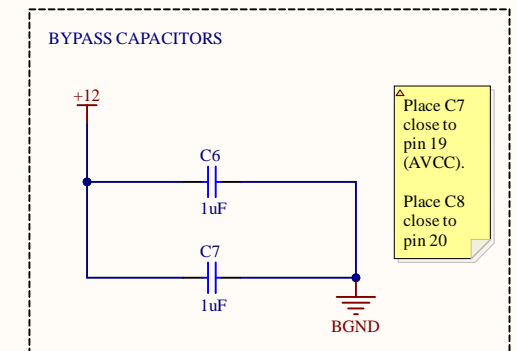
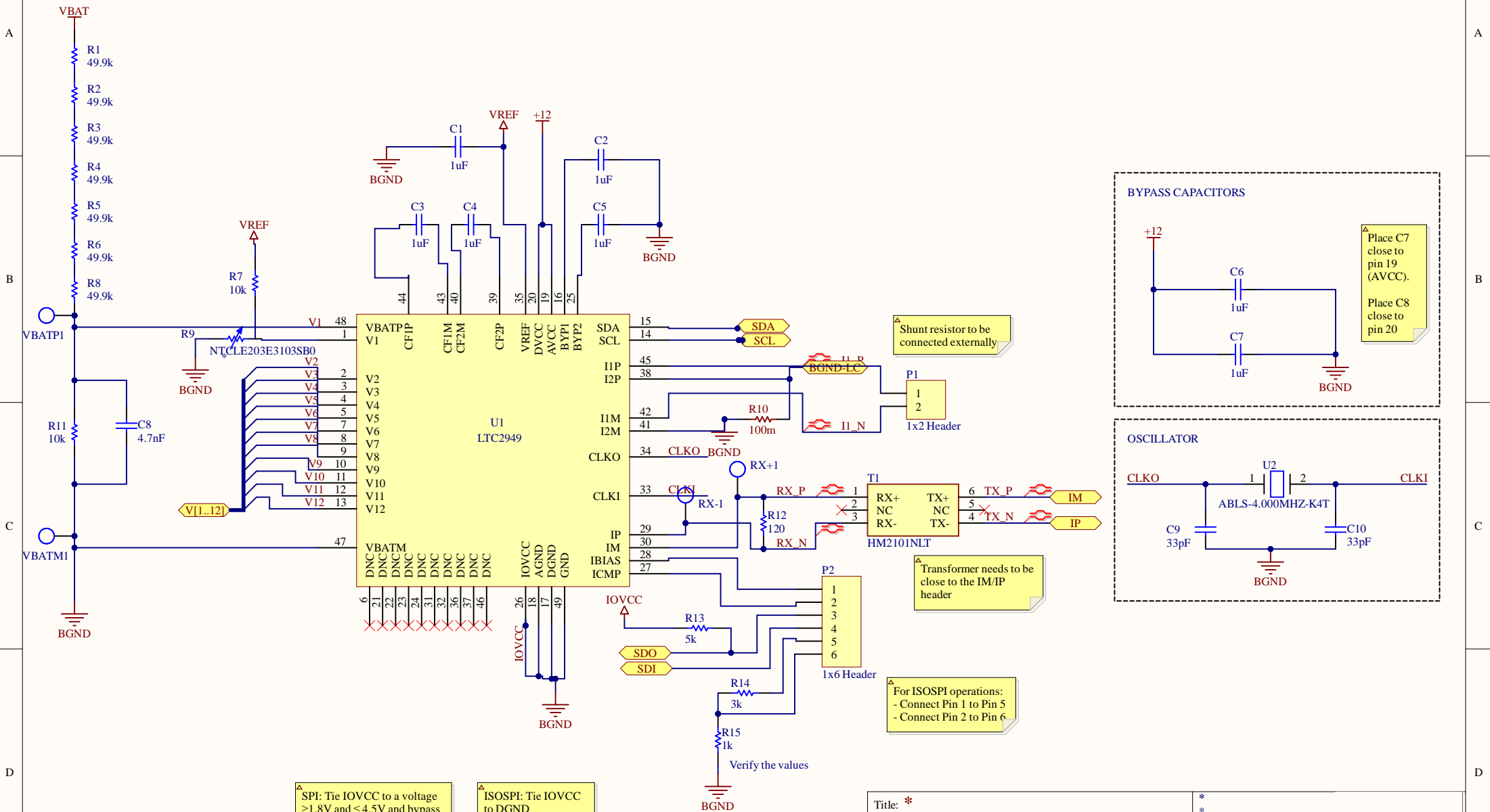


# LTC2949 VOLTAGE AND CURRENT SENSING



▲ SPI: Tie IOVCC to a voltage  $\geq 1.8V$  and  $\leq 4.5V$  and bypass with  $1\mu F$  to DGND

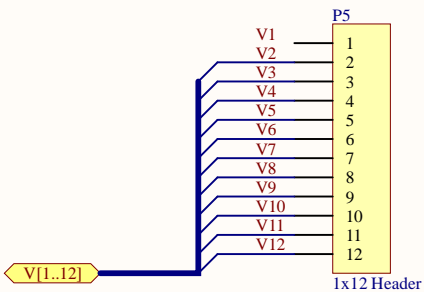
▲ ISOSPI: Tie IOVCC to DGND

Title: *		* * * *
Size: A4	Author: *	
Date: 04-11-2022	Revision: *	
File: LTC2949.SchDoc	Sheet 2 of 3	

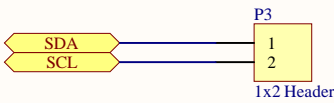
**WATERLOOP**

CONNECTORS

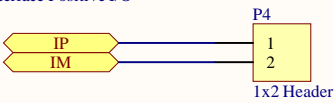
LTC2949 Voltage Inputs and GPIO Pins



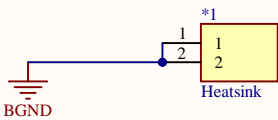
LTC2949 I/O Pins



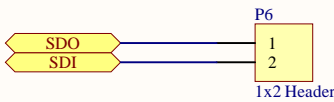
Isolated Interface Positive I/O



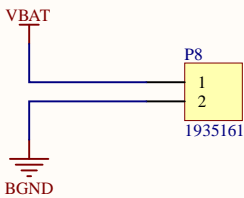
Heatsink



Serial Peripheral Interface



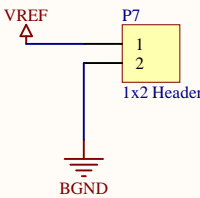
Battery Connector



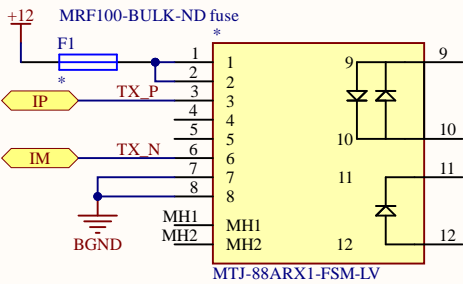
Motor Connector



VREF 3.3V Header



RJ45 Connector



Title		
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File:	C:\Users\...\LTC2949_Connectors.SchDoc	Drawn By:

# Board Stack Report