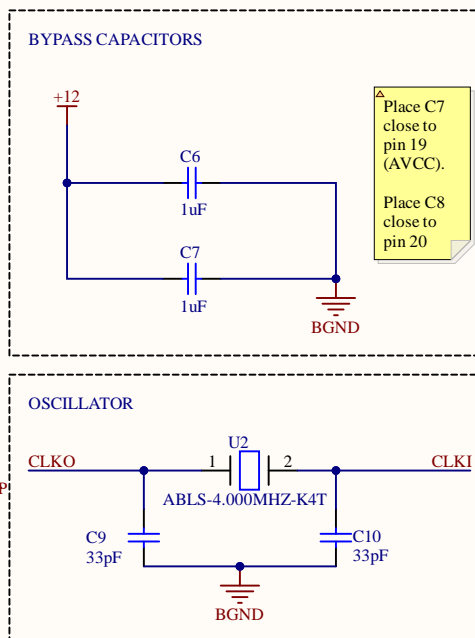


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Date:	11-12-2022	Sheet of
File:	C:\Users\...\LTC2949_Top.SchDoc	Drawn By:



△ ISOSPI: Tie IOVCC to DGND

△ SPI: Tie IOVCC to a voltage  $\geq 1.8\text{V}$  and  $\leq 4.5\text{V}$  and bypass with  $1\mu\text{F}$  to DGND


Shunt resistor to be connected externally

WSL25122L700FEB18

Transformer needs to be close to the IM/IP header

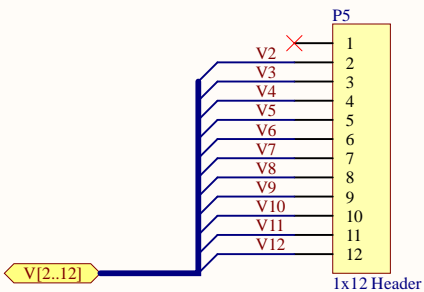
For ISOSPI operations:

- Connect Pin 1 to Pin 5
- Connect Pin 2 to Pin 6

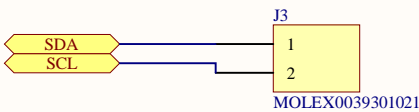
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Date: 12-11-2022	Revision: *	Sheet 2 of 3		
File: LTC2949.SchDoc				

# CONNECTORS

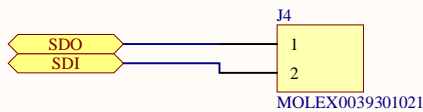
LTC2949 Voltage Inputs and GPIO Pins



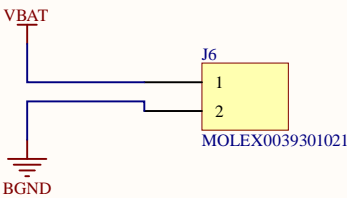
LTC2949 I/O Pins



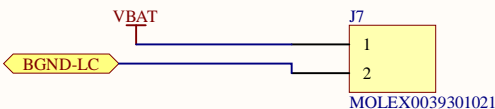
Serial Peripheral Interface



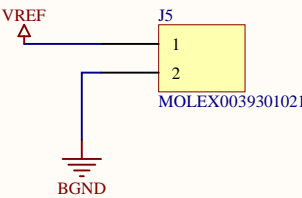
Battery Connector



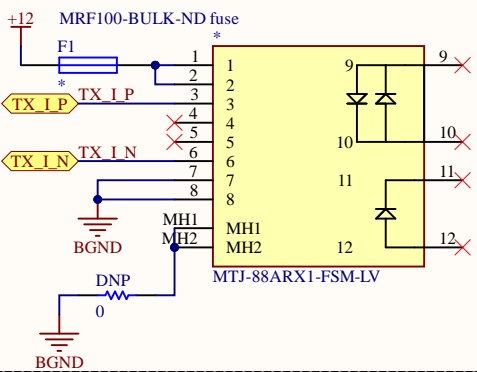
Motor Connector



VREF 3.3V Header



RJ45 Connector



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Size	Number	Revision
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Date:	11-12-2022	Sheet of
File:	C:\Users\...\LTC2949_Connectors.SchDoc	Drawn By:

# Board Stack Report