Due Jan 31 at 11:59pm Points 21 Questions 10 Available Jan 19 at 12am - Feb 2 at 11:59pm Time Limit None

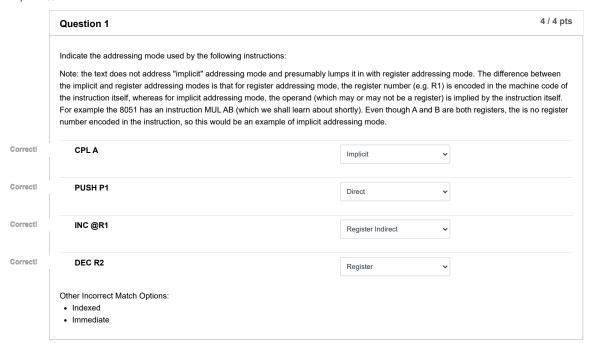
## **Instructions**

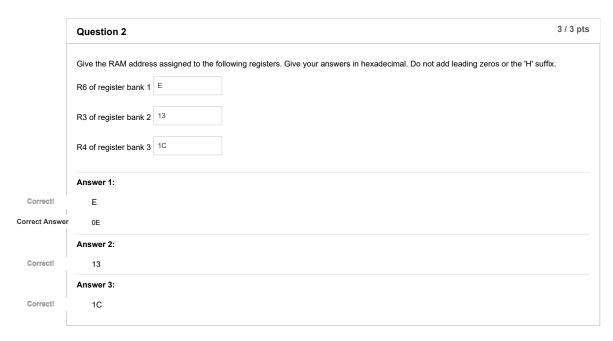
Read Sections 5.1-5.4 then answer the following questions. One try. No time limit.

## Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	33 minutes	21 out of 21

Score for this quiz: **21** out of 21 Submitted Jan 29 at 8:43pm This attempt took 33 minutes.





Question 3	1 / 1 pts
R3 may be used for register indirect addressing mode.	

Correct!    False  Question 4	
Question 4	
Question 4	
	3 / 3 pts
Which of the following SFRs are bit addressable (select all that apply):	
Correct! PSW	
□ TMOD	
Correct! 🛮 IE	
Correct! B B	
Correct! 🖾 ACC	
□ TL0	
□ PCON	
□ DPH	
Correcti  TCON	
TOOK	
SBUF	
SCON	
□ SP	
Question 5	2 / 2 pts
Data RAM addresses in the range 20 to 2F are bit addressable. Give your answering zeros or the 'H' suffix.	vers in hexadecimal without
Answer 1:	
Correcti 20 Answer 2:	
Correct! 2F	
Correcti 2F	
Question 6	3 / 3 pts
Question 6	
Question 6  Indicate the bit address that is altered by the following instructions. Give your answers in hexadecimal without lead	
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Question 6  Indicate the bit address that is altered by the following instructions. Give your answers in hexadecimal without lease the control of the bits in the PSW.	
Question 6  Indicate the bit address that is altered by the following instructions. Give your answers in hexadecimal without less CLR P2.1 A1  SETB ACC.6 E6  CPL OV D2  Note: OV is one of the bits in the PSW.  Answer 1:  Correct!  A1  Answer 2:	
Question 6  Indicate the bit address that is altered by the following instructions. Give your answers in hexadecimal without let CLR P2.1 A1  SETB ACC.6 E6  CPL OV D2  Note: OV is one of the bits in the PSW.  Answer 1:  Correct!  A1  Answer 2:	
Question 6  Indicate the bit address that is altered by the following instructions. Give your answers in hexadecimal without less CLR P2.1 A1  SETB ACC.6 E6  CPL OV D2  Note: OV is one of the bits in the PSW.  Answer 1:  Correct!  A1  Answer 2:	

Question 7  Give the names of the bits that have the (bit) addresses, below.		2 / 2 pts
Sive the names of the bits that have the (bit) addresses, below.		
Bit 0D6H is called AC		
Bit 0D0H is called P		
Answer 1:		
AC		
Answer 2:		
Question 8		1 / 1 pts
Fig. by dda a single Day (to said a bish a)	to F7	
The bit addresses assigned to register B are (lowest to highest)  F0  without leading zeros or the 'H' suffix.	to 17	. Give your answers in hexadecimal
Answer 1: F0		
Answer 2:		
F7		
Question 9		1 / 1 pts
What addressing mode used to access the upper 128 bytes of RAM in the 805.	2?	
O Direct		
○ Immediate		
○ Indexed		
○ Register		
Register Indirect		
		1 / 1 pts
Question 10		
Question 10 On an 8052, the stack can be placed in the upper RAM.		