Quiz 4

Instructions

Read Sections 4.1-4.2 then answer the following questions. One try. No time limit

Attempt History

	Attempt	Time	Score
LATEST	**Attempt 1**	774 minutes	12 out of 12

Score for this quiz: **12** out of 12
Submitted Jan 26 at 11:33am
This attempt took 774 minutes.

Question 1   1 / 1 pts

On the original 8051, which ports have internal pull-up resistors? (select all that apply)

☐ Port 0

Correct!   ☑ Port 1

Correct!   ☑ Port 3

Correct!   ☑ Port 2

Question 2   1 / 1 pts

A port on the 8051 must be written with a _____ to make it into an input.

○ 0

Correct!   ◉ 1

Question 3   1 / 1 pts

Which of the ports below are bit-addressable?

Correct!   ☑ P2

Correct!   ☑ P1

Correct!   ☑ P3

Correct!   ☑ P0

Question 4   1 / 1 pts

Upon reset, the ports on an (original) 8051 are:

Correct!   ◉ set to 11111111

○ set to 00000000

○ set to a high-impedance state

○ undefined

Question 5

1 / 1 pts

Confusion about which instructions read the port latch and which read the input port is a major source of errors in 8051 programming.

- ⦿ True
- ○ False

Question 6

1 / 1 pts

Read-modify-write instructions (when applied to I/O ports) read (and may modify) the port latch. For these instructions, the actual input on the port pins is irrelevant.

- ⦿ True
- ○ False

Question 7

3 / 3 pts

Which of the following instructions are read-modify-write instructions?

- ☐ mov C,P1.0

- ☑ orl P3,#4

- ☑ setb P1.2

- ☐ mov A,P2

- ☐ mov P2,#3FH

- ☑ jbc P3.1,there

Question 8

3 / 3 pts

Match the bit instructions, below, to byte instructions that perform exactly the same function.

setb P1.2 | orl P1,#4   ▾ |

clr P1.2 | anl P1,#0FBH   ▾ |

cpl P1.2 | xrl P1,#4   ▾ |

Other Incorrect Match Options:

- anl P1,#4
- xrl P1,#0FBH
- orl P1,#0FBH

Quiz Score: **12** out of 12