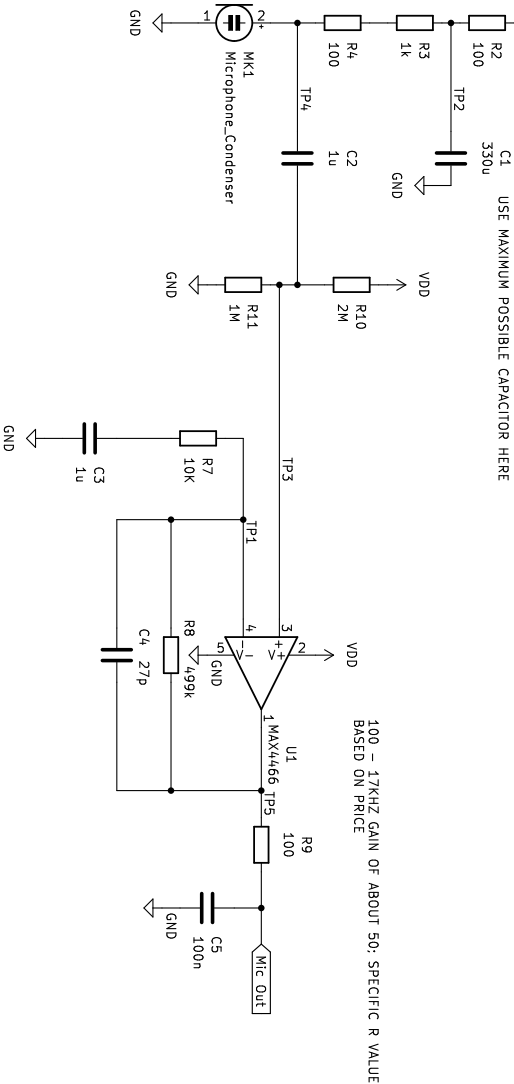


H1 MountingHole H2 MountingHole H3 MountingHole H4 MountingHole

J1 Conn_01x01_Pin J2 Conn_01x01_Pin J3 Conn_01x01_Pin J4 Conn_01x01_Pin J5 Conn_01x01_Pin
TP1 TP2 TP3 TP4 TP5

J6 Screw_Terminal_01x03
VDD GND Mic Out

VDD = 5V AS PER FPGA POWER REQ



***** REVISION TABLE *****	
DATE	AUTHOR
12/29/2023	EN
1/20/2024	EN
2/22/2024	EN
2/22/2024	EN

AUTHOR: EVAN NEWELL
LWD

Sheet: /
File: Microphone.kicad_sch

Title: MICROPHONE SCHEMATIC

Size: A4 Date:

KiCad E.D.A. eeschema 7.0.8

Rev: D
Id: 1/1