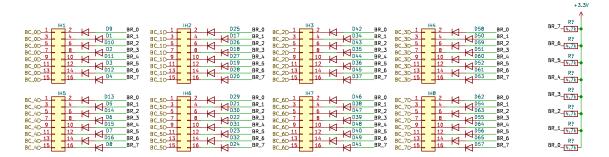
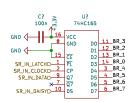
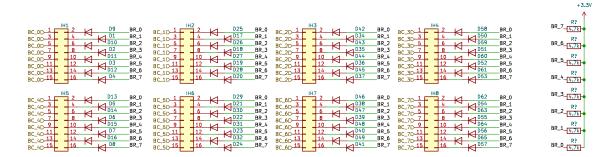
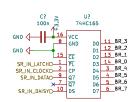


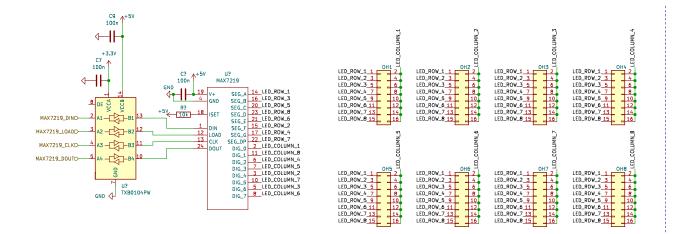
| Rows 0-7 | | SR_IN_CLOCK | SR_IN_LATCH | S











Multiplexer 1

MUX_SO — GMUX_SO
MUX_S1 — GMUX_S1
MUX_S2 — GMUX_S2
MUX_S2 — GMUX_S2
MUX_S3 — GMUX_S3

Multiplexer 2

MUX_COMMON_2
MUX_50 — QMUX_50
MUX_51 — QMUX_51
MUX_52 — QMUX_52
MUX_53 — QMUX_53

Multiplexer 3

Multiplexer 4

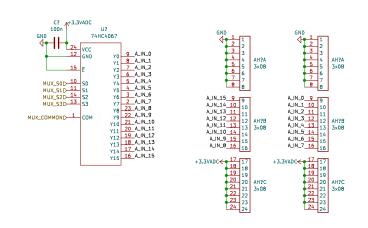
MUX_COMMON — AMUX_COMMON_4

MUX_SO — AMUX_SO

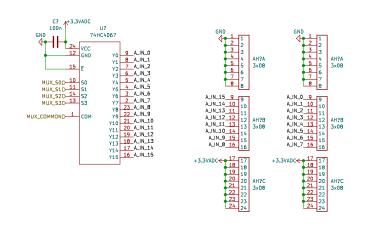
MUX_S1 — AMUX_S1

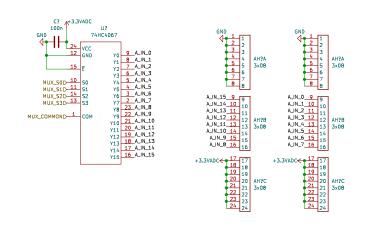
MUX_S2 — AMUX_S2

MUX_S3 — AMUX_S3

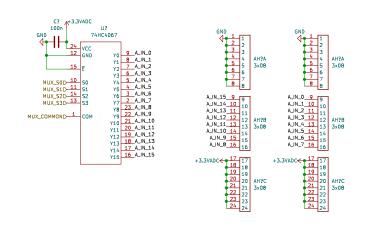


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