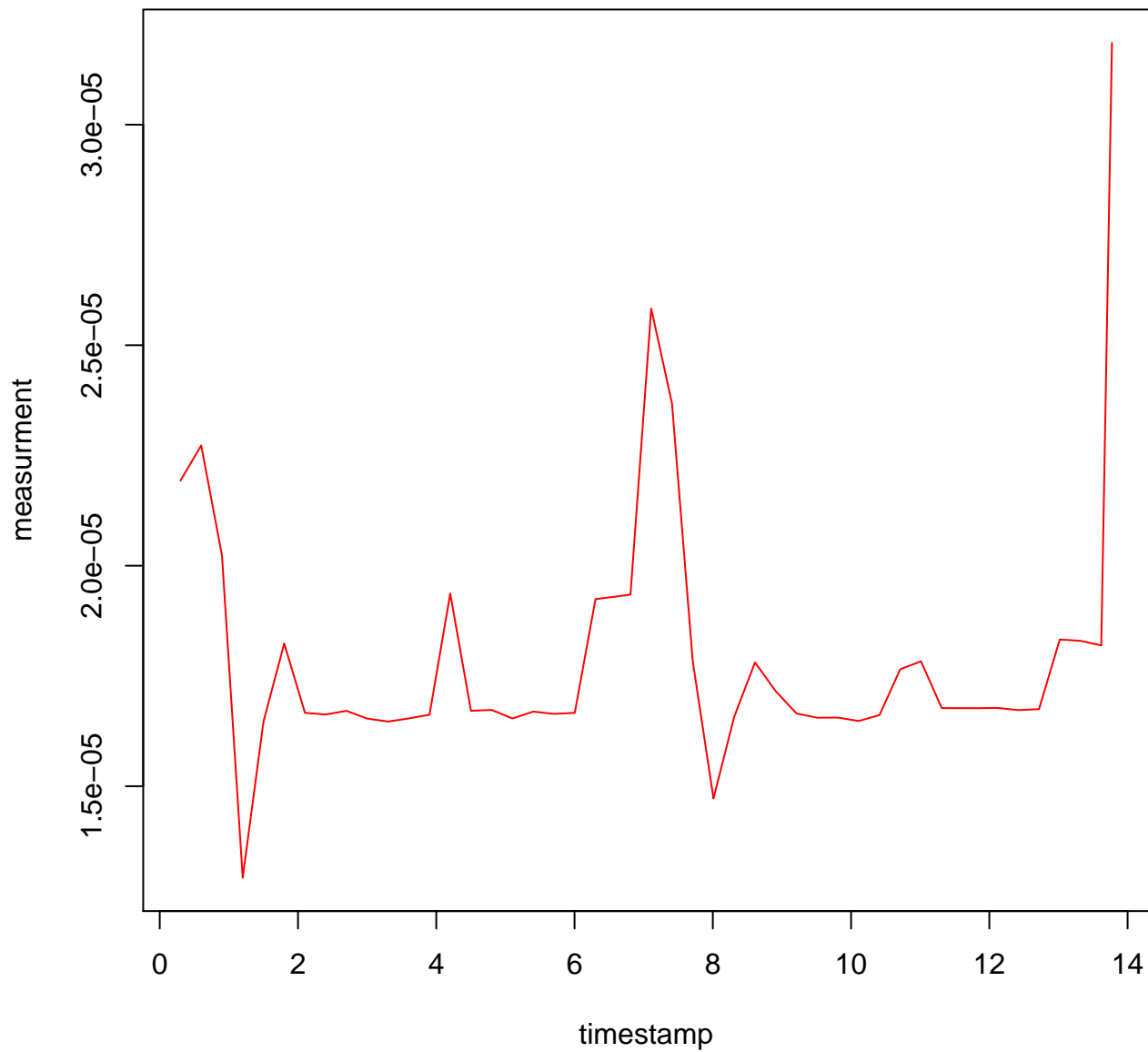**1T4 L1MKI**

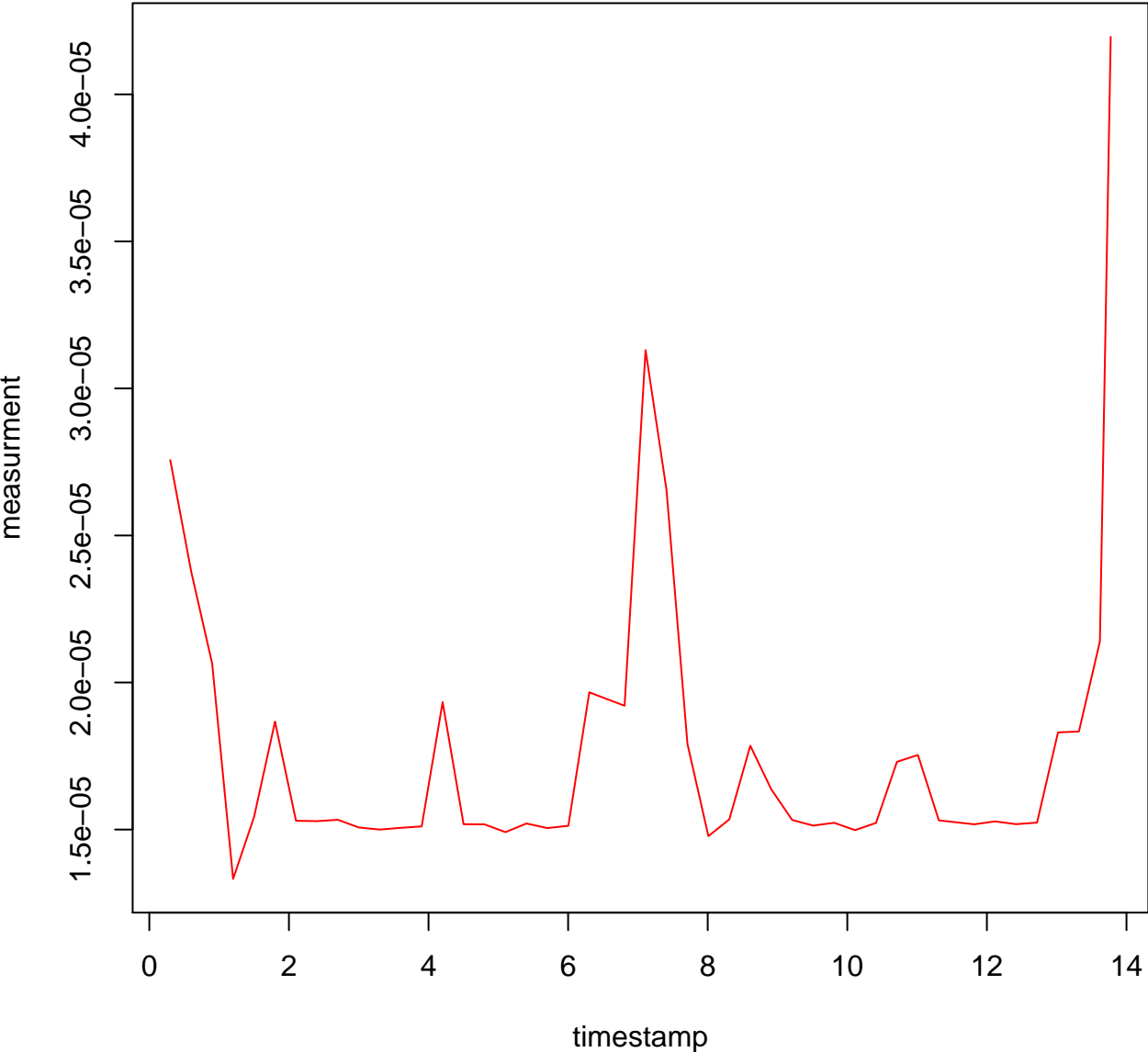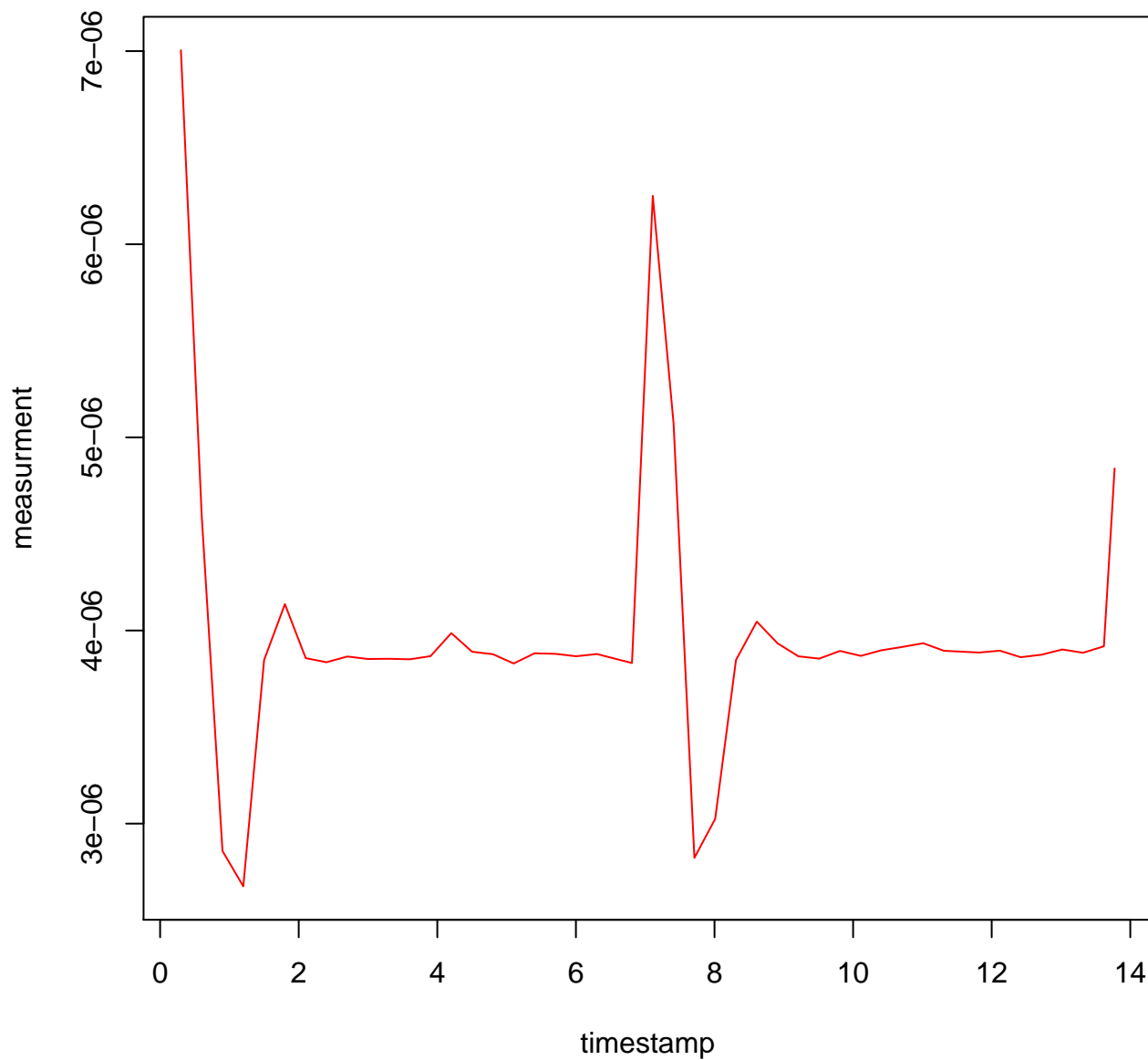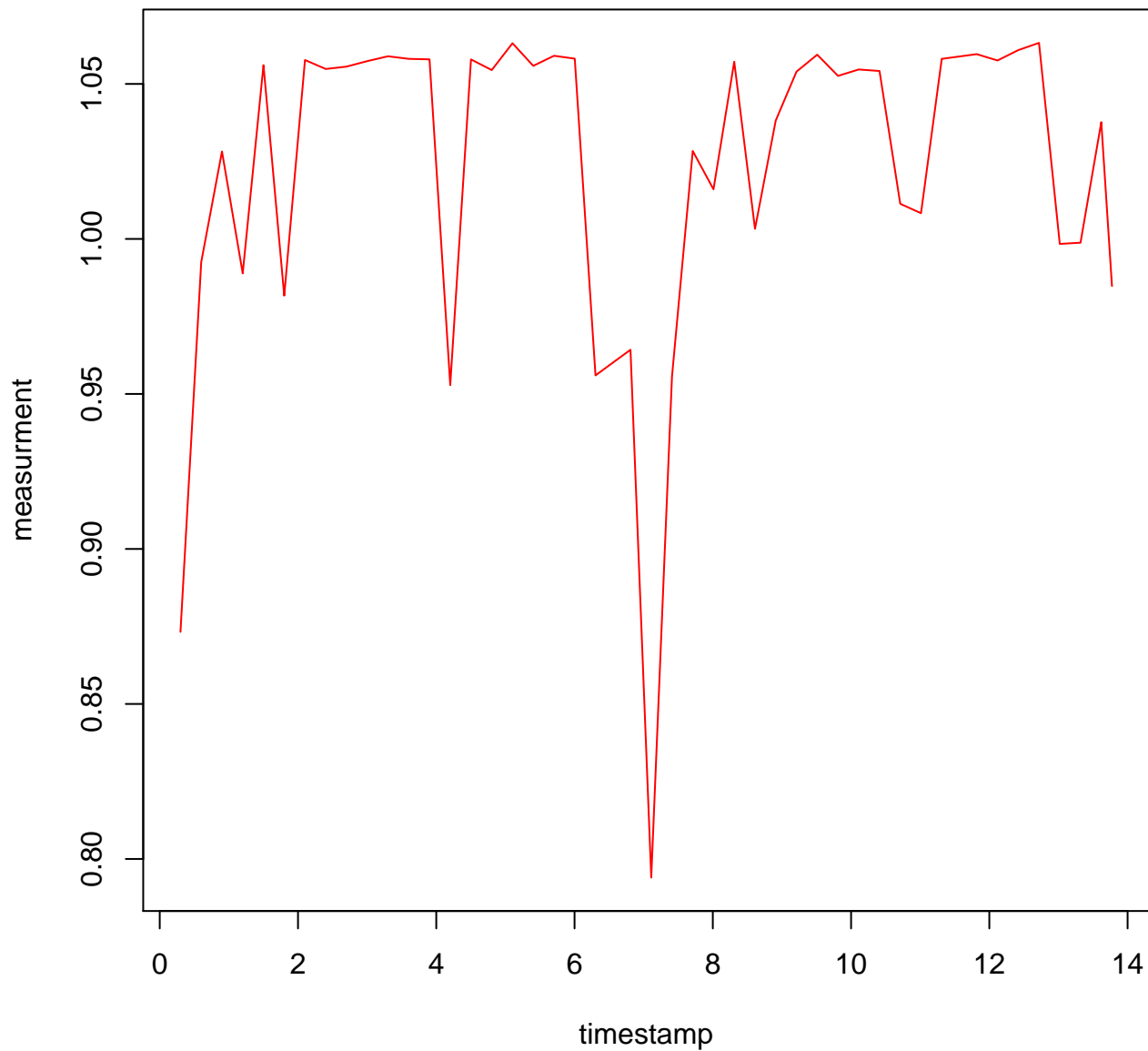**thread8:1T4 inst L2MKI**

thread8:1T4 inst branchMKI

thread8:1T4 inst IPC

thread8:1T4 inst L3MKI