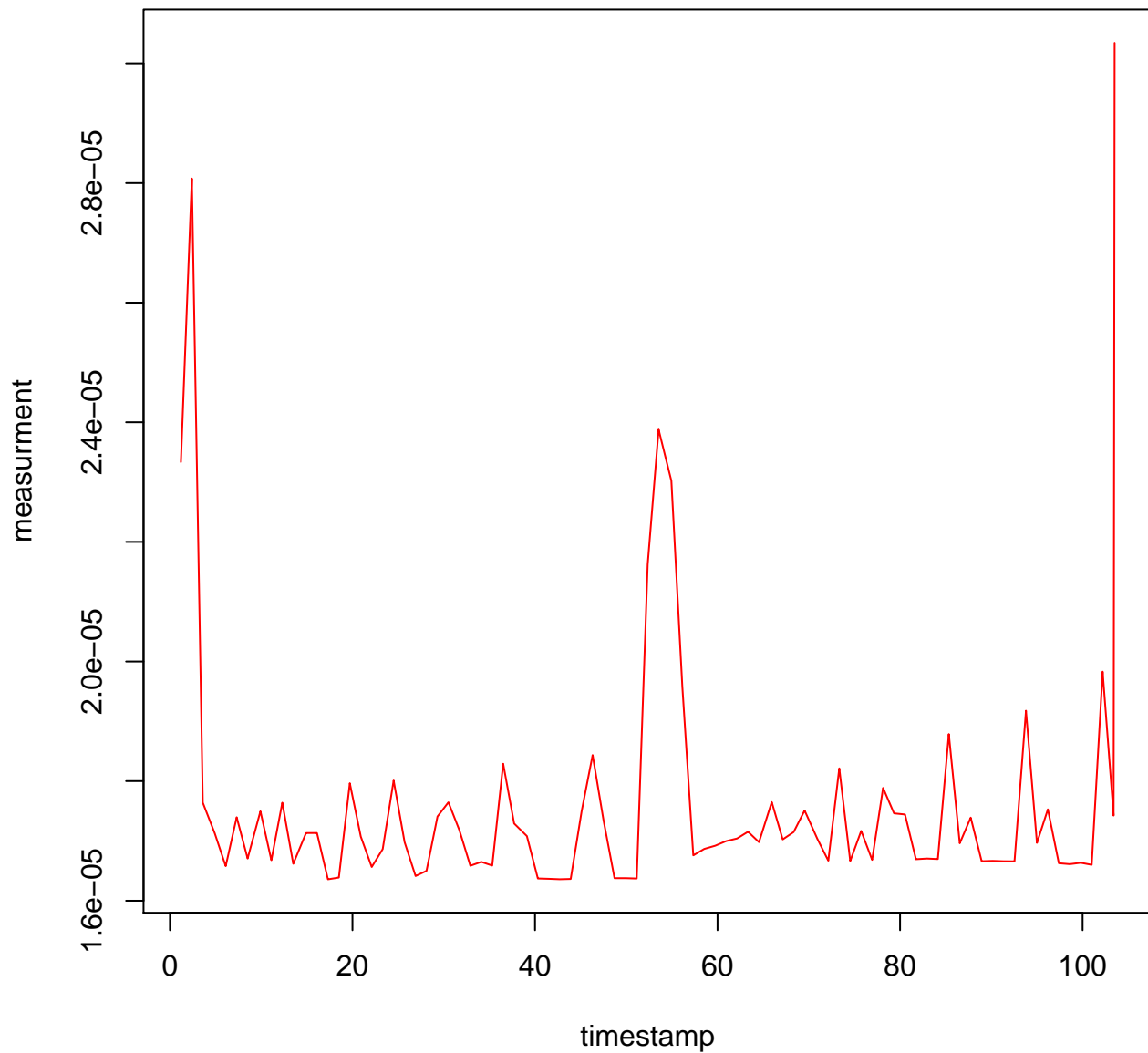
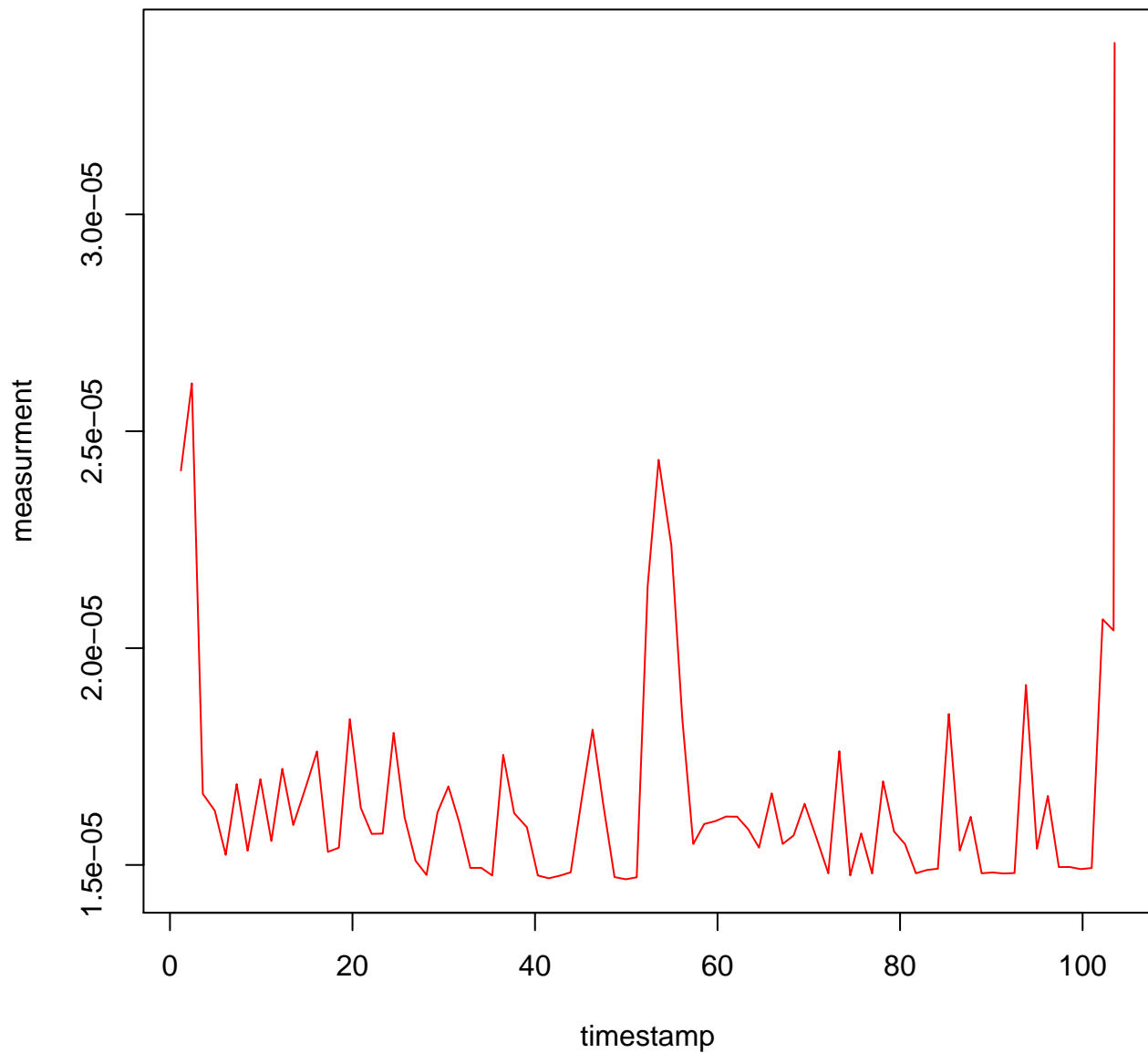


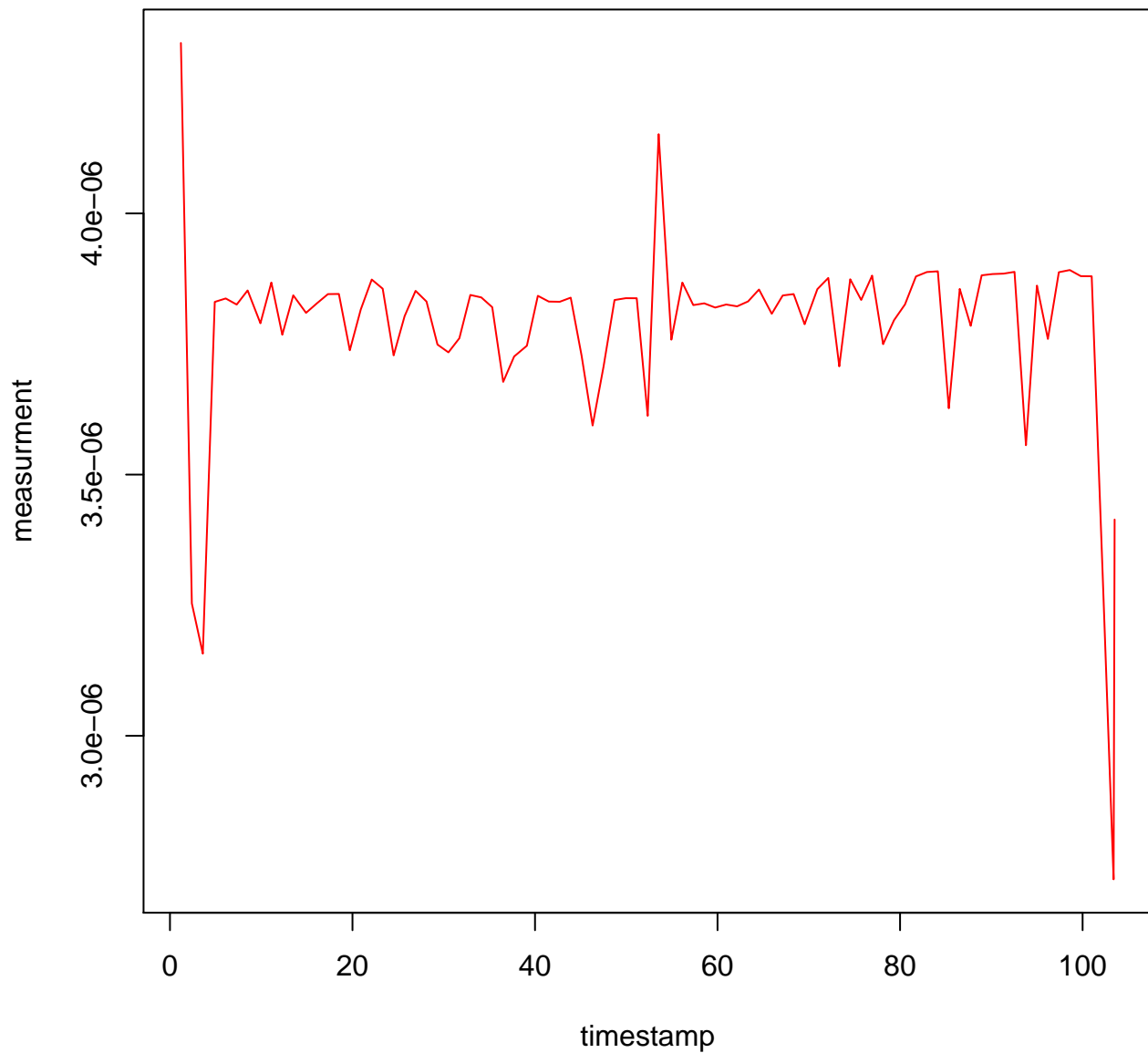
# thread4:1T L1MKI



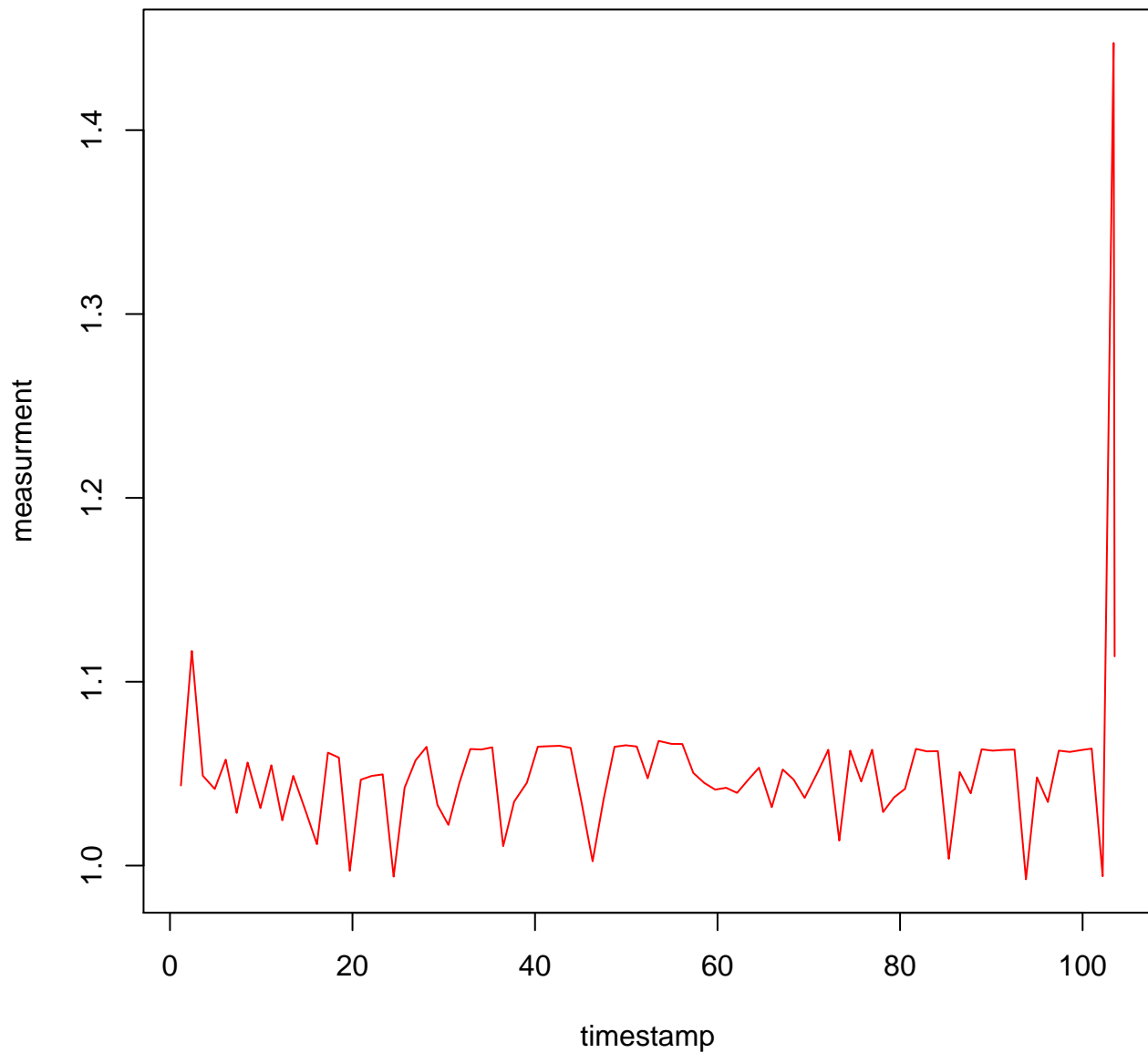
thread4:1T inst L2MKI



**thread4:1T inst branchMKI**



**thread4:1T inst IPC**



thread4:1T inst L3MKI

