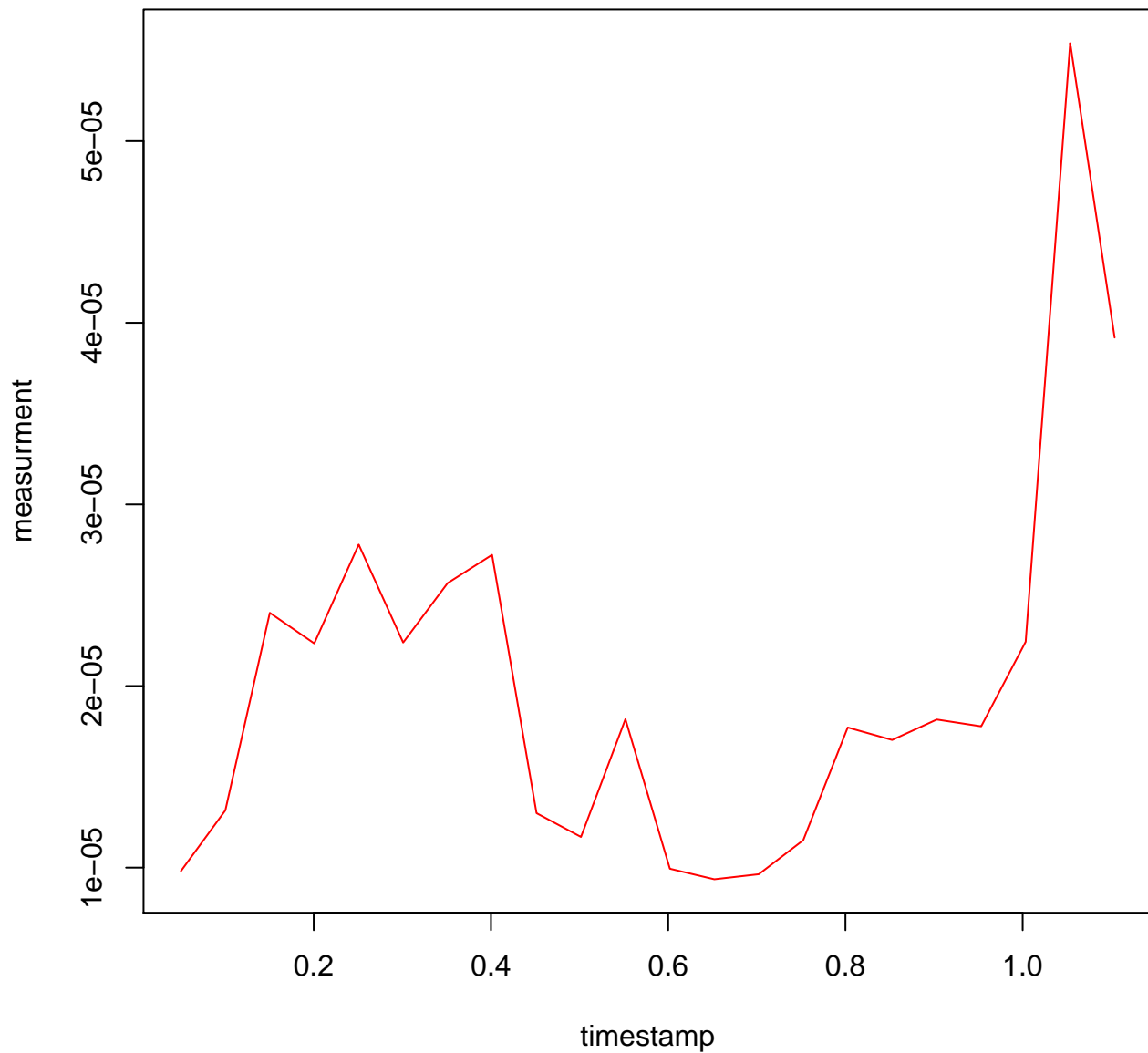
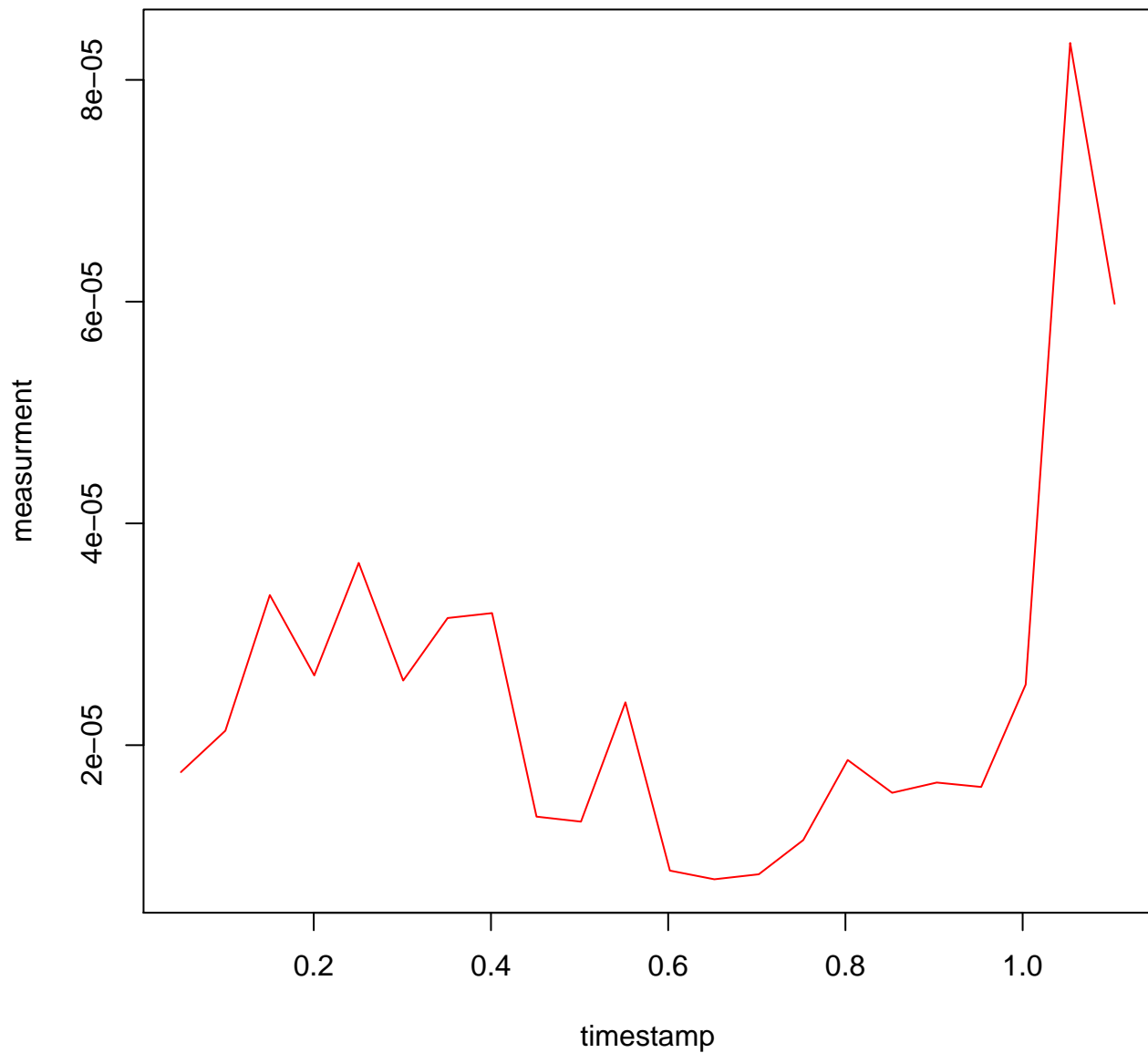


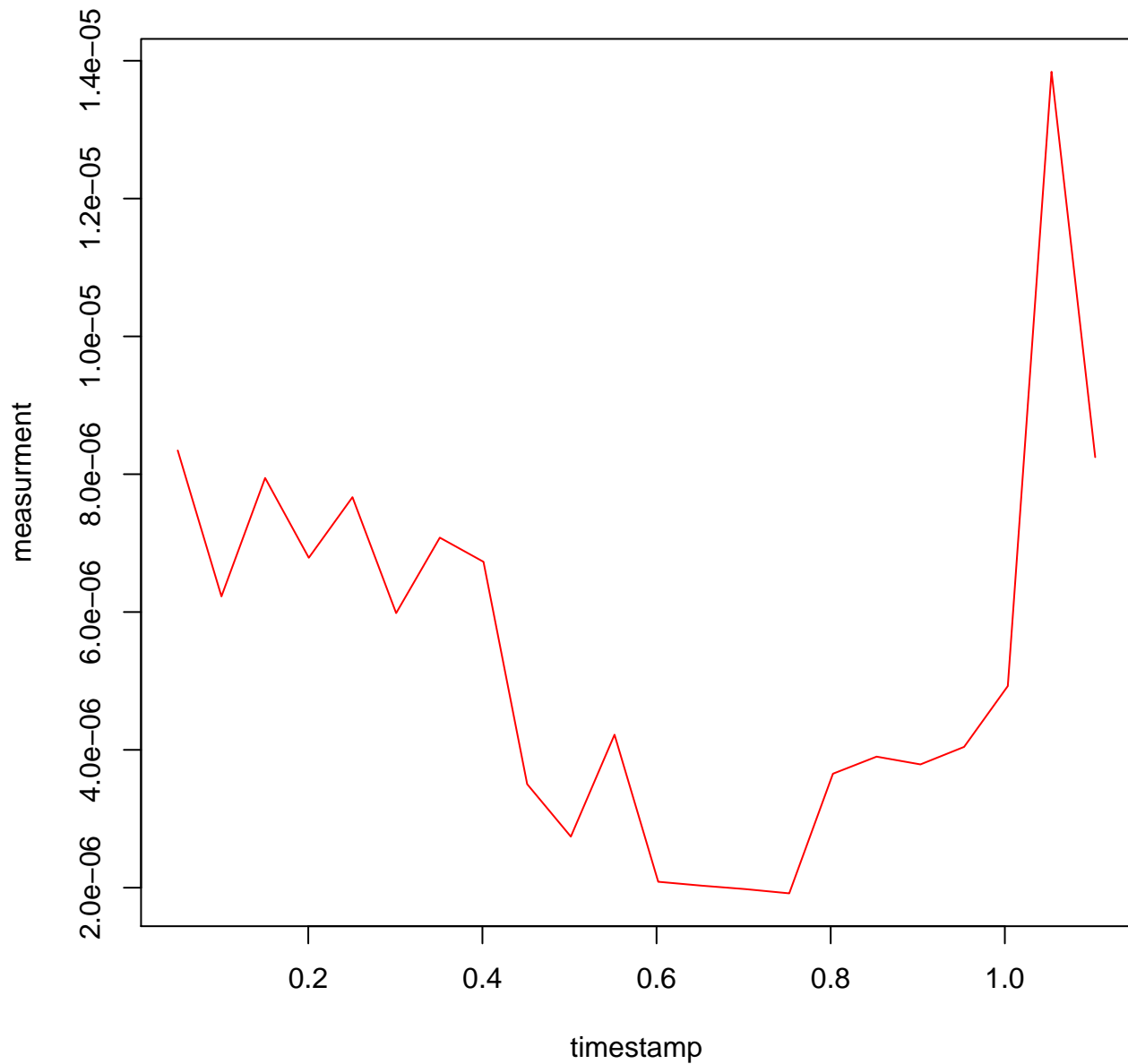
**thread4:1T256 L1MKI**



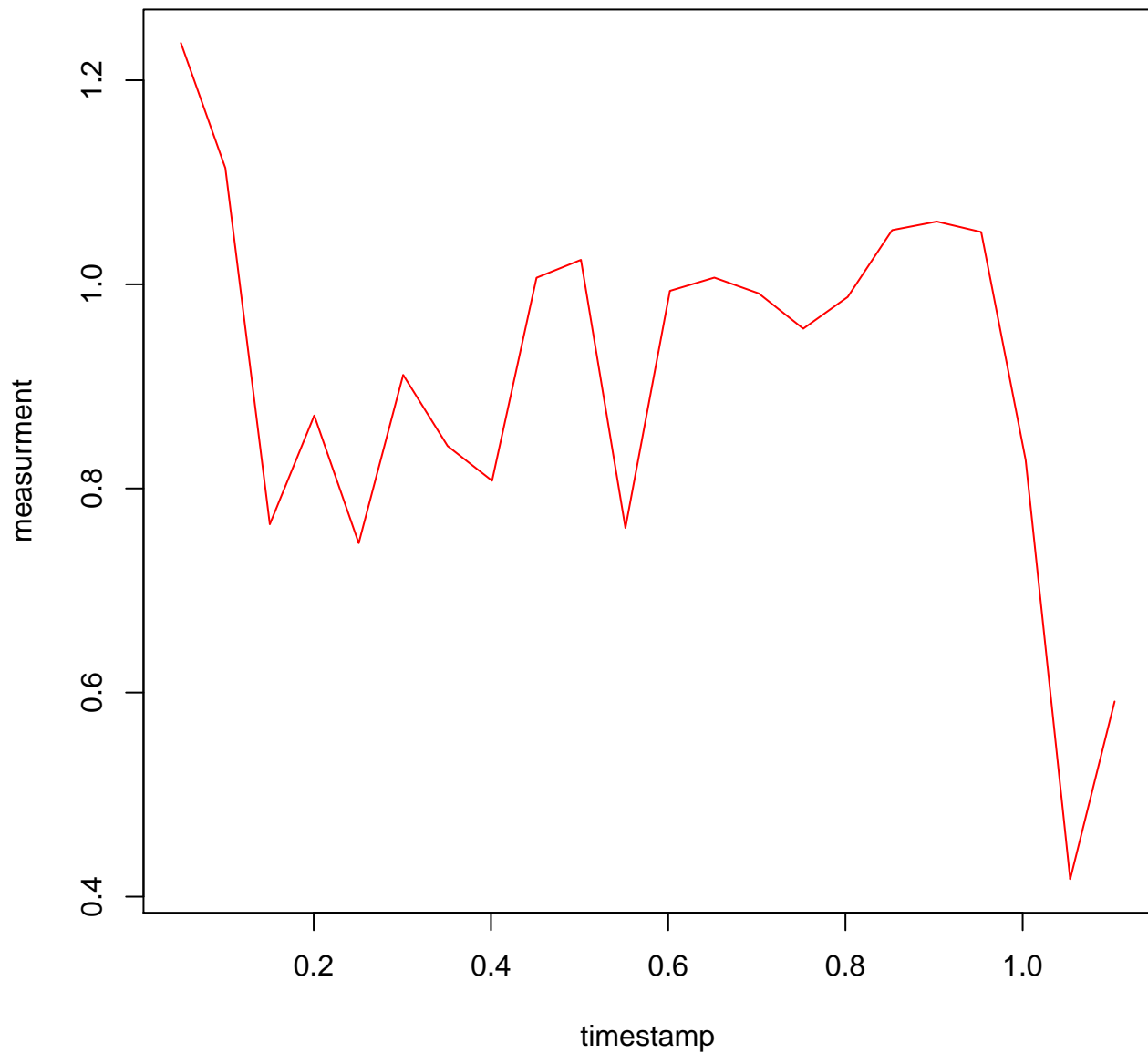
**thread4:1T256 inst L2MKI**



# thread4:1T256 inst branchMKI



**thread4:1T256 inst IPC**



# thread4:1T256 inst L3MKI

