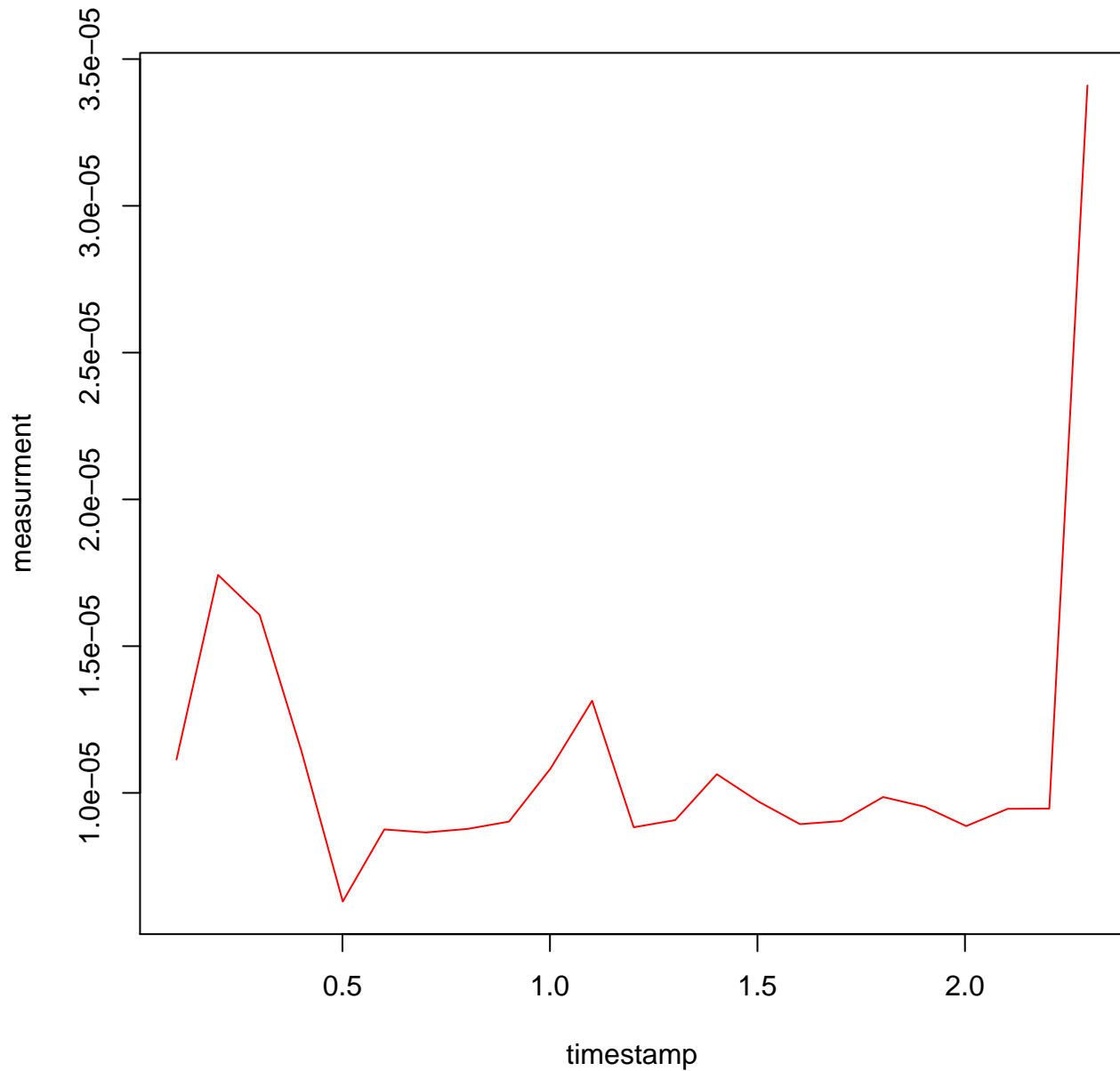
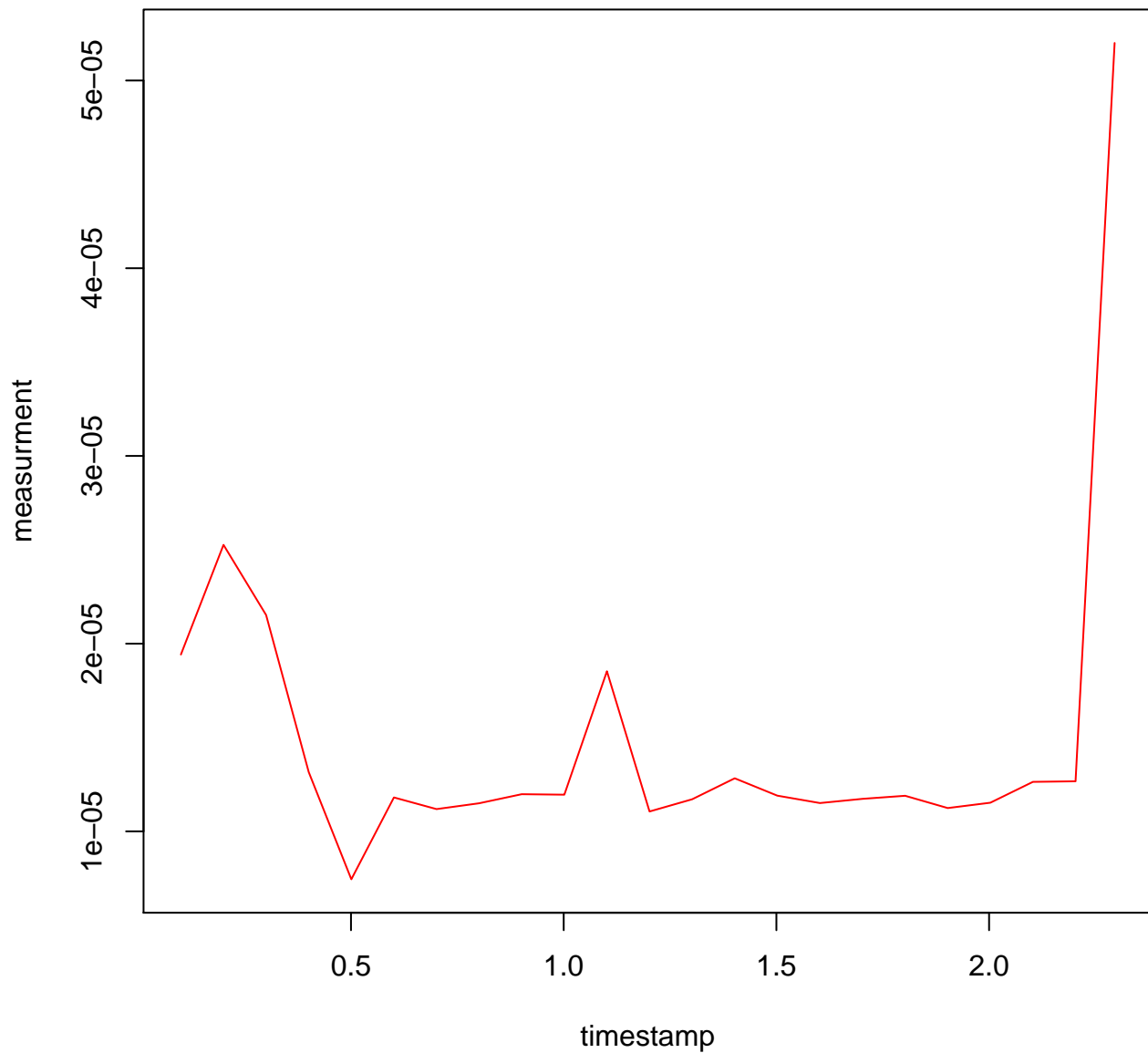


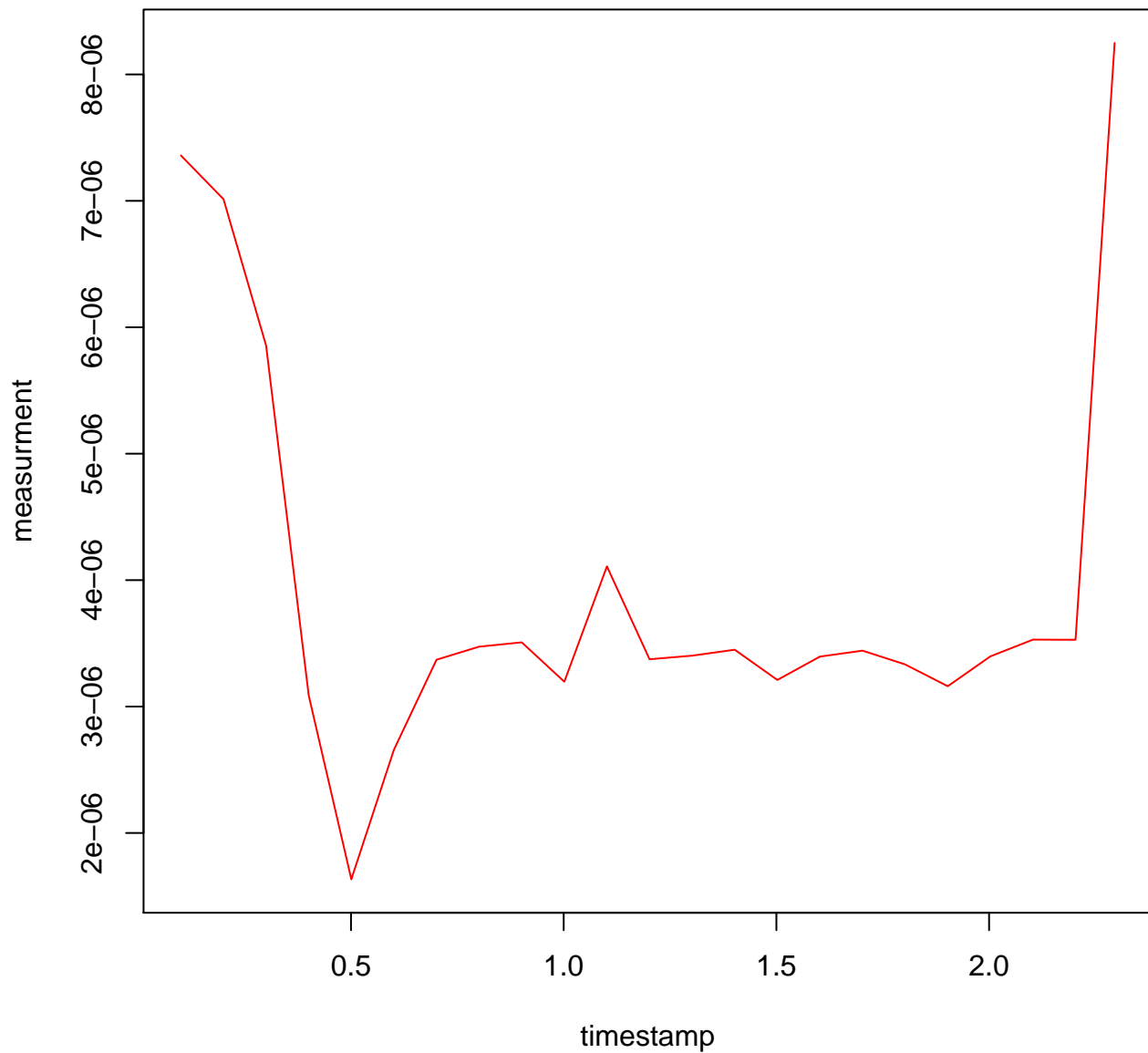
thread2:1T64 inst L1MKI



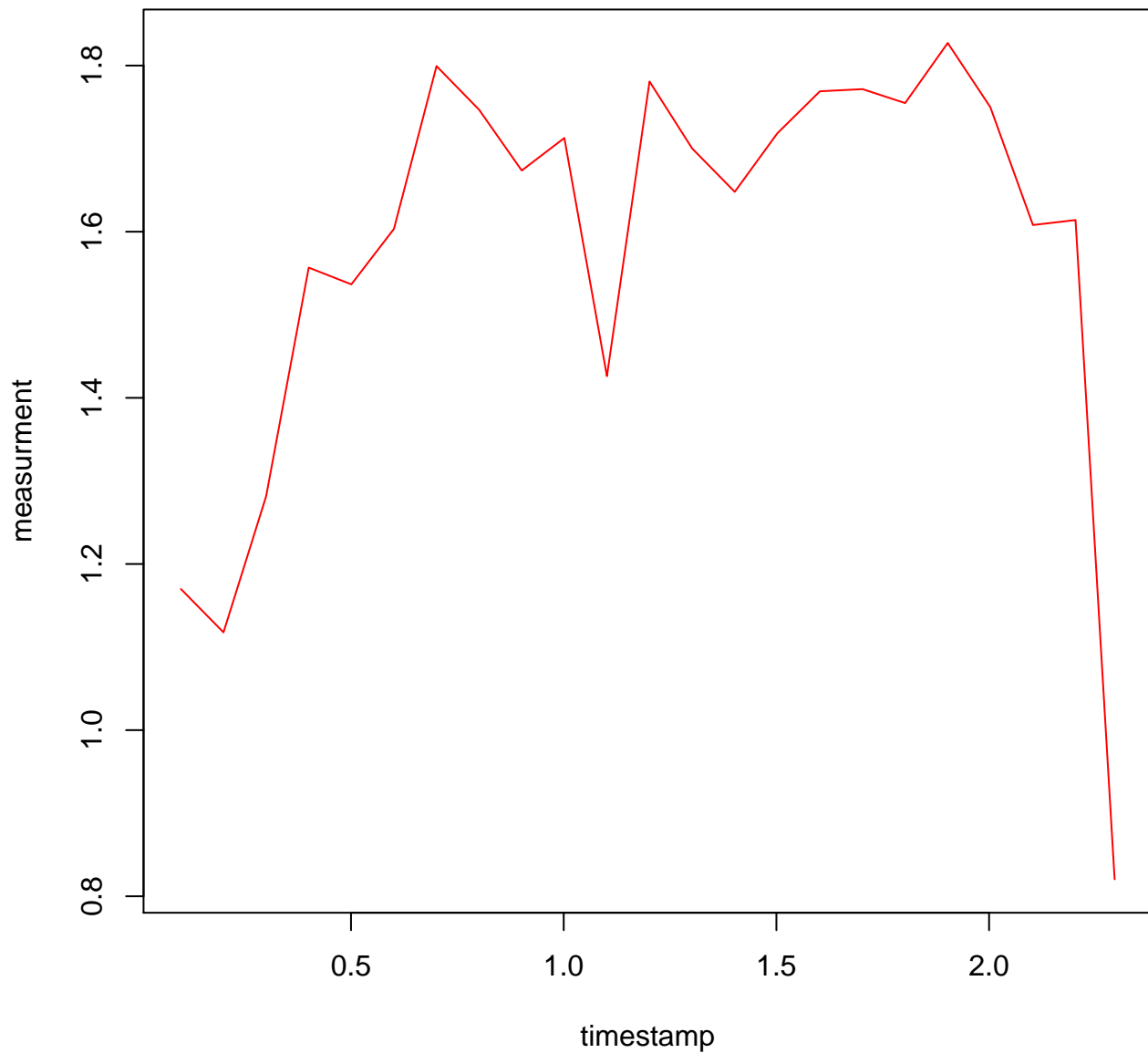
thread2:1T64 inst L2MKI



thread2:1T64 inst branchMKI



thread2:1T64 inst IPC



thread2:1T64 inst L3MKI

