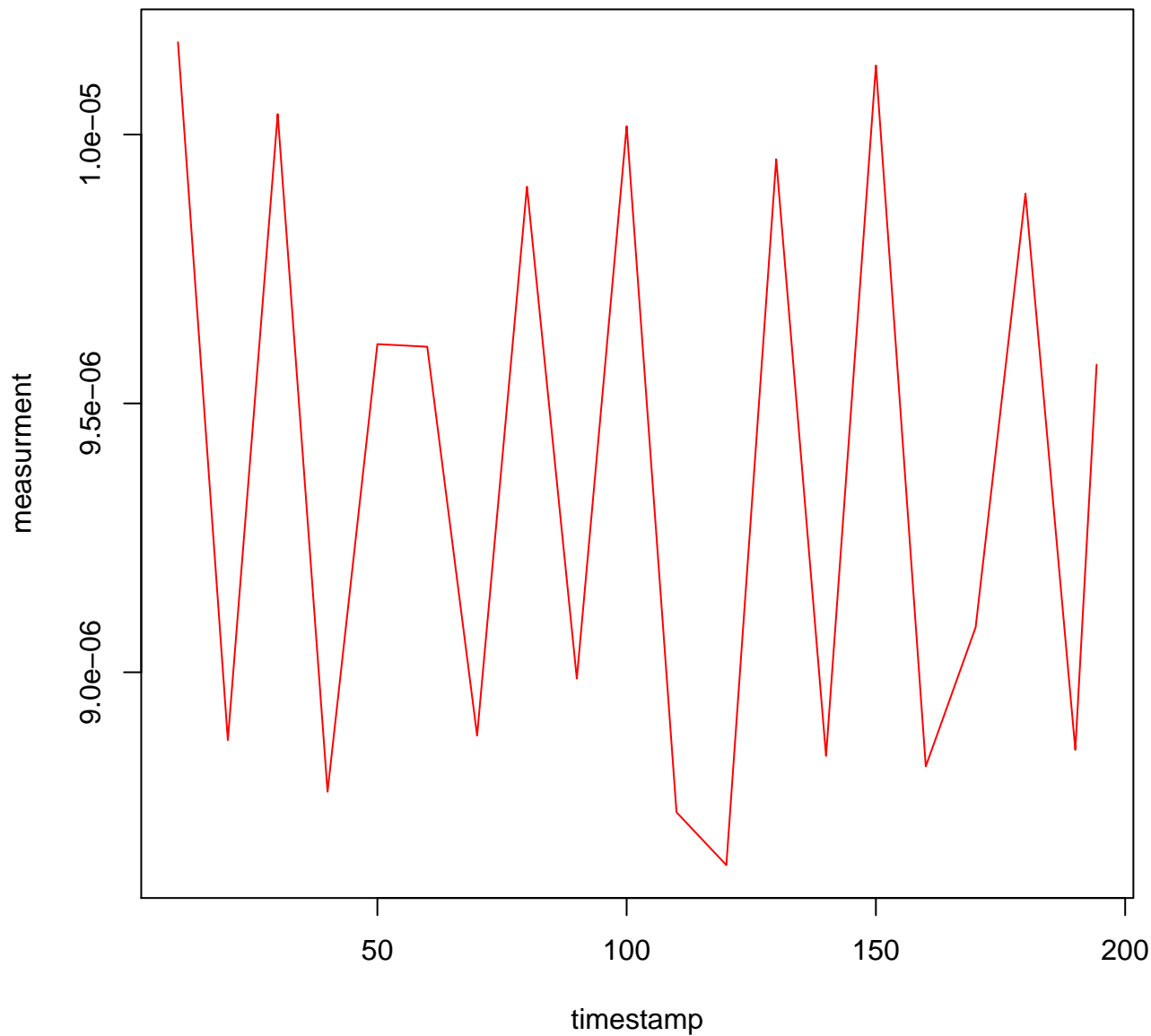
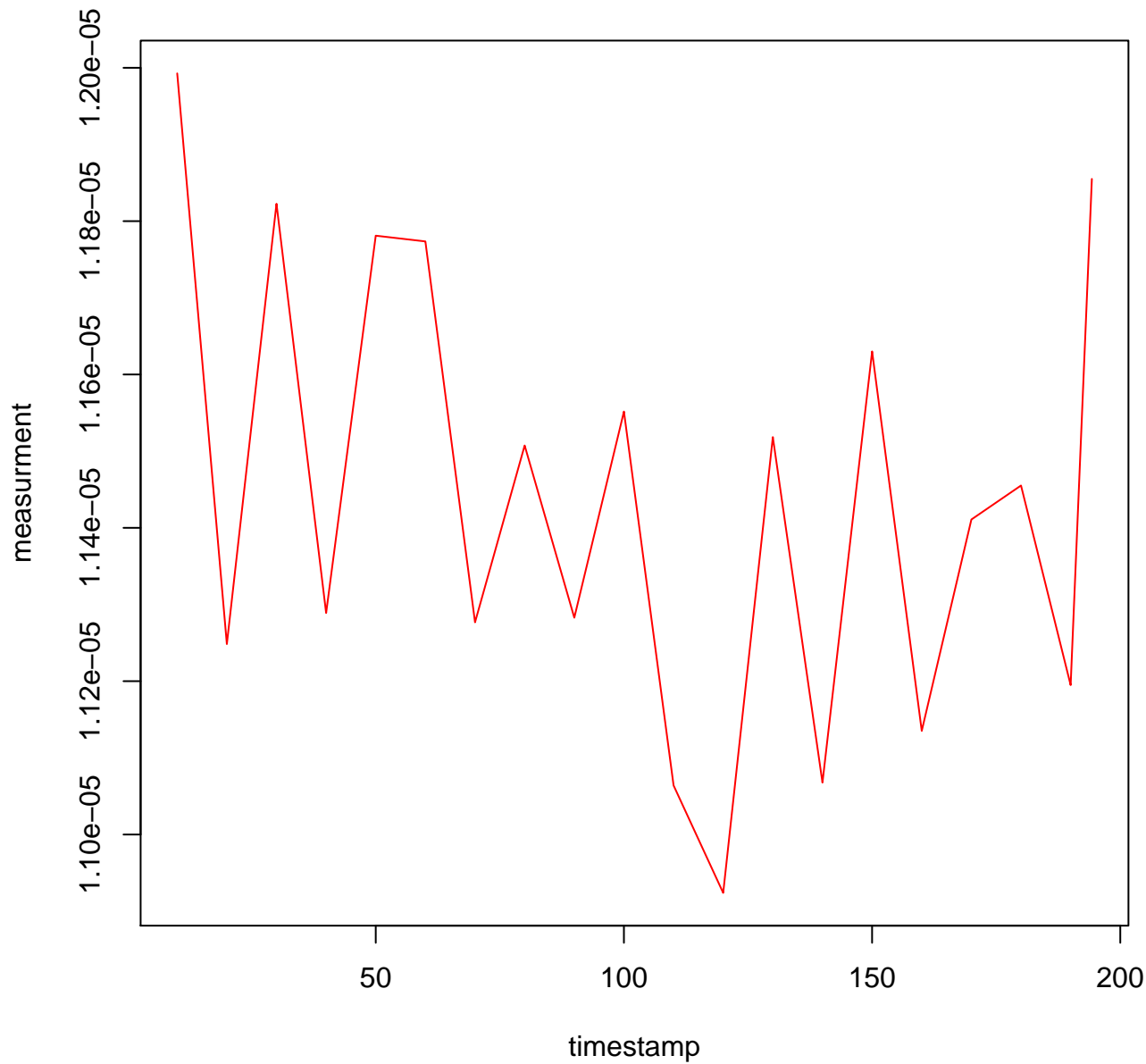


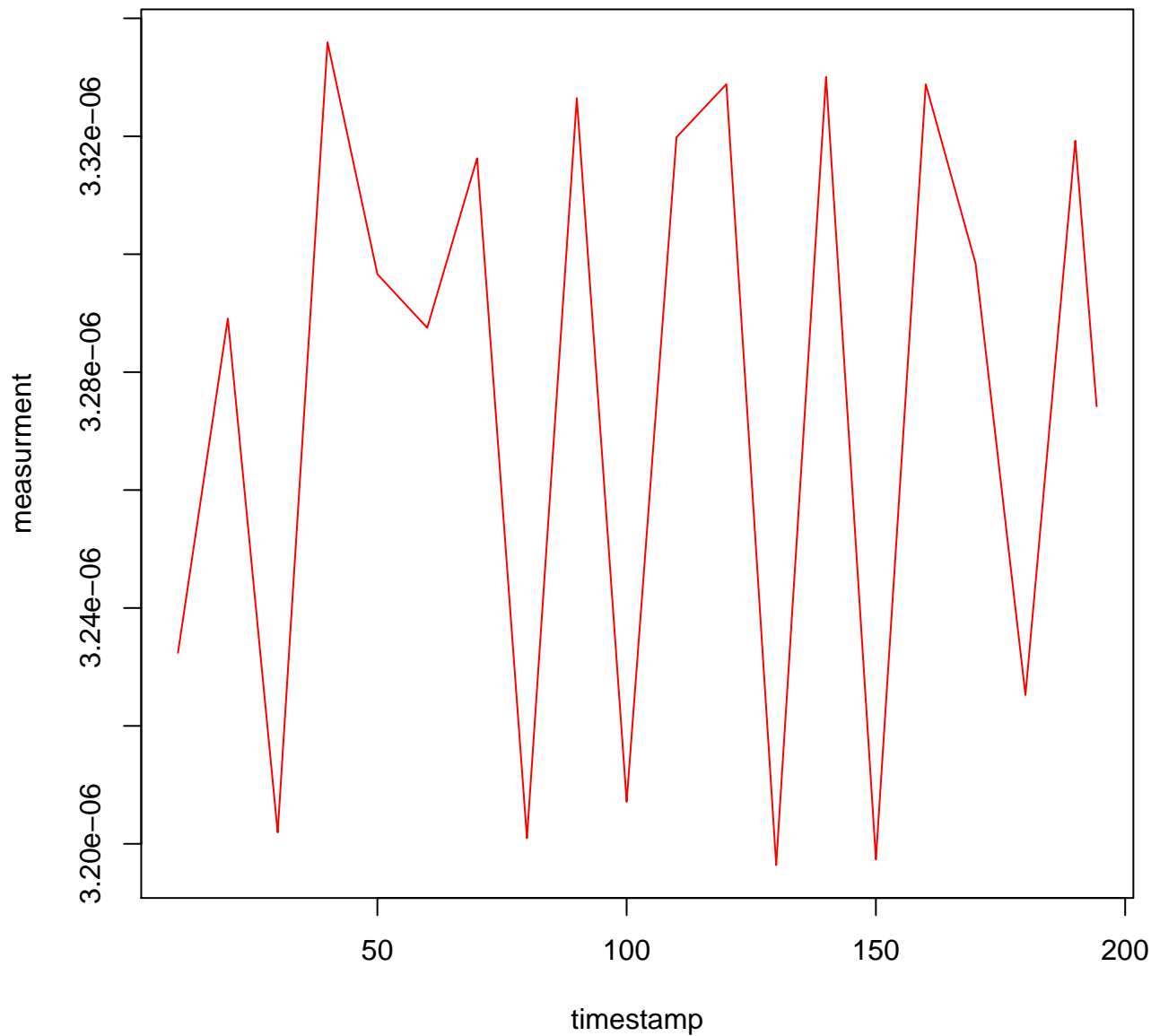
thread1: 1T inst L1MKI



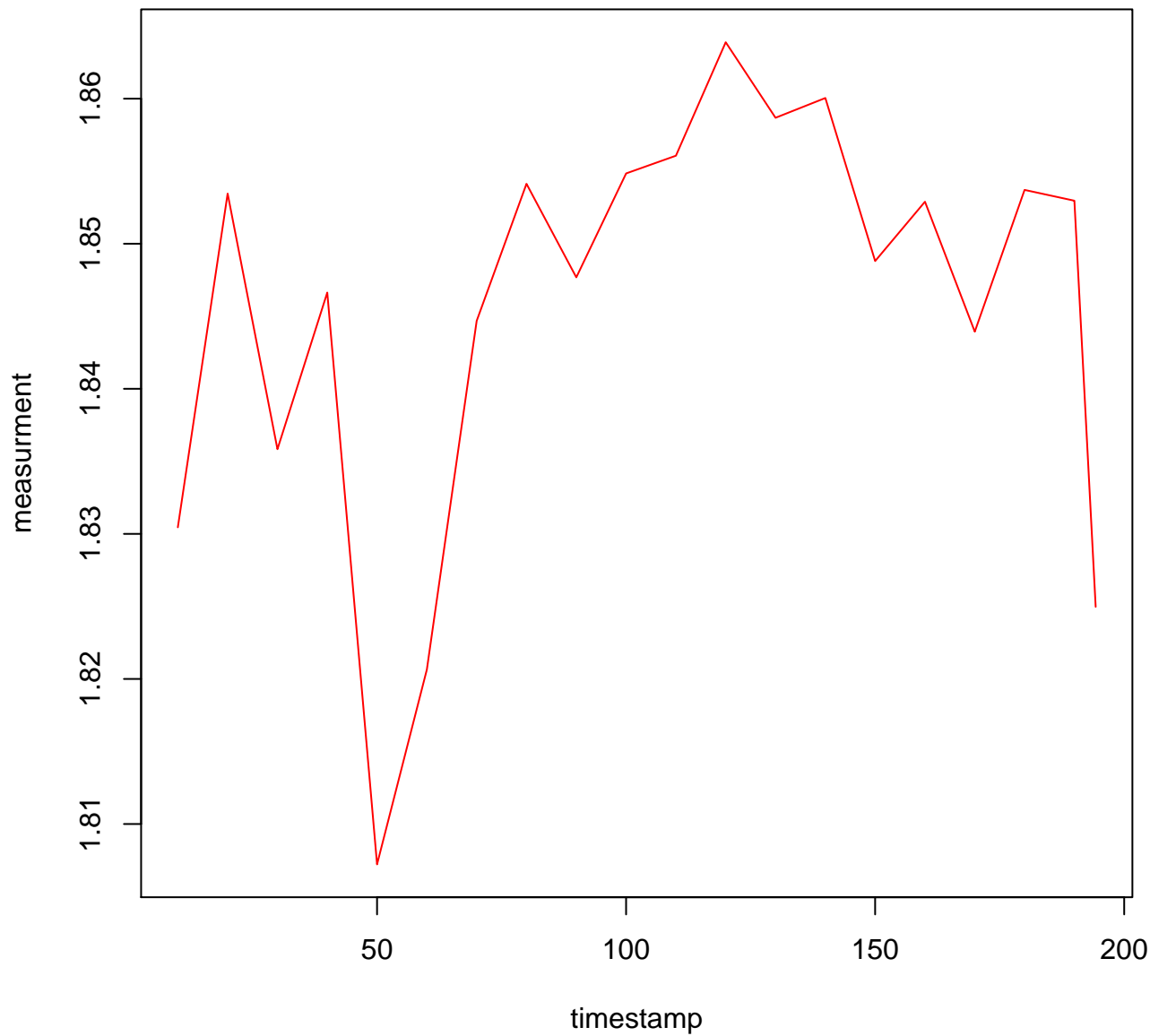
thread1:1T inst L2MKI



thread1:1T inst branchMKI



thread1:1T inst IPC



thread1:1T inst L3MKI

