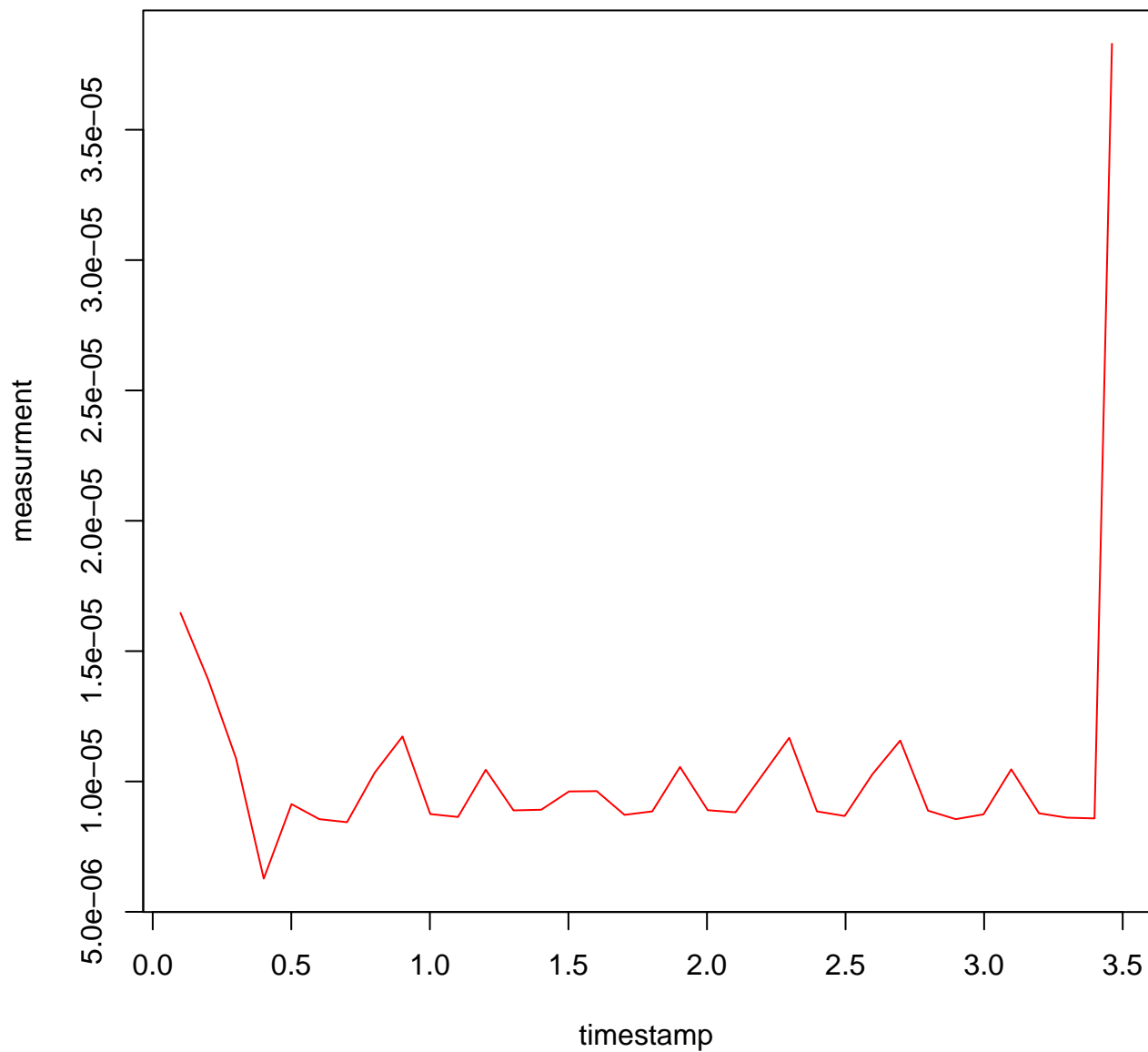
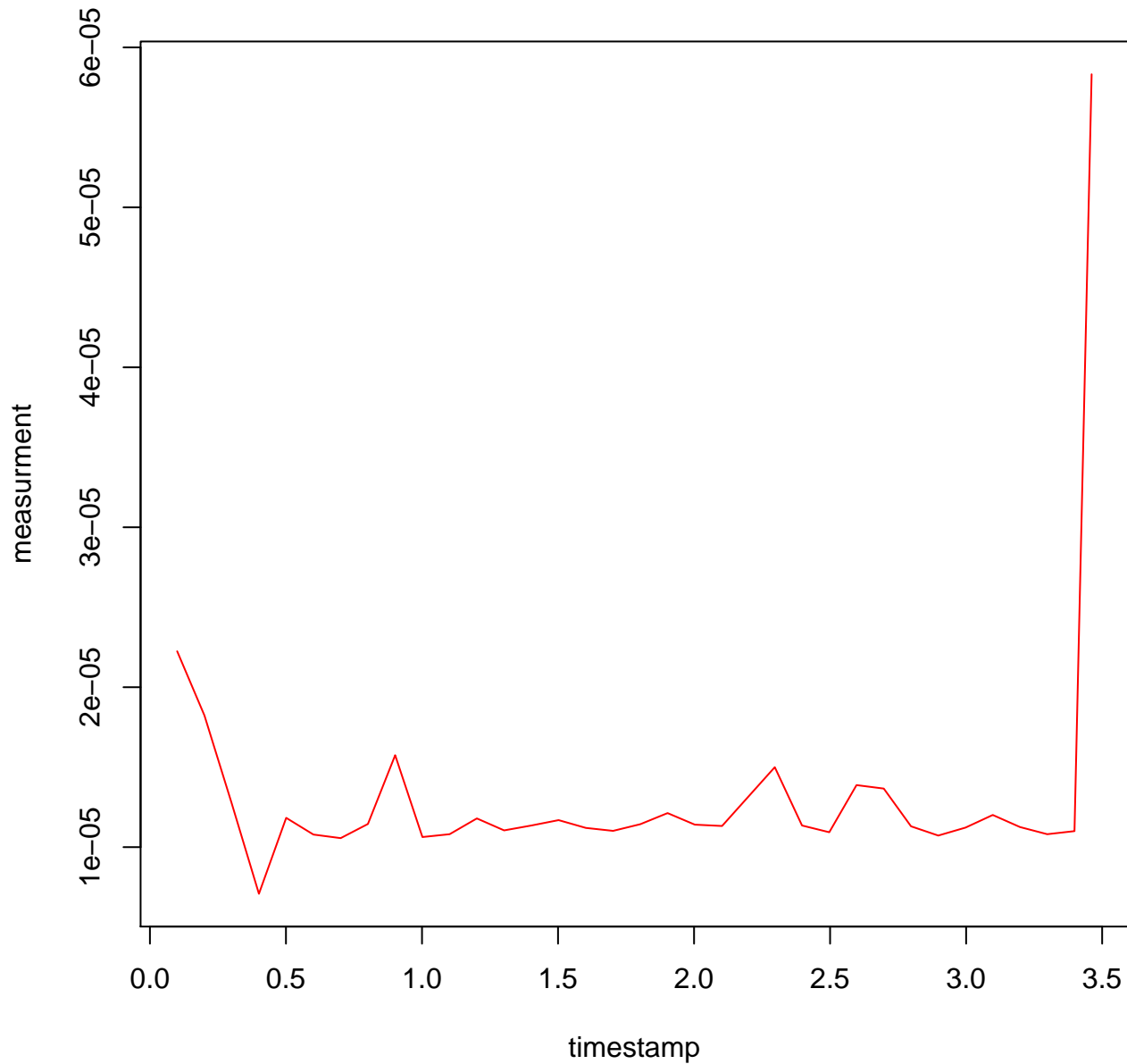


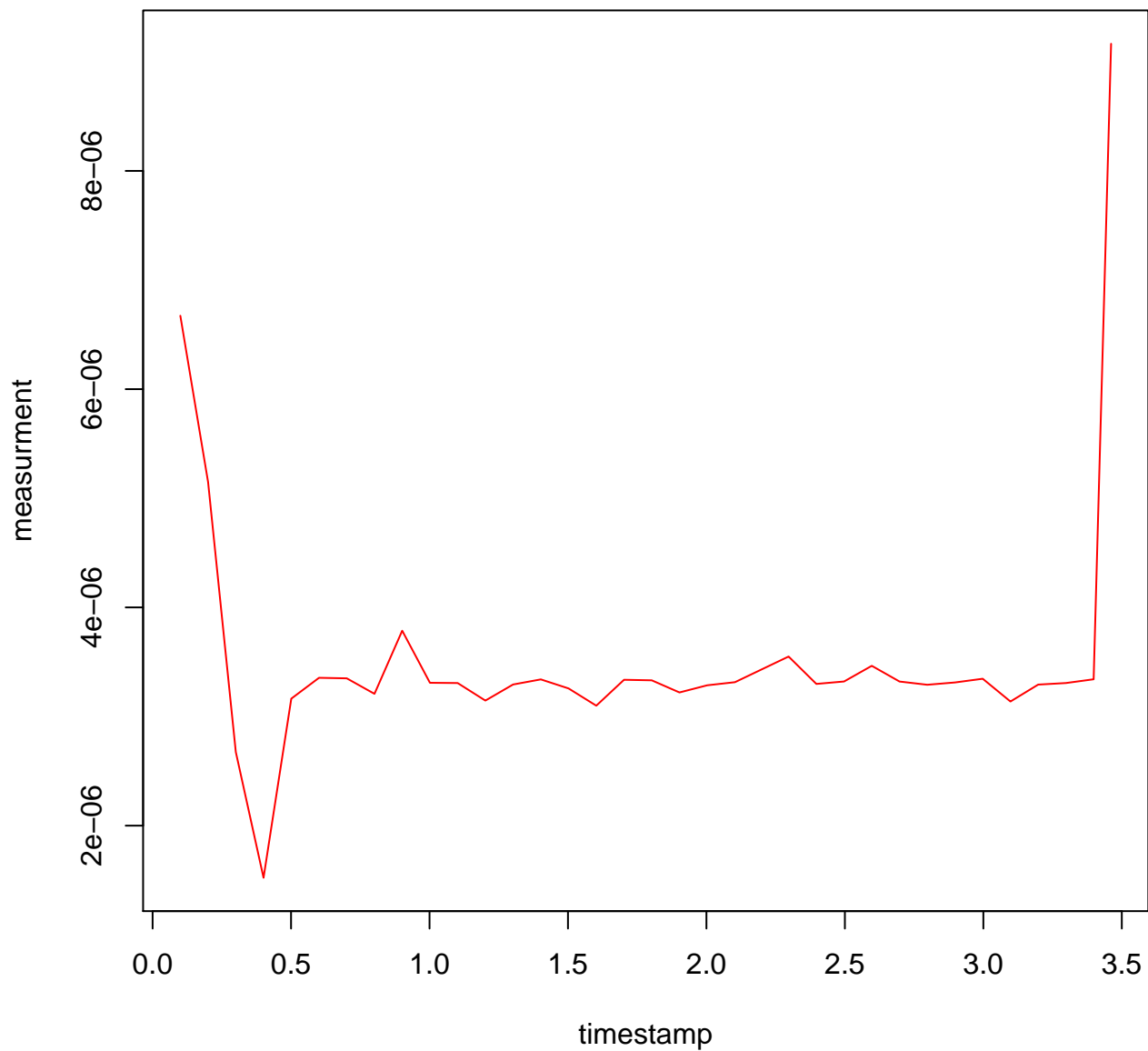
thread1: 1T/64 inst L1MKI



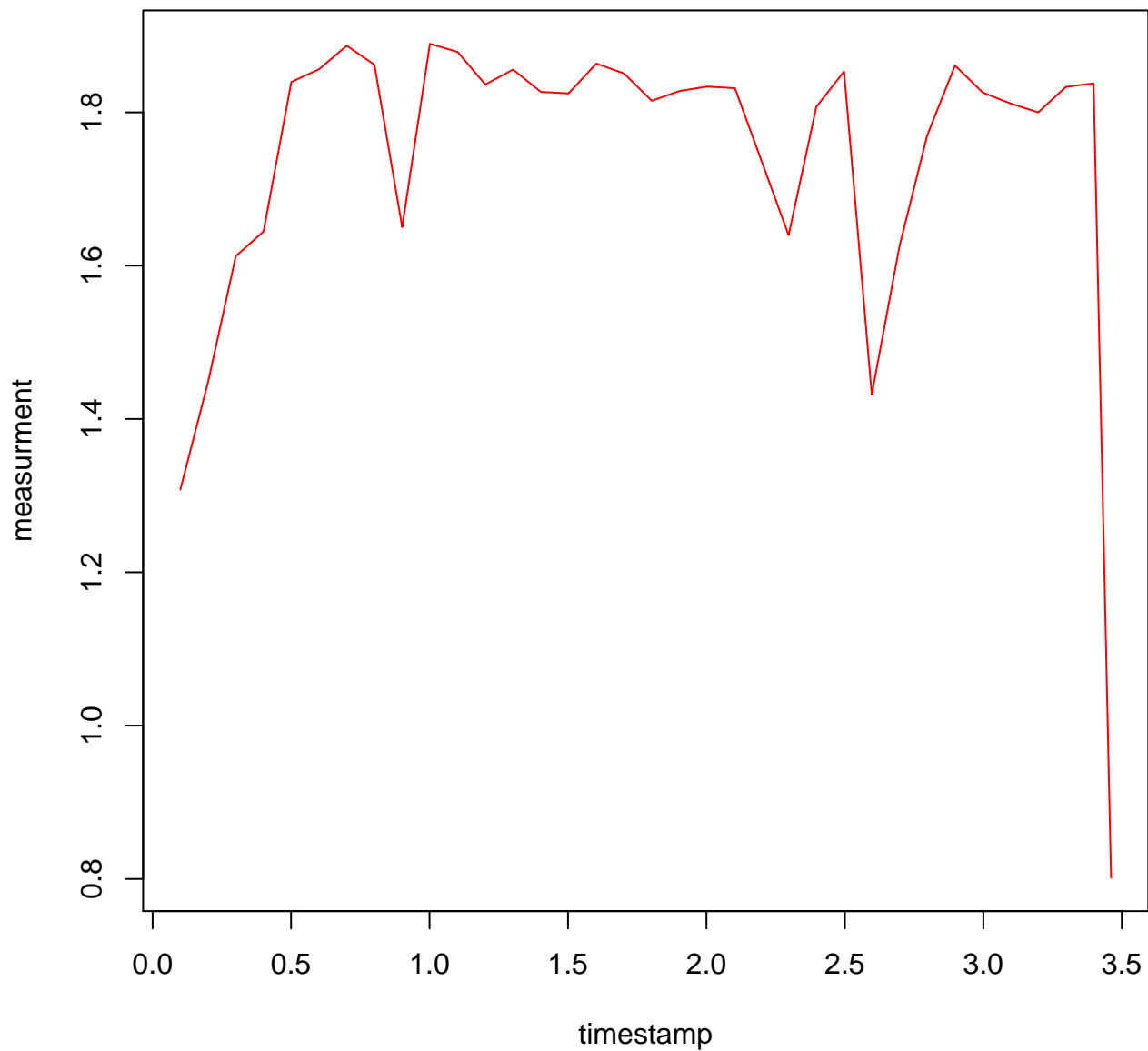
thread1:1T/64 inst L2MKI



thread1:1T/64 inst branchMKI



thread1:1T/64 inst IPC



thread1:1T/64 inst L3MKI

