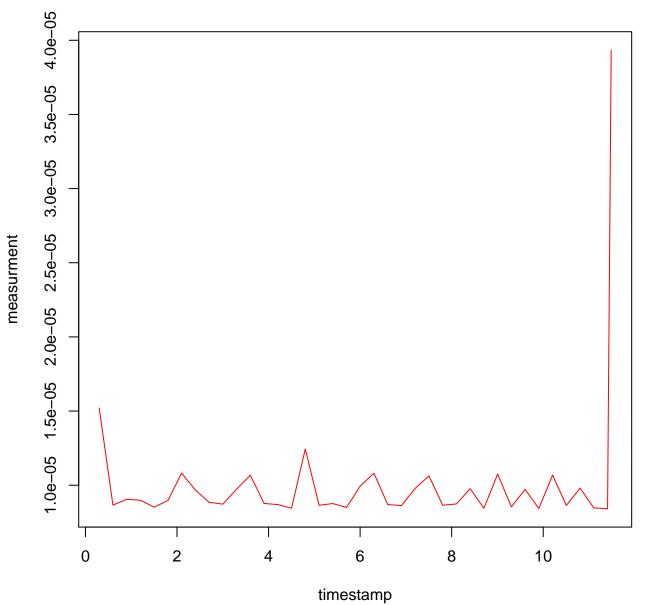
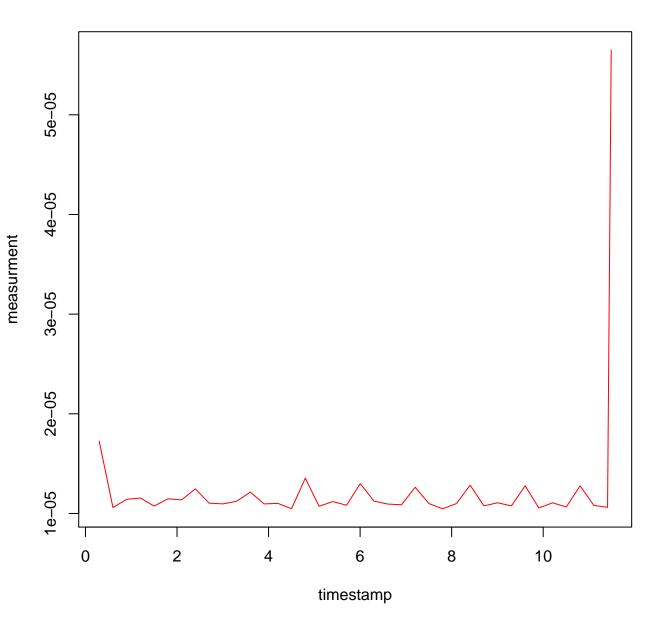
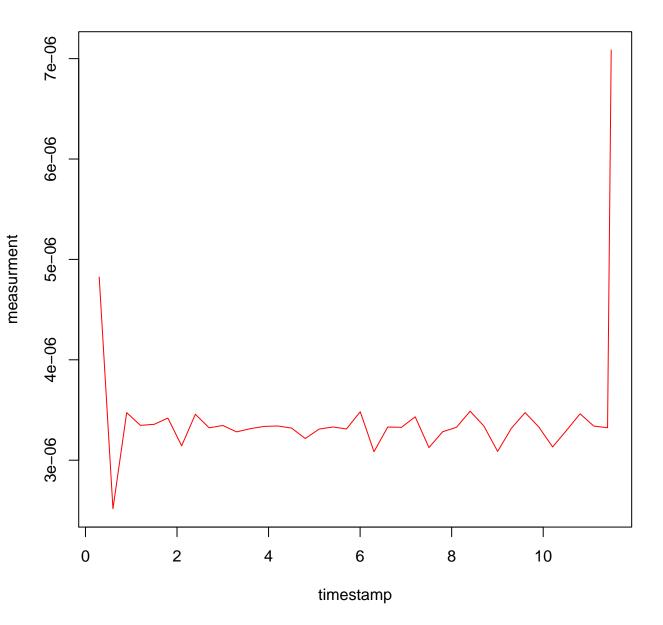
thread1: 1T/16 inst L1MKI



## thread1:1T/16 inst L2MKI



## thread1:1T/16 inst branchMKI



## thread1:1T/16 inst IPC

