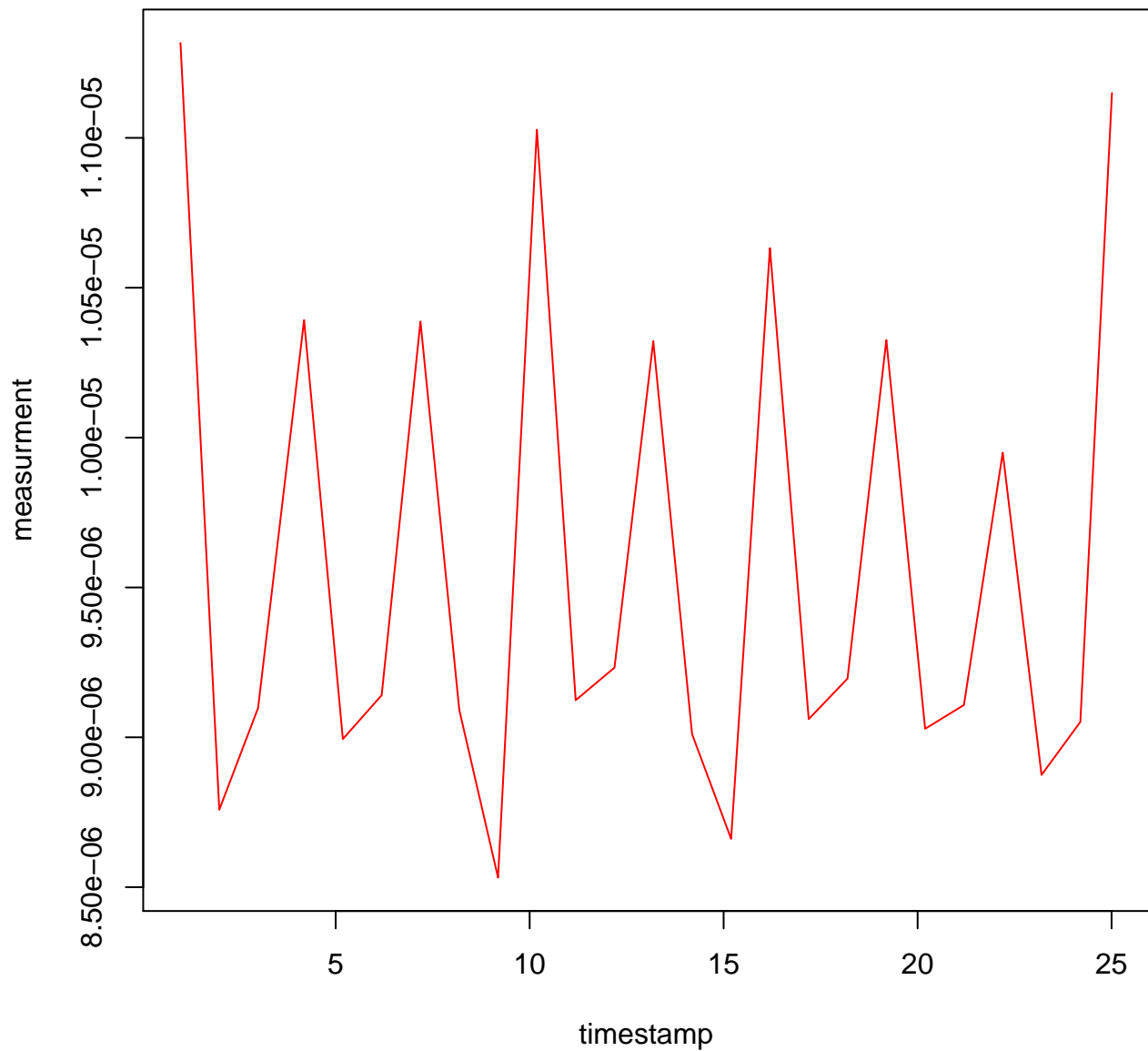
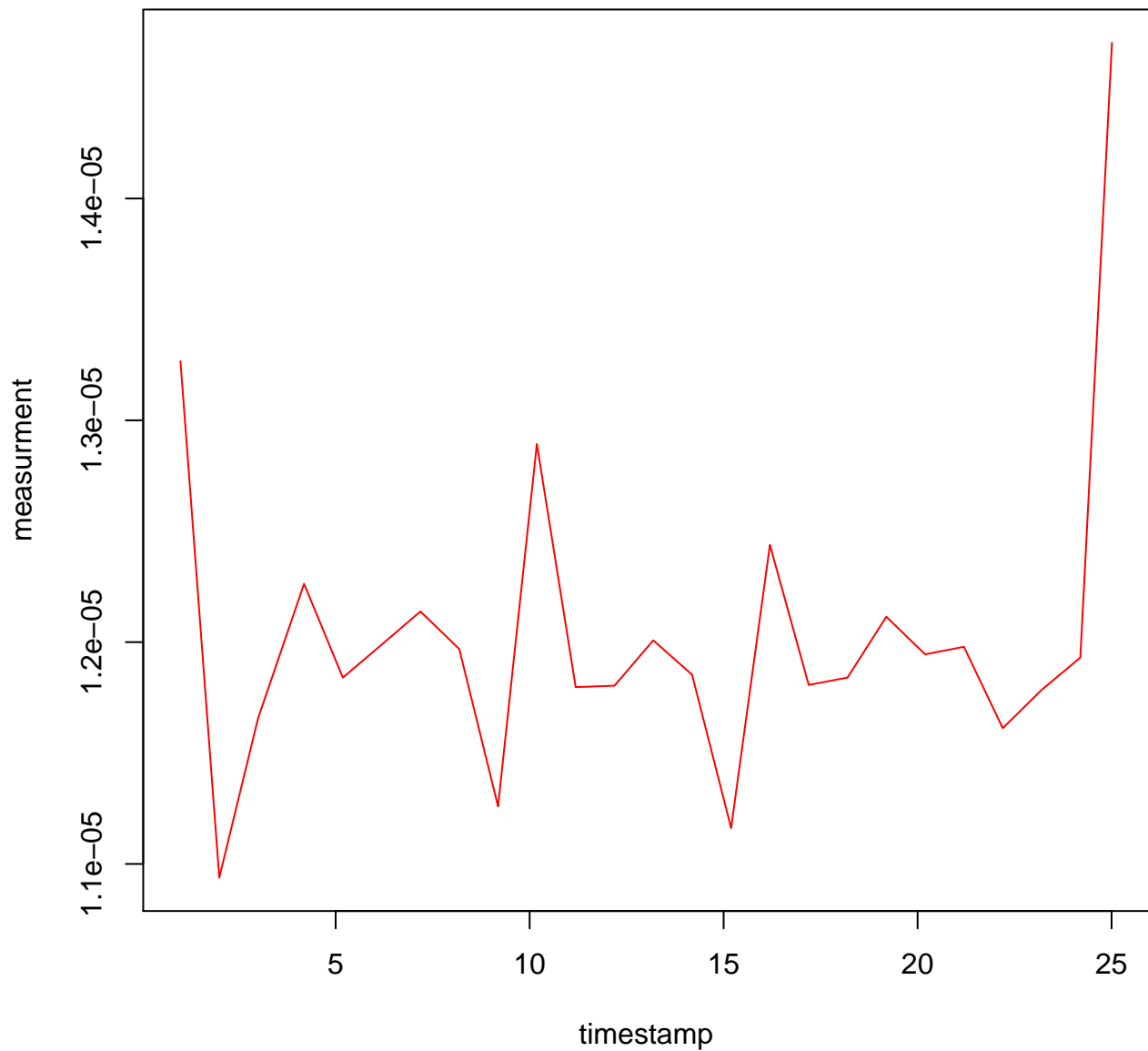


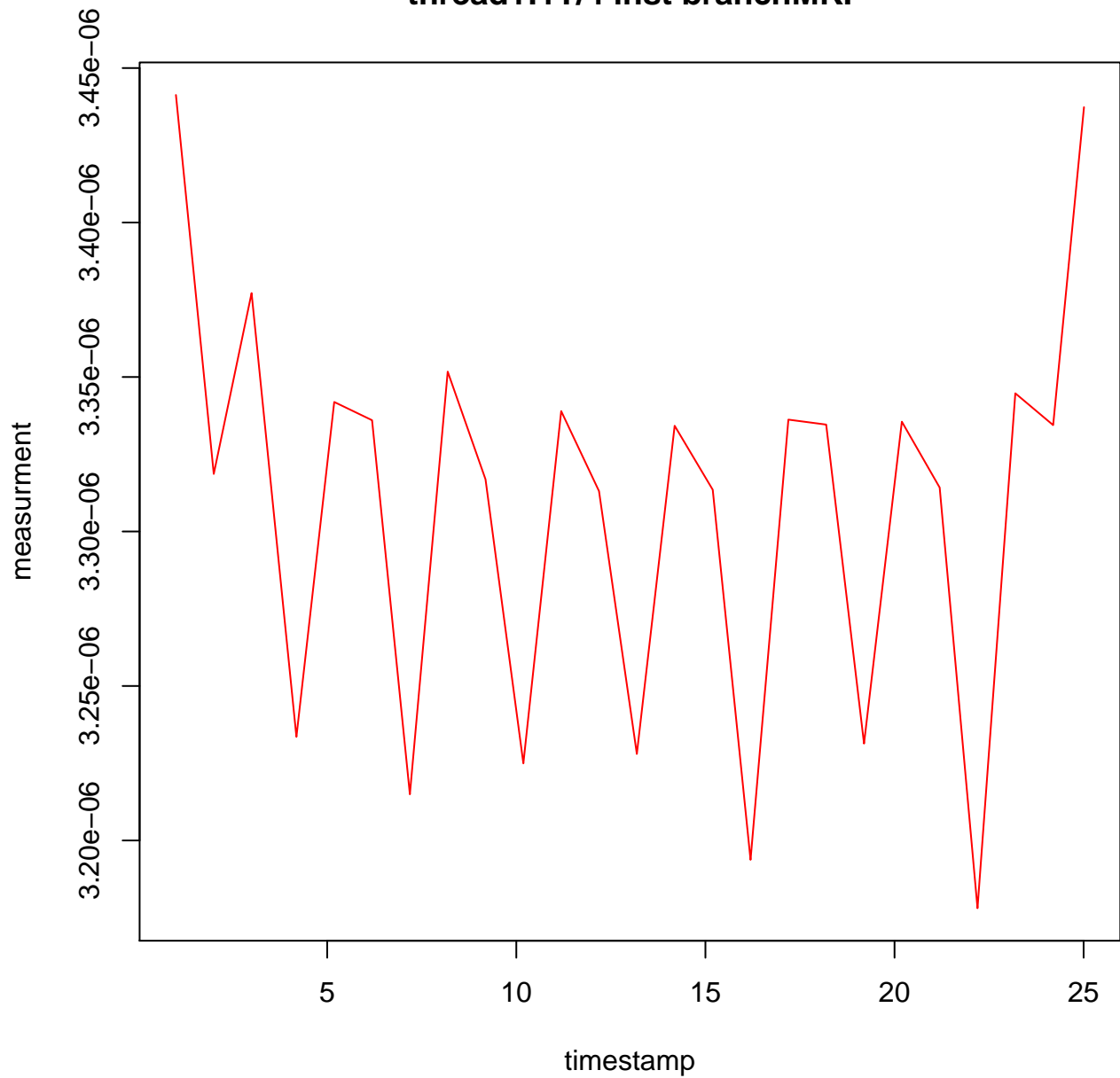
thread1: 1T/4 inst L1MKI



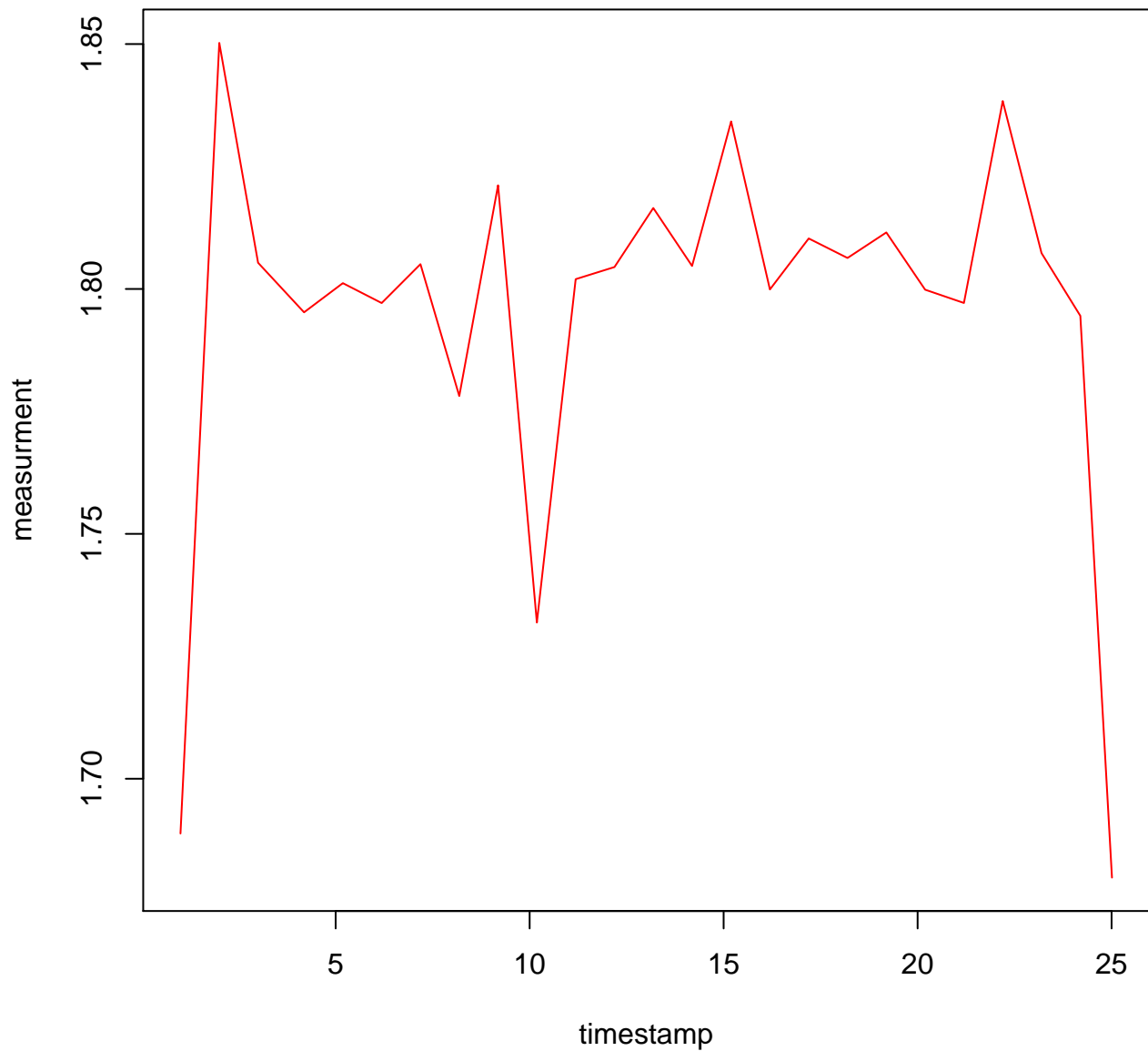
thread1:1T/4 inst L2MKI



thread1:1T/4 inst branchMKI



thread1:1T/4 inst IPC



thread1:1T/4 inst L3MKI

