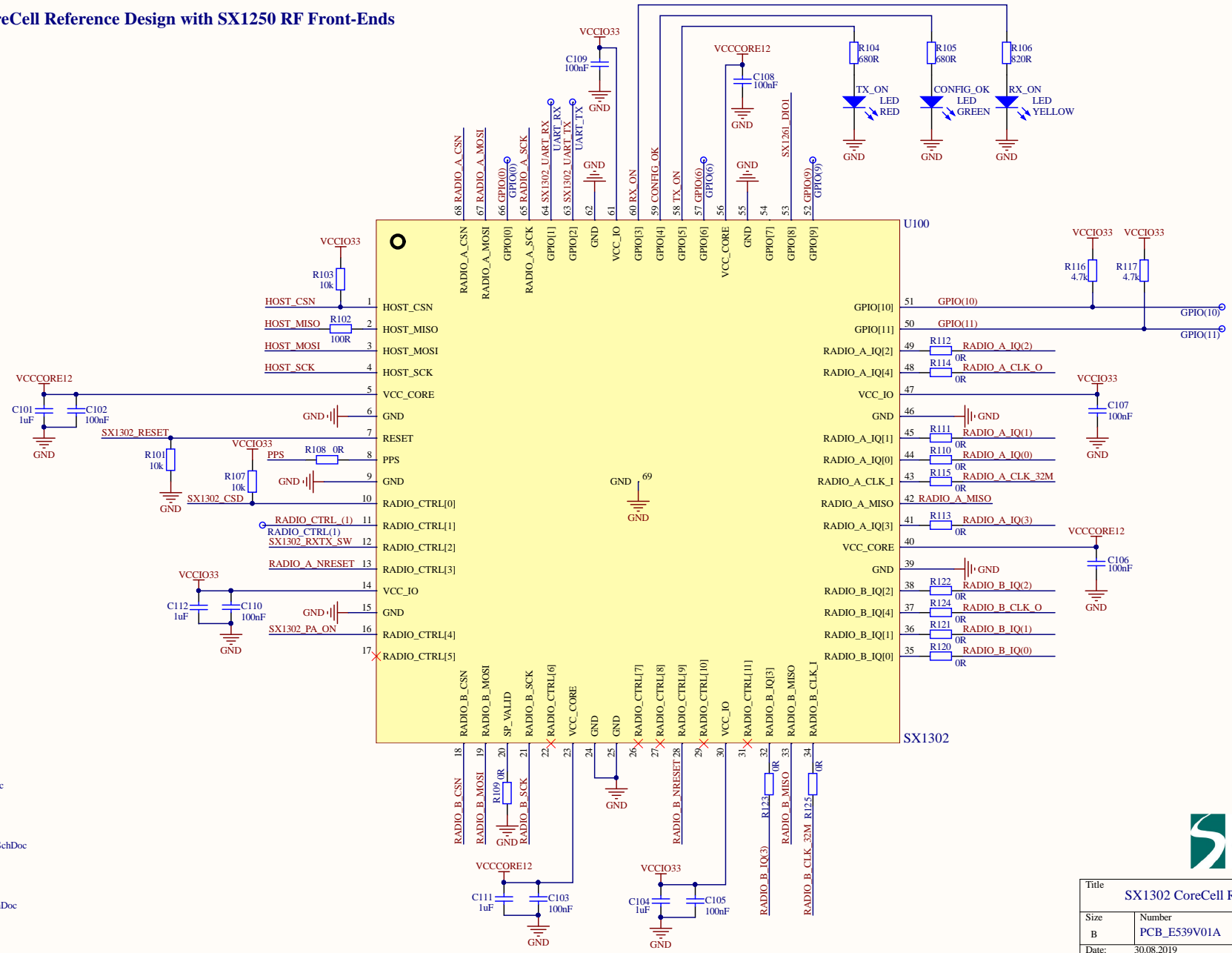


SX1302 CoreCell Reference Design with SX1250 RF Front-Ends

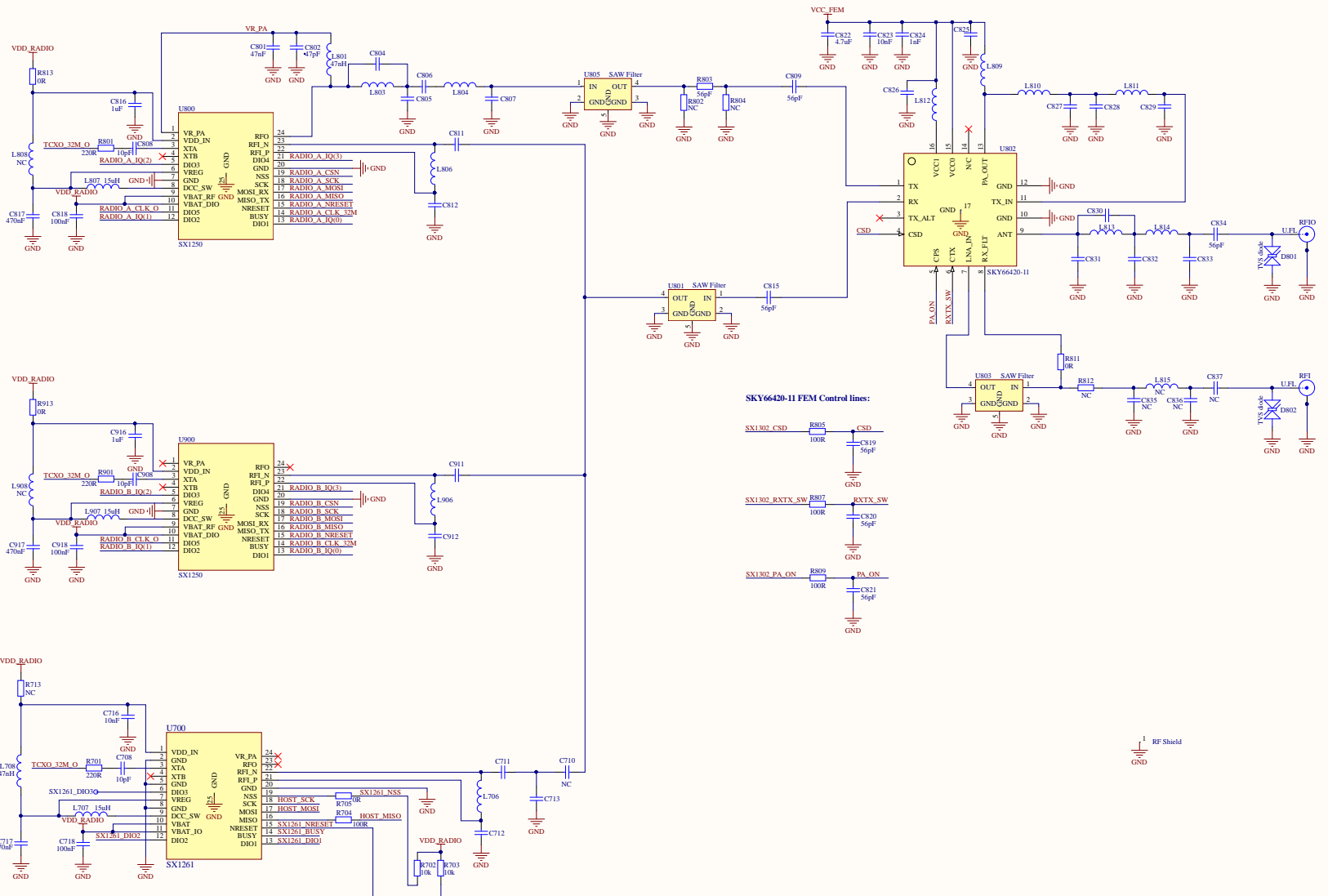
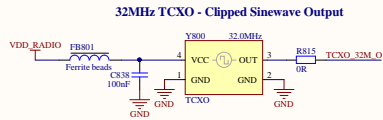


- SX1302_e539v01a_RF.SchDoc
- SCH1
- SX1302_e539v01a_Interface.SchDoc
- SCH2
- SX1302_e539v01a_Power.SchDoc
- SCH3



Title		
SX1302 CoreCell Reference Design - SX1302 Part		
Size	Number	Revision
B	PCB_E539V01A	V1a
Date:	30.08.2019	Sheet 06
File:	B:\support\...\SX1302_e539v01a_Main.SchDoc	

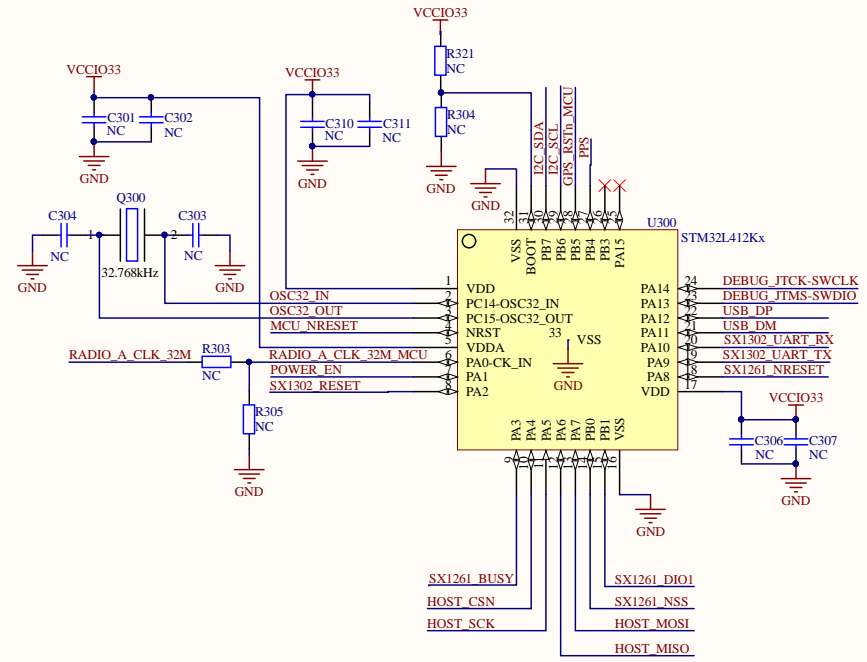
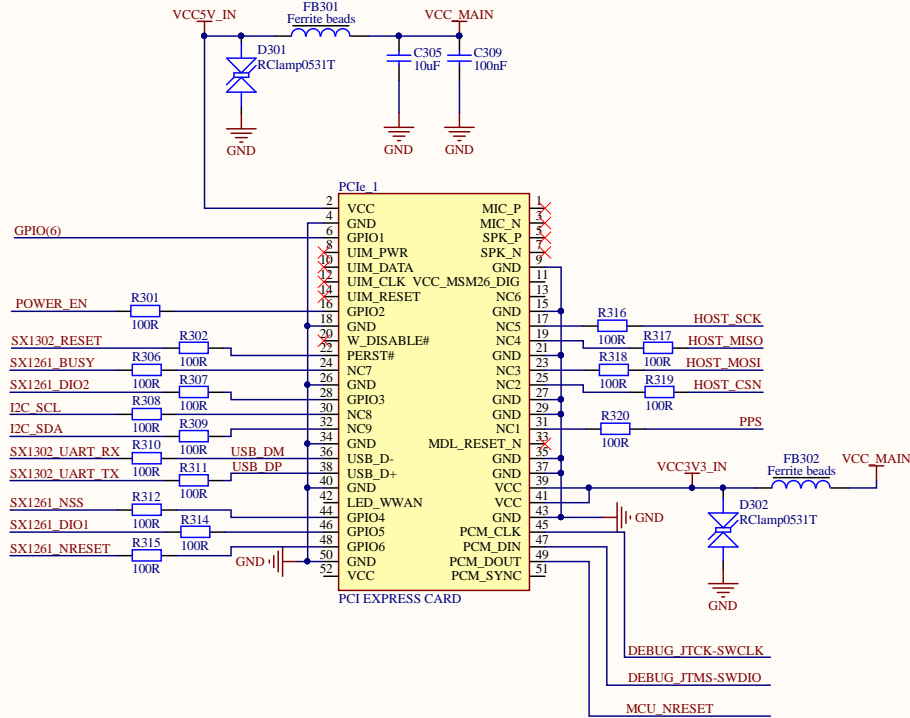
SX1302 CoreCell Reference Design - RF Part with 2x SX1250 RF Front-Ends:

 RF Shield

Title SX1302 CoreCell Reference Design - RF part			
Size A2	Number PCB_E539V01A	Revision Via	
Date: 30.08.2019	Sheet 45		Drawn By:
File: B:\support\SX1302_c539v01a_RF_Sch... B:\support\SX1302_c539v01a_RF_Sch...			

SX1302 CoreCell Reference Design - mini-PCIe Interface & MCU

PCI Express Connector



Series 0 ohm resistors = DNP when MCU/USB is used

MCU = DNP when Corecell Main board is used through mini-PCIe

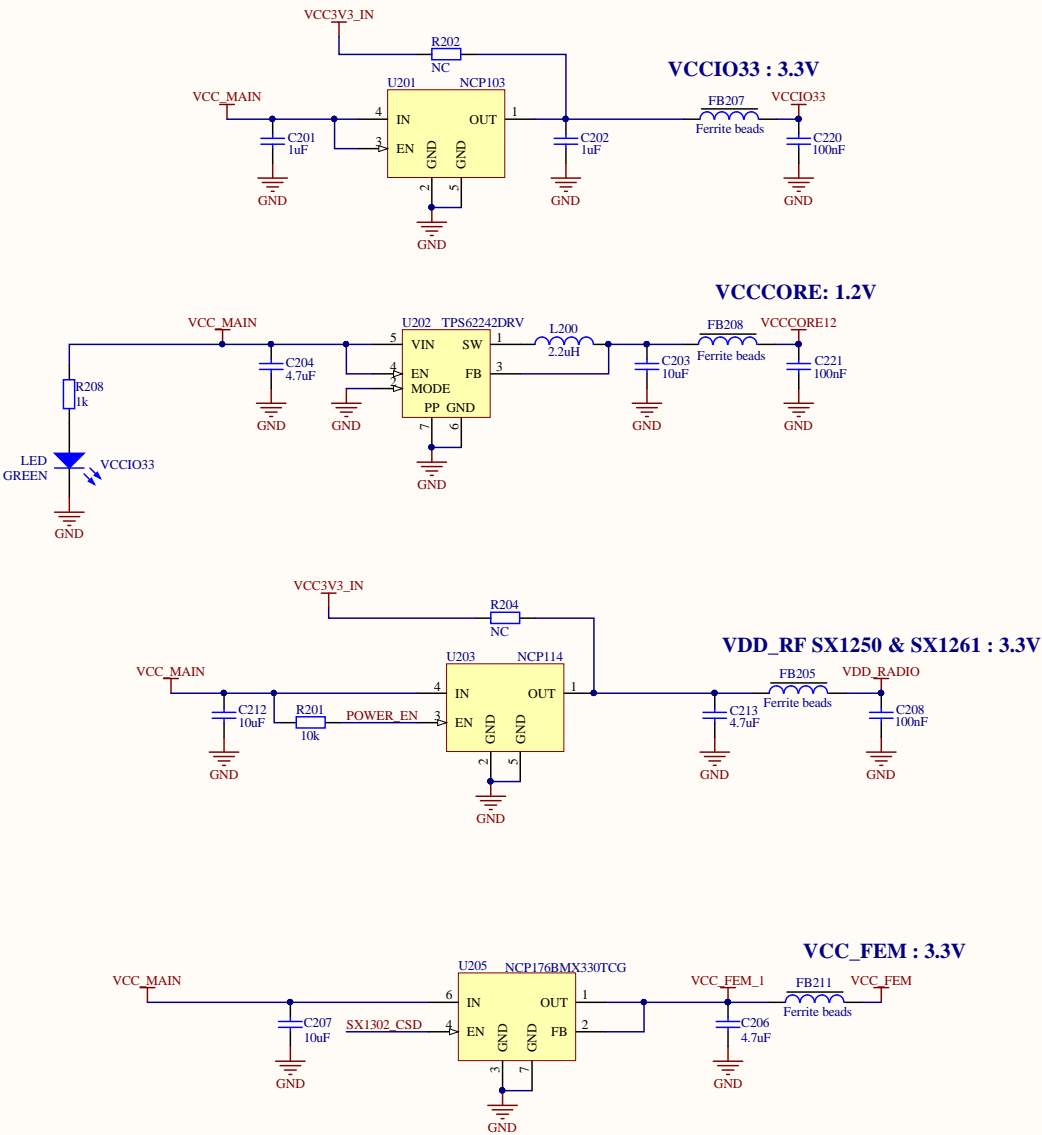
SWCLK, SWDIO, and MCU_NRESET are connected to mini-PCIe

VCC_MAIN comes from 5V or 3.3V

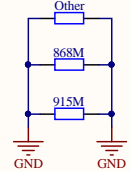


Title SX1302 CoreCell Reference Design - Interface & MCU		
Size B	Number PCB_E539V01A	Revision V1a
Date: 30.08.2019	Sheet 1 of 1	File: B:\support\...SX1302_e539v01a_Interface_SchDoc

SX1302 CoreCell Reference Design - Power Management



Frequency reference:



Semtech logos + Fiducial_Marks:

- 0 Logo Recycling2
- 0 Logo ESD 2
- 0 Logo LoRa 1
- 0 Logo LoRa 2
- 0 Fiducial Mark1
- 0 Logo Semtech 1
- 0 Logo Semtech 2



Title SX1302 CoreCell Reference Design - Power		
Size B	Number PCB_E539V01A	Revision V1a
Date: 30.08.2019	Sheet 4/5	Drawn By:
File: B:\support\...\SX1302_e539v01a_Power.Sch		