Loop-Aware Optimizations in PyPy's Tracing JIT

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Abstract

One of the nice properties of a tracing JIT is that many of its optimizations are simple requiring one forward pass only. This is not true for loop-invariant code motion which is a very important optimization for code with tight kernels. Especially for dynamic languages that typically perform quite a lot of loop invariant type checking, boxed value unwrapping and virtual method lookups. In this paper we explain a scheme pioneered within the context of the LuaJIT project for making simple optimizations loop-aware by using a simple pre-processing step on the trace and not changing the optimizations themselves. We have implemented the scheme in PyPy's tracing JIT compiler, where it can give performance improvements of a factor over two for PyPy's Python JIT executing simple numerical kernels bringing the performance close to that of compiled C code.

Categories and Subject Descriptors   D.3.4 [*Programming Languages*]: Processors—code generation, incremental compilers, interpreters, run-time environments

General Terms   Languages, Performance, Experimentation

Keywords   Tracing JIT, Optimization, Loop-Invariant Code Motion

1.   Introduction

A dynamic language typically needs to do quite a lot of type checking, wrapping/unwrapping of boxed values, and virtual method dispatching. For tight computationally intensive loops a significant amount of the execution time might be spent on such tasks instead of the actual computations. Moreover, the type checking, unwrapping and method lookups are often loop invariant and performance could be increased by moving those operations out of the loop. We explain a simple scheme to make a tracing JIT loop-aware by allowing it's existing optimizations to perform loop invariant code motion.

One of the advantages that tracing JIT compilers have above traditional method-based JITs is that their optimizers are much easier to write. Because a tracing JIT produces only linear pieces of code without control flow joins, many optimization passes on traces can have a very simple structure: They often consist of one forward pass replacing operations by faster ones or even discarding them as they walk along it. This makes optimization of traces very similar to symbolic execution. Also, many difficult problems in traditional

optimizers become tractable if the optimizer does not need to deal with control flow merges.

One disadvantage of this simplicity is that such forward-passing optimizers ignore the only bit of control flow they have available, which is the fact that most traces actually represent loops. Making use of this information is necessary to perform optimizations that take the whole loop into account, such as loop-invariant code motion or optimizations that improve across several iterations of the loop. Having to deal with this property of traces complicates the optimization passes, as a more global view of a trace needs to be considered when optimizing.

Mike Pall pioneered a solution to address this problem in the context of a dynamic language using a tracing JIT compiler. He published his algorithm and its rationale in 2009 [19] and implemented it in LuaJIT 2.0[1], an open source JIT compiler for the Lua language. His approach allows to reuse all forward pass optimizations to achieve loop invariant code motion and other loop-related optimizations, which greatly simplifies the implementation. Using this scheme one does not need to change the underlying optimization much to get these advantages. We have implemented the same approach in PyPy's tracing JIT compiler the results of which we present here.

The resulting optimizations one gets using this scheme are in no way novel, most of them are well-known loop optimizations. However, the way to implement them is a lot simpler than directly implementing loop-aware optimizations.

2.   Background: RPython and PyPy

The work described in this paper was done in the context of the PyPy project.[2] PyPy is a framework for implementing dynamic languages efficiently [20]. When implementing a language with PyPy, one writes an interpreter for the language in RPython [2]. RPython ("Restricted Python") is a subset of Python chosen in such a way that it can be efficiently translated to a C-based VM by performing type inference.

Many low-level aspects of the final VM are not contained within the interpreter implementation but are inserted during translation to C. Examples for this are a garbage collector and also a tracing JIT compiler [6].

RPython's tracing JIT compiler traces on the level of RPython programs. Thus it actually traces the execution of an interpreter written in RPython, not of the program itself. This makes the details of the object model of the implemented language transparent and optimizable by the tracing JIT. In the context of this paper, this aspect of RPython's tracing JIT can be ignored. Instead, it is sufficient to view RPython's tracing JIT as a JIT for RPython.

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[1] http://luajit.org/

[2] http://pypy.org

3. Motivation

To motivate the approach we propose here, let's look at a trivial (unrealistic) trace which corresponds to an infinite loop:

```
L_0(i_0):                                          1
i_1 = i_0 + 1                                      2
print(i_1)                                         3
jump(L_0, i_0)                                     4
```

The first line is a label L_0 with argument i_0. Every label has a list of arguments. The **print** operation just prints its argument (it is not an operation that RPython's tracing JIT really supports, we just use it for this example). The `jump` operation jumps back to the beginning of the trace, listing the new values of the arguments of the trace. In this case, the new value of i_0 is i_0, making it a loop-invariant.

Because i_0 is loop-invariant, the addition could be moved out of the loop. However, it is desirable to get this effect using our existing optimization passes without changing them too much. Optimizations with one forward pass cannot directly achieve this effect: They just look at the trace without taking into account that the trace executes many times in a row. Therefore to achieve loop-invariant code motion, we peel one iteration off the loop before running the optimizations. This peeling gives the following trace:

```
L_0(i_0):                                          1
i_1 = i_0 + 1                                      2
print(i_1)                                         3
jump(L_1, i_0)                                     4
                                                   5
L_1(i_0):                                          6
i_2 = i_0 + 1                                      7
print(i_2)                                         8
jump(L_1, i_0)                                     9
```

The iteration of the loop that was peeled off (lines 1-4) is called the *preamble*, the loop afterwards (lines 6-9) the *peeled loop*.

Now the optimizer optimizes both of these two iterations of the loop together, disregarding the `jump` and the label in lines 4-6. Doing this, common subexpression elimination will discover that the two additions are the same, and replace i_2 with i_1. This leads to the following trace:

```
L_0(i_0):                                          1
i_1 = i_0 + 1                                      2
print(i_1)                                         3
jump(L_1, i_0)                                     4
                                                   5
L_1(i_0):                                          6
print(i_1)                                         7
jump(L_1, i_0)                                     8
```

This trace is malformed, because i_1 is used after the label L_1 without being passed there, so we need to add i_1 as an argument to the label and pass it along the `jump`s:

```
L_0(i_0):                                          1
i_1 = i_0 + 1                                      2
print(i_1)                                         3
jump(L_1, i_0, i_1)                                4
                                                   5
L_1(i_0, i_1):                                     6
print(i_1)                                         7
jump(L_1, i_0, i_1)                                8
```

The final result is that the loop-invariant code was moved out of the loop into the peeled-off iteration. Thus the addition is only executed in the first iteration, while the result is reused in all further iterations.

This scheme is quite powerful and generalizes to other optimizations than just common subexpression elimination. It allows linear

```python
class Base(object):
    pass

class BoxedInteger(Base):
    def __init__(self, intval):
        self.intval = intval

    def add(self, other):
        return other.add__int(self.intval)

    def add__int(self, intother):
        return BoxedInteger(intother + self.intval)

    def add__float(self, floatother):
        floatvalue = floatother + float(self.intval)
        return BoxedFloat(floatvalue)


class BoxedFloat(Base):
    def __init__(self, floatval):
        self.floatval = floatval

    def add(self, other):
        return other.add__float(self.floatval)

    def add__int(self, intother):
        floatvalue = float(intother) + self.floatval
        return BoxedFloat(floatvalue)

    def add__float(self, floatother):
        return BoxedFloat(floatother + self.floatval)


def f(y):
    step = BoxedInteger(-1)
    while True:
        y = y.add(step)
```

Figure 1. An "Interpreter" for a Tiny Dynamic Language Written in RPython

optimization passes to perform loop-aware optimizations, such as loop-invariant code motion without changing them at all. All that is needed is to peel off one iteration, then apply one-pass optimizations and make sure that the necessary extra arguments are inserted into the label of the loop itself and the jumps afterwards.

This is the key insight of the implementation scheme: If an optimization is given two iterations together at the same time, the optimization has enough context to remove operations from the peeled loop, because it detects that the operation was performed in the preamble already. Thus at runtime these moved operations are only executed once when entering the loop and the results are reused in further iterations.

4. Running Example

The last section gave a motivating but unrealistically small example. This section will define a slightly larger example that the rest of the paper uses to demonstrate the effect of optimizations. For this we are going to use a tiny interpreter for a dynamic language with a very small object model, that just supports an integer and a float type (this example has been taken from a previous paper [4]). The objects support only one operation, `add`, which adds two objects (promoting ints to floats in a mixed addition). The implementation of `add` uses classical double-dispatching. The classes can be seen in Figure 1 (written in RPython).

Using these classes to implement arithmetic shows the basic problem of many dynamic language implementations. All the numbers are instances of either `BoxedInteger` or `BoxedFloat`, therefore they consume space on the heap. Performing many arithmetic op-

```
L₀(p₀, p₁):                                              1
# inside f: y = y.add(step)                              2
guard_class(p₁, BoxedInteger)                            3
    # inside BoxedInteger.add                            4
    i₂ = get(p₁, intval)                                 5
    guard_class(p₀, BoxedInteger)                        6
        # inside BoxedInteger.add__int                   7
        i₃ = get(p₀, intval)                             8
        i₄ = i₂ + i₃                                     9
        p₅ = new(BoxedInteger)                           10
            # inside BoxedInteger.__init__               11
            set(p₅, intval, i₄)                          12
jump(L₀, p₀, p₅)                                         13
```

Figure 2. An Unoptimized Trace of the Example Interpreter

erations produces lots of garbage quickly, putting pressure on the garbage collector. Using double dispatching to implement the numeric tower needs two method calls per arithmetic operation, which is costly due to the method dispatch.

Let us now consider an "interpreter" function `f` that uses the object model (see the bottom of Figure 1). Simply running this function is slow, because there are lots of virtual method calls inside the loop, two for each call to `add`. These method calls need to check the type of the involved objects every iteration. In addition, a lot of objects are created when executing that loop, many of these objects are short-lived. The actual computation that is performed by `f` is simply a sequence of float or integer additions (note that `f` does not actually terminate, but it is still instructive to look at the produced traces).

If the function is executed using the tracing JIT, with `y` being a `BoxedInteger`, the produced trace looks like the one of Figure 2 (lines starting with a hash "#" are comments). The trace corresponds to one iteration of the while-loop in `f`.

The operations in the trace are indented corresponding to the stack level of the function that contains the traced operation. The trace is in single-assignment form, meaning that each variable is assigned a value exactly once. The arguments p_0 and p_1 of the loop correspond to the live variables `y` and `step` in the while-loop of the original function.

The label of the loop is L_0 and is used by the jump instruction to identify it's jump target.

The operations in the trace correspond to the operations in the RPython program in Figure 1:

- `new` creates a new object.
- `get` reads an attribute of an object.
- `set` writes to an attribute of an object.
- `guard_class` is a precise type check. It typically precedes an (inlined) method call and is followed by the trace of the called method.

Method calls in the trace are preceded by a `guard_class` operation, to check that the class of the receiver is the same as the one that was observed during tracing.[3] These guards make the trace specific to the situation where `y` is really a `BoxedInteger`. When the trace is turned into machine code and afterwards executed with `BoxedFloat`, the first `guard_class` instruction will fail and execution will continue using the interpreter.

5. Making Trace Optimizations Loop Aware

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[3] `guard_class` performs a precise class check, not checking for subclasses.

Original Loop: After Loop Peeling:
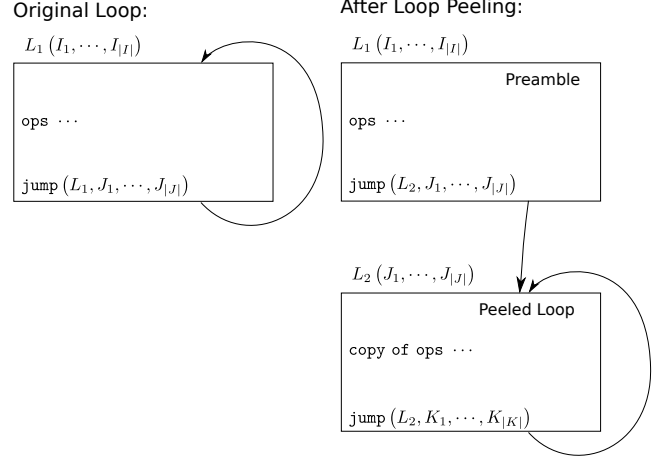
Figure 3. Overview of Loop Peeling

Before a trace is passed to the backend compiling it into machine code it is optimized to achieve better performance. One goal of that is to move operations out of the loop making them executed only once and not every iteration. This can be achieved by loop peeling. It leaves the loop body intact, but prefixes it with one iteration of the loop. This operation by itself will not achieve anything. But if it is combined with other optimizations it can increase the effectiveness of those optimizations. For many optimizations of interest only a few additional details have to be considered when they are combined with loop peeling. These are described below by explaining the loop peeling optimization followed by a set of other optimizations and how they interact with loop peeling.

5.1 Loop Peeling

Loop peeling is achieved by appending an copy of the traced iteration at the end of itself. See Figure 3 for an illustration. The first part (called *preamble*) finishes with a jump to the second part (called the *peeled loop*). The second part finishes with a jump to itself. This way the preamble will be executed only once while the peeled loop will be used for every further iteration. New variable names have to be introduced in the entire copied trace in order to maintain the SSA-property.

When peeling the loop, no assumptions are made that the preamble is the *first* iteration when later executing the loop. The preamble stays general enough to correspond to any iteration of the loop. However, the peeled loop can then be optimized using the assumption that a previous iteration (the preamble) has been executed already.

When applying optimizations to this two-iteration trace some care has to taken as to how the arguments of the two `jump` operations and the input arguments of the peeled loop are treated. It has to be ensured that the peeled loop stays a proper trace in the sense that the operations within it only operates on variables that are either among its input arguments or produced within the peeled loop. To ensure this we need to introduce a bit of formalism.

The original trace (prior to peeling) consists of three parts. A vector of input variables, $I = (I_1, I_2, \cdots, I_{|I|})$, a list of non- jump operations and a single jump operation. The jump operation contains a vector of jump variables, $J = (J_1, J_2, \cdots, J_{|J|})$, that are passed as the input variables of the target loop. After loop peeling there will

```
L_0(p_0, p_1):                                                  1
# inside f: y = y.add(step)                                     2
guard_class(p_1, BoxedInteger)                                 3
    # inside BoxedInteger.add                                  4
    i_2 = get(p_1, intval)                                     5
    guard_class(p_0, BoxedInteger)                             6
        # inside BoxedInteger.add__int                         7
        i_3 = get(p_0, intval)                                 8
        i_4 = i_2 + i_3                                        9
        p_5 = new(BoxedInteger)                                10
            # inside BoxedInteger.__init__                     11
            set(p_5, intval, i_4)                             12
jump(L_1, p_0, p_5)                                            13
                                                               14
L_1(p_0, p_5):                                                 15
# inside f: y = y.add(step)                                    16
guard_class(p_5, BoxedInteger)                                17
    # inside BoxedInteger.add                                 18
    i_6 = get(p_5, intval)                                    19
    guard_class(p_0, BoxedInteger)                            20
        # inside BoxedInteger.add__int                        21
        i_7 = get(p_0, intval)                                22
        i_8 = i_6 + i_7                                       23
        p_9 = new(BoxedInteger)                                24
            # inside BoxedInteger.__init__                     25
            set(p_9, intval, i_8)                             26
jump(L_1, p_0, p_9)                                            27
```

Figure 4. A peeled trace of the example interpreter

be a second copy of this trace with input variables equal to the jump arguments of the preamble, J, and jump arguments K. Figure 3 illustrates the general case. The running example in Figure 2 has $I = (p_0, p_1)$ and $J = (p_0, p_5)$. The result of applying loop peeling to it is shown in Figure 4 with $K = (p_0, p_9)$.

To construct the second copy of the trace (the peeled loop) from the first (the preeamble) we need a function m, mapping the variables of the preamble onto the variables of the peeled loop. This function is constructed during the copying. It is initialized by mapping the input arguments, I, to the jump arguments J,

$$m(I_i) = J_i \text{ for } i = 1, 2, \cdots |I|. \tag{1}$$

In the example that means:

$$\begin{aligned} m(p_0) &= p_0 \\ m(p_1) &= p_5 \end{aligned}. \tag{2}$$

Each operation in the trace is copied in order. To copy an operation $v = \text{op}(A_1, A_2, \cdots, A_{|A|})$ a new variable, $\hat{v}$, is introduced. The copied operation will return $\hat{v}$ using

$$\hat{v} = \text{op}(m(A_1), m(A_2), \cdots, m(A_{|A|})). \tag{3}$$

Before the next operation is copied, m is extend by assigning $m(v) = \hat{v}$. For the example above, that will extend m with

$$\begin{aligned} m(i_2) &= i_6 \\ m(i_3) &= i_7 \\ m(i_4) &= i_8 \\ m(p_5) &= p_9 \end{aligned}. \tag{4}$$

6. Interaction of Optimizations with Loop Peeling

6.1 Redundant Guard Removal

No special concern needs to be taken when implementing redundant guard removal together with loop peeling. The guards from the preamble might make the guards of the peeled loop redundant and thus removed. Therefore one effect of combining redundant guard removal with loop peeling is that loop-invariant guards are moved out of the loop. The peeled loop of the example reduces to the trace in Figure 5.

```
L_1(p_0, p_5):                                                  1
# inside f: y = y.add(step)                                     2
    # inside BoxedInteger.add                                   3
    i_6 = get(p_5, intval)                                      4
        # inside BoxedInteger.add__int                          5
        i_7 = get(p_0, intval)                                  6
        i_8 = i_6 + i_7                                         7
        p_9 = new(BoxedInteger)                                 8
            # inside BoxedInteger.__init__                       9
            set(p_9, intval, i_8)                              10
jump(L_1, p_0, p_9)                                            11
```

Figure 5. Peeled loop after redundant guard removal

The guard on p_5 on line 17 of Figure 4 can be removed since p_5 is allocated on line 10 with a known class. The guard on p_0 on line 20 can be removed since it is identical to the guard on line 6.

Note that the guard on p_5 is removed even though p_5 is not loop invariant, which shows that loop invariant code motion is not the only effect of loop peeling. Loop peeling can also remove guards that are implied by the guards of the previous iteration.

6.2 Common Subexpression Elimination and Heap Optimizations

If a pure operation appears more than once in the trace with the same input arguments, it only needs to be executed the first time and then the result can be reused for all other appearances. RPython's optimizers can also remove repeated heap reads if the intermediate operations cannot have changed their value.[4]

When that is combined with loop peeling, the single execution of the operation is placed in the preamble. That is, loop invariant pure operations and heap reads are moved out of the loop.

Consider the get operation on line 22 of Figure 4. The result of this operation can be deduced to be i_3 from the get operation on line 8. The optimization will thus remove line 22 from the trace and replace i_7 with i_3. Afterwards the trace is no longer in correct form, because the argument i_3 is not passed along the loop arguments. Therefore i_3 needs to be added to the loop arguments.

Doing this, the trace from Figure 4 will be optimized to the trace in Figure 6.

After loop peeling and redundant operation removal the peeled loop will typically no longer be in SSA form but operate on variables that are the result of operations in the preamble. The solution is to extend the input arguments, J, with those variables. This will also extend the jump arguments of the preamble, which is also J. Implicitly that also extends the jump arguments of the peeled loop, K, since they are the image of J under m. For the example I has to be replaced by $\hat{I}$ which is formed by appending i_3 to I. At the same time K has to be replaced by $\hat{K}$ which is formed by appending $m(i_3) = i_7$ to K. The variable i_7 will then be replaced by i_3 by the heap caching optimization as it has removed the variable i_7.

In general what is needed is to keep track of which variables from the preamble are reused in the peeled loop. By constructing a vector, H, of such variables, the input and jump arguments can be updated using

$$\hat{J} = (J_1, J_2, \cdots, J_{|J|}, H_1, H_2, \cdots, H_{|H|}) \tag{5}$$

and

$$\hat{K} = (K_1, K_2, \cdots, K_{|J|}, m(H_1), m(H_2), \cdots, m(H_{|H|})). \tag{6}$$

In the optimized trace J is replaced by $\hat{J}$ and K by $\hat{K}$.

---

[4] We perform a type-based alias analysis to know which writes can affect which reads [11]. In addition writes on newly allocated objects can never change the value of old existing ones.

```
L₀(p₀, p₁):                                              1
# inside f: y = y.add(step)                              2
guard_class(p₁, BoxedInteger)                            3
    # inside BoxedInteger.add                            4
    i₂ = get(p₁, intval)                                 5
    guard_class(p₀, BoxedInteger)                        6
        # inside BoxedInteger.add__int                   7
        i₃ = get(p₀, intval)                             8
        i₄ = i₂ + i₃                                     9
        p₅ = new(BoxedInteger)                           10
            # inside BoxedInteger.__init__               11
            set(p₅, intval, i₄)                          12
jump(L₁, p₀, p₅, i₃)                                     13
                                                         14
L₁(p₀, p₅, i₃):                                          15
# inside f: y = y.add(step)                              16
guard_class(p₅, BoxedInteger)                            17
    # inside BoxedInteger.add                            18
    i₆ = get(p₅, intval)                                 19
    guard_class(p₀, BoxedInteger)                        20
        # inside BoxedInteger.add__int                   21
        i₈ = i₄ + i₃                                     22
        p₉ = new(BoxedInteger)                           23
            # inside BoxedInteger.__init__               24
            set(p₉, intval, i₈)                          25
jump(L₁, p₀, p₉, i₃)                                     26
```

Figure 6. Trace after common subexpression elimination

6.3 Allocation Removal

RPython's allocation removal optimization [4] makes it possible to identify objects that are allocated within the loop but never escape it. That is, no outside object ever gets a reference to them. This is performed by processing the operations in order and optimistically removing every new operation. Later on if it is discovered that a reference to the object escapes the loop, the new operation is inserted at this point. All operations (get, set and guard_class) on the removed objects are also removed and the optimizer needs to keep track of the value of all used attributes of the object.

Consider again the original unoptimized trace of Figure 4. Line 10 contains the first allocation. It is removed and p_5 is marked as allocation-removed. This means that it refers to an object that has not yet been (and might never be) allocated. Line 12 sets the intval attribute of p_5. This operation is also removed and the optimizer registers that the attribute intval of p_5 is i_4.

When the optimizer reaches line 13 it needs to construct the arguments of the jump operation, which contains the reference to the allocation-removed object in p_5. This can be achieved by exploding p_5 into the attributes of the allocation-removed object. In this case there is only one such attribute and its value is i_4, which means that p_5 is replaced with i_4 in the jump arguments.

In the general case, each allocation-removed object in the jump arguments is exploded into a vector of variables containing the values of all registered attributes.[5] If some of the attributes are themselves references to allocation-removed objects they are recursively exploded to make the vector contain only concrete variables. Some care has to be taken to always place the attributes in the same order when performing this explosion. Notation becomes somewhat simpler if also every concrete variable of the jump arguments is exploded into a vector containing itself. For every variable, J_k, of the original jump arguments, J, let

$$\tilde{J}^{(k)} = \begin{cases} (J_k) & \text{if } J_k \text{ is concrete} \\ H^{(k)} & \text{if } J_k \text{ is allocation-removed} \end{cases}, \quad (7)$$

where $H^{(k)}$ is a vector containing all concrete attributes of J_k. The arguments of the optimized jump operation are constructed as the

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[5] This is sometimes called *scalar replacement* [16].

```
L₀(p₀, p₁):                                              1
# inside f: y = y.add(step)                              2
guard_class(p₁, BoxedInteger)                            3
    # inside BoxedInteger.add                            4
    i₂ = get(p₁, intval)                                 5
    guard_class(p₀, BoxedInteger)                        6
        # inside BoxedInteger.add__int                   7
        i₃ = get(p₀, intval)                             8
        i₄ = i₂ + i₃                                     9
            # inside BoxedInteger.__init__               10
jump(L₁, p₀, i₄)                                         11
                                                         12
L₁(p₀, i₄):                                              13
# inside f: y = y.add(step)                              14
    # inside BoxedInteger.add                            15
    guard_class(p₀, BoxedInteger)                        16
        # inside BoxedInteger.add__int                   17
        i₇ = get(p₀, intval)                             18
        i₈ = i₄ + i₇                                     19
            # inside BoxedInteger.__init__               20
jump(L₁, p₀, i₈)                                         21
```

Figure 7. Trace after allocation removal

```
L₀(p₀, p₁):                                              1
# inside f: y = y.add(step)                              2
guard_class(p₁, BoxedInteger)                            3
    # inside BoxedInteger.add                            4
    i₂ = get(p₁, intval)                                 5
    guard_class(p₀, BoxedInteger)                        6
        # inside BoxedInteger.add__int                   7
        i₃ = get(p₀, intval)                             8
        i₄ = i₂ + i₃                                     9
            # inside BoxedInteger.__init__               10
jump(L₁, p₀, i₄)                                         11
                                                         12
L₁(p₀, i₃, i₄):                                          13
    i₈ = i₄ + i₃                                         14
jump(L₁, p₀, i₃, i₈)                                     15
```

Figure 8. The fully optimized loop of the Example Interpreter

concatenation all the $\tilde{J}^{(k)}$ vectors,

$$\hat{J} = \begin{pmatrix} \tilde{J}^{(1)} & \tilde{J}^{(2)} & \cdots & \tilde{J}^{(|J|)} \end{pmatrix}. \quad (8)$$

The arguments of the jump operation of the peeled loop, K, is constructed from $\hat{J}$ using the map m,

$$\hat{K} = \left(m\left(\hat{J}_1\right), m\left(\hat{J}_1\right), \cdots, m\left(\hat{J}_{|\hat{J}|}\right) \right). \quad (9)$$

In the optimized trace I is replaced by $\hat{I}$ and K by $\hat{K}$. The trace from Figure 2 will be optimized to the trace in Figure 7.

If all the optimizations presented above are applied, the resulting loop looks as in Figure 8. The resulting optimized peeled loop consists of a single integer addition only. That is it will become type-specialized to the types of the variables step and y, and the overhead of using boxed values is removed.

7. Benchmarks

Reviewer 2 ▶ *A nit: Section 7 says that loop peeling never makes runtime performance worse, but generating more code can potentially slow performance. I assume that non-numeric benchmarks show no slowdown in practice, and that might be worth noting.* ◀

Reviewer 3 ▶ *Providing source code for the benchmarks measured are needed for others to reproduce and build on your results. I believe this should be the minimum standard for publishing measurements such as these.* ◀ CFB ▶ *let's link to the bitbucket source*

	CPython	Psyco	PyPy no LP	PyPy	GCC -O3
conv3(1e5)	77.89	9.52	1.77	0.68	0.59
conv3(1e6)	77.15	9.58	1.69	0.77	0.74
conv3x3(1000)	233.54	125.40	0.57	0.27	0.25
conv3x3(3)	234.45	126.28	0.60	0.31	0.28
conv5(1e5)	122.54	16.67	1.86	1.05	0.65
conv5(1e6)	125.77	16.80	1.92	1.09	0.80
dilate3x3(1000)	232.51	125.85	3.89	3.69	0.25
sobel(1000)	181.49	95.05	0.71	0.42	0.20
sqrt(Fix16)	744.35	421.65	3.93	2.14	0.96
sqrt(float)	24.21	5.52	1.36	1.00	0.98
sqrt(int)	20.84	1.78	2.26	1.82	0.80
Variations	-	-	±0.03	±0.01	±0.01

Figure 9. Benchmark Results in Seconds. Arrays of length 10^5 and 10^6 and matrices of size 1000×1000 and 1000000×3 are used. The one used in each benchmark is indicated in the leftmost column. For the matrices, only the number of rows are specified.

code view. how about we move the benchmarks to the dls directory as well? or their own repository, we've been using them as demos ◀

Reviewer 3 ▶ *I would have liked to have benchmark results for some larger applications. When is this optimization effective on a large scale, if ever?* ◀ CFB ▶ *I don't actually know. Does anybody?* ◀

Reviewer 4 ▶ *It isn't clear from the paper, but a reader might conclude that the bulk of the time savings are from removing boxing/unboxing operations.* ◀

Reviewer 4 ▶ *This paper is relatively short, and could be significantly improved with a couple of pages of additional information about the details of the benchmarks − both on the Python and on the C side.* ◀

The loop peeling optimization was implemented in the PyPy framework in about 450 lines of RPython code. That means that the JIT-compilers generated for all interpreters implemented with RPython now can take advantage of it. Benchmarks have been executed for a few different interpreters and we see improvements in several cases. The ideal loop for this optimization is short and contains numerical calculations with no failing guards and no external calls. Larger loops involving many operations on complex objects typically benefit less from it. Loop peeling never makes runtime performance worse, in the worst case the peeled loop is exactly the same as the preamble. Therefore we chose to present benchmarks of small numeric kernels where loop peeling can show its use.

7.1 Python

The Python interpreter of the RPython framework is a complete Python version 2.7 compatible interpreter. A set of numerical calculations were implemented in both Python and in C and their runtimes are compared in Figure 7. The benchmarks are

- **sqrt**: approximates the square root of y. The approximation is initiated to $x_0 = y/2$ and the benchmark consists of a single loop updating this approximation using $x_i = (x_{i-1} + y/x_{i-1})/2$ for $1 \le i < 10^8$. Only the latest calculated value x_i is kept alive as a local variable within the loop. There are three different versions of this benchmark where x_i is represented with different type of objects: int's, float's and Fix16's. The latter, Fix16, is a custom class that implements fixpoint arithmetic with 16 bits precision. In Python there is only a single implementation of the benchmark that gets specialized depending on the class of it's input argument, y, while in C, there are three different implementations.

- **conv3**: one-dimensional convolution with fixed kernel-size 3. A single loop is used to calculate a vector $\mathbf{b} = (b_1, \cdots, b_n)$ from a vector $\mathbf{a} = (a_1, \cdots, a_n)$ and a kernel $\mathbf{k} = (k_1, k_2, k_3)$ using $b_i = k_3 a_i + k_2 a_{i+1} + k_1 a_{i+2}$ for $1 \le i \le n$. Both the output vector, $\mathbf{b}$, and the input vectors, $\mathbf{a}$ and $\mathbf{k}$, are allocated prior to running the benchmark. It is executed with $n = 10^5$ and $n = 10^6$.

- **conv5**: one-dimensional convolution with fixed kernel-size 5. Similar to conv3, but with $\mathbf{k} = (k_1, k_2, k_3, k_4, k_5)$. The enumeration of the elements in $\mathbf{k}$ is still hardcoded into the implementation making the benchmark consist of a single loop too.

- **conv3x3**: two-dimensional convolution with kernel of fixed size 3×3 using a custom class to represent two-dimensional arrays. It is implemented as two nested loops that iterates over the elements of the $n \times n$ output matrix $\mathbf{B} = (b_{i,j})$ and calculates each element from the input matrix $\mathbf{A} = (a_{i,j})$ and a kernel $\mathbf{K} = (k_{i,j})$ using $b_{i,j} =$

$$
\begin{array}{llll}
k_{3,3}a_{i-1,j-1} & + & k_{3,2}a_{i-1,j} & + & k_{3,1}a_{i-1,j+1} & + \\
k_{2,3}a_{i,j-1} & + & k_{2,2}a_{i,j} & + & k_{2,1}a_{i,j+1} & + \\
k_{1,3}a_{i+1,j-1} & + & k_{1,2}a_{i+1,j} & + & k_{1,1}a_{i+1,j+1}
\end{array}
\tag{10}
$$

for $1 \le i \le n$ and $1 \le j \le n$. The memory for storing the matrices are again allocated outside the benchmark and $n = 1000$ was used.

- **dilate3x3**: two-dimensional dilation with kernel of fixed size 3×3. This is similar to convolution but instead of summing over the terms in Equation 10, the maximum over those terms is taken. That places a external call to a max function within the loop that prevents some of the optimizations.

- **sobel**: a low-level video processing algorithm used to locate edges in an image. It calculates the gradient magnitude using sobel derivatives. A Sobel x-derivative, D_x, of a $n \times n$ image, I, is formed by convolving I with

$$
K = \begin{pmatrix} -1 & 0 & 1 \\ -2 & 0 & 2 \\ -1 & 0 & 1 \end{pmatrix},
\tag{11}
$$

and a Sobel y-derivative, D_y, is formed convolving I with $K^\top$. The gradient magnitude is then formed for each pixel independently by $\sqrt{D_x^2 + D_y^2}$. The two convolutions and the pixelwise magnitude calculation are combined in the implementation of this benchmark and calculated in a single pass over the input image. This single pass consists of two nested loops with a somewhat larger amount of calculations performed each iteration as compared to the other benchmarks.

The sobel and conv3x3 benchmarks are implemented on top of a custom two-dimensional array class. It is a straightforward implementation providing 2 dimensional indexing with out of bounds checks. For the C implementations it is implemented as a C++ class. The other benchmarks are implemented in plain C. All the benchmarks except sqrt operate on C double-precision floating point numbers, both in the Python and the C code.

Benchmarks were run on Intel i7 M620 @2.67GHz with 4M cache and 8G of RAM using Ubuntu Linux 11.4 in 32bit mode. The machine was otherwise unoccupied. We use the following software for benchmarks:

- PyPy 1.5
- CPython 2.7.2
- Psyco 1.6 with CPython 2.6.6
- GCC 4.4.5 shipped with Ubuntu 11.4

We run GCC with -O3 -march=native, disabling the automatic loop vectorization. In all cases, SSE2 instructions were used for floating point operations, except Psyco which uses x87 FPU instructions. We also run PyPy with loop peeling optimization and without (but otherwise identical).

For PyPy 10 iterations were run, prefaced with 3 iterations for warming up. Due to benchmarks taking large amounts of time on CPython, only one run was performed, prefaced with one warmup run for Psyco. For GCC 5 iterations were run. In all cases, the standard deviation is very low, making benchmarks very well reproducible.

We can observe that PyPy (even without loop peeling) is orders of magnitude faster than either CPython or Psyco. This is due to the JIT compilation advantages and optimizations we discussed in previous work [4, 5]. The geometric mean of the speedup of loop peeling is 70%, which makes benchmark times comparable with native-compiled C code. We attribute the performance gap to C code to the relative immaturity of RPython's JIT machine code backend as well as missing optimizations, like instruction scheduling.

Other interesting interpreters that are helped greatly by this optimization are for example our Prolog interpreter written in RPython [7]. Prolog programs often contain tight loops that perform list processing. Furthermore we experimented with a Python library for writing numerical kernels doing array manipulation. The exact extent is out of scope for this paper.

8. Related Work

Loop invariant code motion optimizations are a well-known approach to optimize loops [18]. Therefore, the effects that the optimizations described here achieve are not in any way new. However, we think that achieving them in the way described in this paper is simpler than writing explicit algorithms.

Loop invariant code motion has been part of early compilers in the 1960s and 1970s [1]. A common approach for achieving loop invariant code motion is to perform partial redundancy elimination. The approach was first proposed by Morel and Renvoise [17]. It involves solving data flow problems of bidirectional data flow equations. After improvements [9, 10] this approach was followed by the work of Knoop et.al. [15] who cleanly separated the problem into a backward and forward data flow analysis. Implementing partial redundancy elimination in compilers that use SSA form [8] simplified the algorithms because no iterative data flow analysis is needed any more.

As described in the introduction, Mike Pall pioneered the approach described in this paper. He showed that, unlike traditional loop-invariant code motion (LICM), this approach is effective, even in the presence of many guards and global control dependencies, which are caused by the semantics of dynamic languages.

He writes on the Lua-users mailing list: "The LOOP pass does synthetic unrolling of the recorded IR, combining copy-substitution with redundancy elimination to achieve code hoisting. The unrolled and copy-substituted instructions are simply fed back into the compiler pipeline, which allows reuse of all optimizations for redundancy elimination. Loop recurrences are detected on-the-fly and a minimized set of PHIs is generated." [19]

Both the Hotpath VM [14] and SPUR [3] implements loop-invariant code motion directly, by explicitly marking as loop-invariant all variables that stay the same along all looping paths and then moving all pure computation that depends only on these variables out of the loop. SPUR can also hoist loads out of the loop if nothing in the loop can ever write to the memory location. It can also move allocations out of the loop, but does not replace the object by its attributes. This saves only the allocation, not the access to the object attributes.

The type specialization described by Gal *et al.* [12] can be seen as doing a similar optimization (again by manually implementing it) than the one described in Section 6.3: The effect of both is that type checks are fully done before a loop is even entered.

9. Conclusions

In this paper we have studied loop invariant code motion during trace compilation. We claim that the loop peeling approach of LuaJIT is a very convenient solution since it fits well with other trace optimizations and does not require large changes to them. The approach improves the effect of standard optimizations such as redundant guard removal, common subexpression elimination and allocation removal. The most prominent effect is that they all become loop invariant code motion optimizations.

By using several benchmarks we show that the proposed algorithm can significantly improve the run time of small loops containing numerical calculations.

The current approach still has some limitations which we plan to address in the future. In particular loop peeling works poorly in combination with trace trees [13] or trace stitching [12]. The side exits attached guards that fail often currently have to jump to the preamble which makes loops with several equally common paths less efficient than they could be.

Acknowledgments

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