

Linux Scheduler



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graph TD; LS[Linux Scheduler] --> VC0[Virtual Core 0]; LS --> VC1[Virtual Core 1]; LS --> VC2[Virtual Core 2]; LS --> VC3[Virtual Core 3]; VC0 --> CA7_0[Cortex-A7]; VC0 --> CA15_0[Cortex-A15]; VC1 --> CA7_1[Cortex-A7]; VC1 --> CA15_1[Cortex-A15]; VC2 --> CA7_2[Cortex-A7]; VC2 --> CA15_2[Cortex-A15]; VC3 --> CA7_3[Cortex-A7]; VC3 --> CA15_3[Cortex-A15];
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Virtual Core 0

Cortex-A7

Cortex-A15

Virtual Core 1

Cortex-A7

Cortex-A15

Virtual Core 2

Cortex-A7

Cortex-A15

Virtual Core 3

Cortex-A7

Cortex-A15