

# **Time Warp Simulation on Multi-core Processors and Clusters**

A thesis submitted to the

Division of Research and Advanced Studies  
of the University of Cincinnati

in partial fulfillment of the  
requirements for the degree of

**MASTER OF SCIENCE**

in the School of Electric and Computing Systems  
of the College of Engineering and Applied Sciences

August xx, 2015

by

**Doug Weber**

BSEE, University of Cincinnati, 2014

Thesis Advisor and Committee Chair: Dr. Philip A. Wilsey

# Abstract

Parallel Discrete Event Simulation (PDES) systems have traditionally been designed for either clusters using message passing communication for shared-memory multiprocessors using shared data structures. PDES applications are an example of fine-grained parallelism which means that there is a high cost of communication with message passing. Message passing, however, is necessary to scale up the computational power and memory required for larger simulation models that would be limited in a single shared-memory processor. Shared memory communication, on the other hand significantly reduces communication costs by minimizing copying overheads with pointers and eliminating network latency.

To get the best of both forms of communication, WARPED2, a new PDES simulation kernel which implements the time warp mechanism, uses a hybrid approach where a set of multithreaded processes communicate with message passing but threads within each process communicate with shared data structures. If the logical processes (LPs) of the simulation model are partitioned well, then the interprocess communication can be minimized and the simulation models can scale to more reasonable levels.

A hybrid communication approach, however, adds some significant problems that make it hard to optimize the simulation mechanism to get the best performance. The first problem that must be dealt with, which is common to any multithreaded application, is the high cost of thread synchronization to protect shared data structures. This problem not only adds significant overheads in the simulation mechanism (dispatching of local events and processing events) but with the standard runtime and message passing libraries as well. A few of the solutions what will described include separating data structures for thread local use to eliminate synchronization, using spinlocks to reduce lock aquisition latency, using alternative multithreaded memory allocators, and using a separate thread to handle message passing communication.

The second, and more time warp specific problem arises with a hybrid communication model is with

---

developing asynchronous GVT and termination detection algorithms which are usually designed for either shared-memory multiprocessors *or* message passing systems, but not both. For both GVT and termination detection, a two level approach for each will be discussed and analyzed as well as alternative approaches.

Experimental analysis will be performed on SMP nodes and clusters to gain a better understanding of the tunable performance and memory parameters of WARPED2. Preliminary results indicate that any optimization that minimizes thread synchronization is usually preferable on a single SMP node even though rollbacks usually increase and efficiency decreases. Furthermore, TCMalloc, a memory allocator for multi-threaded applications also enhances performance.

On a cluster, funneling communication to a dedicated thread increases performance over multi-thread communication but does not scale well due to an extreme decrease in efficiency with more nodes. Aggregating messages that are destined for the same node, however, significantly increases efficiency and performance.

# Acknowledgments

First and foremost, I would like to thank Dr. Wilsey for his guidance throughout my research experience. It has been a great experience working with him and I have learned more than I ever thought I would.

I would also like to thank Sounak Gupta for his work with the pending event set in WARPED2 and AJ Alt for his work with the initial development of WARPED. My research would not have been possible without them. Also, thanks to all previous students in the Experimental Computing Lab who contributed to the development of the original WARPED simulation kernel.

Thank you to my family for being supportive throughout my entire education at the University of Cincinnati.

# Contents

<b>1</b>	<b>Introduction</b>	<b>1</b>
1.1	Thesis Overview . . . . .	3
<b>2</b>	<b>Background</b>	<b>5</b>
2.1	Discrete Event Simulation . . . . .	5
2.2	Parallel Discrete Event Simulation . . . . .	6
2.2.1	Time Warp . . . . .	7
2.3	Parallel Systems Architectures . . . . .	8
2.3.1	Shared Memory Multiprocessor Systems . . . . .	8
2.3.2	Clustered Systems . . . . .	10
2.4	Parallel Systems Communication . . . . .	11
2.4.1	Message Passing . . . . .	11
2.4.2	Shared Memory . . . . .	12
<b>3</b>	<b>Related Work</b>	<b>13</b>
3.1	Georgia Tech Time Warp (GTW) . . . . .	13
3.2	Clustered Time Warp (CTW) . . . . .	15
3.3	Rensselaer's Optimistic Simulation System (ROSS) . . . . .	16
3.4	WARPED . . . . .	17
3.5	Others . . . . .	18
3.5.1	The ROme OpTimistic Simulator (ROOT-Sim) . . . . .	18
3.5.2	ROSS-MT . . . . .	18

<b>4</b>	<b>The WARPED2 Simulation Kernel</b>	<b>19</b>
4.1	The Software Architecture of WARPED2 . . . . .	19
4.1.1	Local Time Warp Components . . . . .	20
4.1.2	Global Time Warp Components and Communication Manager . . . . .	21
4.2	The Modeling API of WARPED2 . . . . .	21
4.2.1	The LPState Structure . . . . .	22
4.2.2	The Event Class . . . . .	22
4.2.3	The LogicalProcess class . . . . .	23
4.2.4	The Partitioner class . . . . .	24
4.2.5	Random Number Generation . . . . .	25
4.2.6	Command Line Arguments and the Kernel Entry Point . . . . .	25
4.3	Plans of Study . . . . .	27
4.3.1	Experiments on x86 SMP Nodes . . . . .	27
4.3.2	Experiments x86 Clusters . . . . .	27
4.3.3	Experiments ARM big.LITTLE Nodes . . . . .	27
<b>5</b>	<b>Interprocess Communication and Partitioning</b>	<b>28</b>
5.1	Multithreaded Message Passing with MPI . . . . .	28
5.2	Partitioning . . . . .	31
5.3	Message Aggregation . . . . .	33
5.4	Scaling up the Simulation Model . . . . .	37
<b>6</b>	<b>WARPED2 Data Structures and Organization</b>	<b>38</b>
6.1	Pending Event Set Data Structures . . . . .	38
6.2	Processing Events . . . . .	40
6.2.1	Ordering of Events . . . . .	41
6.2.2	Performance Tuning Parameters . . . . .	41
6.2.3	Protecting Access to LTSF Queues . . . . .	45
6.3	Data Structures to Support Rollback and Cancellation . . . . .	48

<b>7</b>	<b>GVT and Termination Detection</b>	<b>50</b>
7.1	Global Snapshots . . . . .	50
7.2	GVT . . . . .	51
7.2.1	Asynchronous GVT Algorithms . . . . .	52
7.2.2	Synchronous GVT Algorithms . . . . .	54
7.2.3	GVT in WARPED2 . . . . .	54
7.3	Termination Detection . . . . .	59
7.3.1	Termination in WARPED2 . . . . .	59
<b>8</b>	<b>Memory Management</b>	<b>62</b>
8.1	State Saving . . . . .	62
8.1.1	State Saving Techniques . . . . .	62
8.1.2	State Saving in WARPED2 . . . . .	63
8.2	Fossil Collection . . . . .	63
8.2.1	Fossil Collection Techniques . . . . .	64
8.2.2	Fossil Collection in WARPED2 . . . . .	64
8.3	Memory Allocation . . . . .	66
8.3.1	Thread-Caching Malloc (TCMalloc) . . . . .	66
8.4	Memory Tuning Parameters in WARPED2 . . . . .	68
8.4.1	Number of LTSF Queues . . . . .	68
8.4.2	State Saving Period . . . . .	68
8.4.3	GVT Period . . . . .	69
<b>9</b>	<b>ARM big.LITTLE Platform</b>	<b>71</b>
9.1	Comparison of Big and LITTLE Performances . . . . .	72
9.2	CPU Affinity and LTSF Queue Partitioning . . . . .	74
9.3	Atomic Instructions on ODROID XU3 . . . . .	75
<b>10</b>	<b>Conclusions &amp; Future Research</b>	<b>76</b>
10.1	Summary of Findings . . . . .	76

## CONTENTS

---

10.2 Detailed Conclusions . . . . .	76
10.3 Suggestions for Future Work . . . . .	76
<b>A x86 SMP Nodes and Cluster</b>	<b>77</b>
<b>B Simulation Models</b>	<b>78</b>
B.0.1 PCS . . . . .	78
B.0.2 Traffic . . . . .	80
B.0.3 Epidemic . . . . .	81
B.0.4 Airport . . . . .	82
<b>C Simulation Model Configurations</b>	<b>83</b>



# List of Figures

1.1	Communication Model of WARPED2 . . . . .	2
2.1	SMP System . . . . .	9
2.2	NUMA System . . . . .	10
2.3	Beowulf Cluster . . . . .	10
2.4	Message Passing and Shared Memory Communication . . . . .	12
4.1	Time Warp Components in WARPED2 . . . . .	20
5.1	Communication with Various Simulation Models . . . . .	30
5.2	Round Robin Partitioning . . . . .	31
5.3	Aggregate Message Packet Format . . . . .	33
5.4	Aggregate Messages: PCS Model . . . . .	34
5.5	Aggregate Messages: Traffic Model . . . . .	35
5.6	Aggregate Messages: Epidemic Model . . . . .	36
5.7	Scaling on Cluster: Efficiency and % Remote Events . . . . .	37
6.1	WARPED2 Pending Event Set Data Structures . . . . .	39
6.2	LTSF Queues: Performance on SMP Machine . . . . .	43
6.3	LTSF Partitioning: Intel® Xeon® E5410 Processor . . . . .	44
6.4	LTSF Partitioning: Intel® Xeon® X5675 processor . . . . .	44
6.5	Periodic State Saving: Performance on SMP Machine . . . . .	46
6.6	Periodic State Saving on Cluster: Speedup and Efficiency . . . . .	46

## LIST OF FIGURES

---

6.7	Periodic State Saving on Cluster: Remote Events and Rollbacks . . . . .	47
6.8	Speedup with Spinlocks . . . . .	48
6.9	Rollback and Cancellation Data Structures in WARPED2 . . . . .	49
8.1	Performance Comparison for Fossil Collection Methods . . . . .	65
8.2	Memory Allocator Performance Comparison . . . . .	67
8.3	TCMalloc Speedup with Varying Worker Threads . . . . .	67
8.4	LTSF Queues: % Increase in Average Peak Memory . . . . .	68
8.5	Periodic State Saving: % Decrease in Average Peak Memory on SMP Machine . . . . .	69
8.6	Memory and Performance Comparison for Various GVT Periods . . . . .	70
B.1	PCS Model Logical Processes . . . . .	79
B.2	Intersection Event Sequence . . . . .	81
B.3	Epidemic Model . . . . .	81
C.1	Model Parameters: PCS, Traffic, and Airport . . . . .	83
C.2	Model Parameters: Epidemic . . . . .	84

# List of Tables

9.1	ODROID XU3 Platforms . . . . .	72
A.1	x86 Assessment Platforms . . . . .	77

# List of Algorithms

1	GTW Main Event Processing Loop [1] [2] . . . . .	14
2	WARPED2 Main Event Processing Loop . . . . .	40
3	Ticket Lock Procedures [3] . . . . .	47
4	GVT: Worker Threads . . . . .	56
5	Asynchronous GVT: Manager Thread . . . . .	57
6	Synchronous GVT: Manager Thread . . . . .	58
7	Termination Token Receive Procedure . . . . .	61

# Listings

4.1	Example WARPED2 State Definition . . . . .	22
4.2	Example WARPED2 Event Definition . . . . .	23
4.3	Example WARPED2 LogicalProcess Definition . . . . .	24
4.4	Example WARPED2 Partitioner Definition . . . . .	25
4.5	Exmple WARPED2 Main Definition . . . . .	26

# Chapter 1

## Introduction

Many physical systems can be described by events that occur in discrete time intervals such as communication networks, digital logic circuits, transportation systems, or disease outbreaks. To gain a better understanding of these systems, researchers develop models of the systems and perform simulations on computing platforms. These *Discrete Event Simulation* (DES) models can take a long time to simulate large, complex systems by sequentially processing events which has ultimately led researchers to design parallel algorithms to run simulations on parallel computing platforms. The field of study that deals with parallel algorithms to speed up discrete event simulations is known as Parallel Discrete Event Simulation (PDES).

Parallel algorithms can be written to target many different architectures that support parallelism such as shared-memory multiprocessors, clusters, or any other type of system that support parallel execution. Furthermore, communication between concurrent workers in the system can be achieved by using shared data structures or by passing explicit messages between workers.

The Time Warp Mechanism [4], which is the focus of this text, is one approach to PDES where causal dependencies of events are not enforced before processing an event. A method of undoing erroneous computation and cancelling incorrectly sent events must be implemented in a time warp system to ensure causality of events.

Some time warp systems are designed for only shared memory multiprocessors. These systems minimize communication latencies and allow very fast, simple algorithms because everything can run in a single address space and use shared data structures with pointers to prevent unnecessary copying. However, they

are still limited by the computational power and memory size of the machine. Other time warp systems are completely based on a message passing scheme. These systems can be scaled to any number of machines using interconnection networks as a means of exchanging messages. However, this approach introduces high communication latencies.

The WARPED2 simulation kernel, which is a reimplementaion of the original WARPED simulation kernel, uses a hybrid approach by using shared memory between a set of *worker threads* in a single process to eliminate communication overheads, but uses message passing between processes to allow the system to scale to larger sizes. A dedicated *manager thread* handles all message passing communication between processes. Figure 1.1 illustrates the communication model that is used in WARPED2.

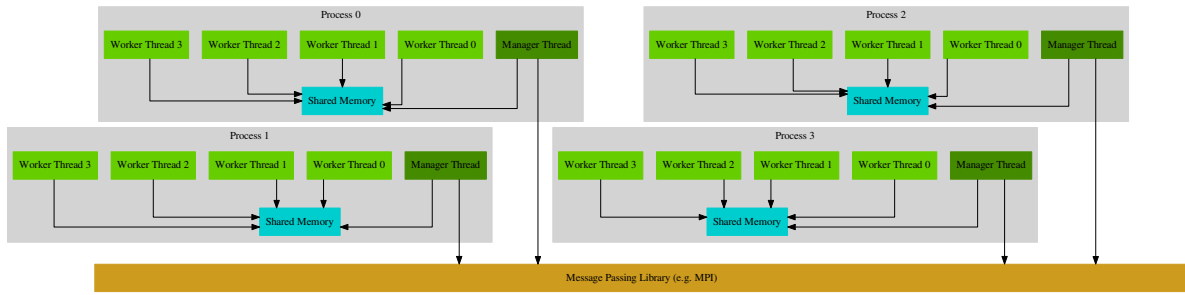


Figure 1.1: Communication Model of WARPED2

This communication model not only allows for high scalability and reduced communication overhead within processes, but allows time warp simulations to follow the critical path better since the worker threads can share a scheduling data structure to process events from. Fewer scheduling data structures creates a bottleneck since multiple worker threads cannot access them simultaneously without race conditions. On the other hand, more scheduling data structures allows more concurrency but spreads out the critical path of execution, allowing more rollbacks so the efficiency decreases.

To reduce communication overheads in both message passing and shared memory communication, partitioning the work between processes and threads will also be explored. Partitioning the work in a parallel discrete event simulation is achieved by simply partitioning the LPs. In WARPED2, a two phase partitioning scheme will be explored. The first phase is for partitiong LPs among processes in order to minimize interprocess communication and the second phase is for further partioning the LPs in a process to

an event scheduling data structure which will localize LP communication among worker threads, allowing better cache locality, and lowering rollbacks. To further reduce message passing communication, messages that are sent to the same process are aggregated and sent together as a single larger message.

The combination of shared memory and message passing communication does create some complications in the implementation of a time warp system. GVT and termination detection algorithms are usually designed for either shared memory or message passing but not both. Using a single message passing algorithm between worker threads and processes would mean that some kind of message passing scheme would have to be implemented for worker threads to communicate. This scheme would still use shared memory for message communication but would still have the overheads of explicit message passing. On the contrary, it is possible to have a single shared memory algorithm that extends to distributed memory systems but would require a shared virtual memory system. However, a shared virtual memory system would still have to transparently use message passing to achieve synchronization. For these reasons, the GVT and termination algorithms developed for WARPED2 include a message passing algorithm and a shared memory algorithm that work in conjunction.

## 1.1 Thesis Overview

The remainder of this thesis is organized as follows:

Chapter 2 contains some background information on parallel simulation and parallel computing that is used in this thesis.

Chapter 3 reviews several of the prominent parallel simulation kernels that use the Time Warp synchronization protocol. The software architecture and target compute platforms for each is described.

Chapter 4 introduces the software architecture and modeling API for the WARPED2 simulation kernel. A basic overview of the communication model and partitioning schemes will also be described.

Chapter 6 describes the basic event scheduling algorithms in WARPED2 as well as the data structures used and the organization of the data structures. The parameters that can be used to tune the performance of the simulation will also be discussed.

Chapter 7 analyzes the problem of GVT and termination detection in distributed systems and describes various algorithms that have been used as well as algorithms used in WARPED2.



Chapter 8 analyzes techniques for managing memory efficiently. A brief overview of state saving techniques, fossil collection, and memory allocation will be analyzed and some of the memory tuning parameters in WARPED2 will be discussed.

Chapter 5 describes the importance of efficient message passing communication and analyzes different approaches. Some preliminary results are shown analyzed to determining the best communication model, partitioning scheme and number of aggregated messages.

Chapter 9 provides some observations of WARPED2 on the ARM big.LITTLE architecture with Heterogeneous Multi-Processing (HMP).

Finally, Chapter 10 contains some concluding remarks and suggestions for future research.

## Chapter 2

# Background

This chapter describes some of the basics of Parallel Discrete Event Simulation (PDES) with a focus on the Time Warp mechanism. Then a brief overview of parallel architectures and parallel communication models and their strengths and weaknesses are compared.

### 2.1 Discrete Event Simulation

Discrete Event Simulation (DES) is a method of modeling the execution of a physical system with a sequence of events that occur at discrete time intervals. A Discrete Event Simulation typically contains three main data structures

**State variables:** A set of variables that describe the current state of the system.

**Simulation Clock:** A clock to measure the progress of the simulation and determine the order of event processing.

**Pending Event Set:** A set of future events that are waiting to be processed.

A *Simulation Model* describes a physical system by a set of *Logical Processes* (LPs). Each LP corresponds to a physical process that is part of the physical system. The LPs interact with timestamped events that dictate the simulation time that the event should be processed. With each event that occurs, and only when an event occurs, the state of the system is updated.

In a *Sequential* Discrete Event Simulation only one event is processed at a time. All pending events are kept in a single list which is sorted by timestamp. The next event to be processed is always the one with lowest timestamp. Each successive event updates the state of the system, advances the simulation clock, and possibly produces new future events. This is clearly not very efficient for large simulations. This method can be improved by realizing that events for different LP's are independant and will only affect the state for a single LP.

## 2.2 Parallel Discrete Event Simulation

*Parallel* Discrete Event Simulation (PDES) is a method running a discrete event simulation on a parallel computer which could be a shared-memory multiprocessor, a distributed memory system such as cluster or NUMA system, or a combination of both. In a parallel discrete event simulation the state of the system is usually split among the logical processes so that each one contains a portion of system's state without any sharing of state variables [5]. In addition to each logical process having it's own separate state, the logical processes also have seperate simulation clocks and pending event sets. Event's from different LP's can then be processed concurrently without the need to worry about sharing state variables and the model can be viewed as concurrent processes operating independantly which contribute to the overall progression of the simulation. This has the potential to increase performance significantly; However, it is possible that events at a receiving LP can be received and processed out of order, violating causality. These *causality errors* can occur because of the independant nature of the logical processes and because the LP's can be processing events at different rates. Causality errors can produce incorrect changes in state variables and incorrect events to be sent to other LP's. Parallel Discrete Event Simulation techniques can be categorized in terms of how causality errors are handled. *Conservative* approaches use methods to detect when possible causality errors might occur and prevent them from ever occurring. *Optimistic* approaches, on the other hand, allow causality errors to occur but use methods to detect and recover from the errors. Generally, the simulation models can be developed without the knowledge of the underlying simulation mechanism. The simulation mechanism is usually implemented in a self-contained module which provides an API for the models and is commonly referred to as the *kernel* or *executive*. For the remainder of this text, only optimistic methods will be discussed, specifically the Time Warp mechanism which is the most widely used optimistic mechanism

used in practice.

### 2.2.1 Time Warp

The Time Warp mechanism is an optimistic method of simulation which is based on the virtual time paradigm [4]. *Virtual Time* provides a method of ordering events in distributed systems which are not described by real time such as a simulation. When used for parallel discrete event simulation, Virtual Time is synonymous with simulation time. The current time of an LP's simulation clock in Time Warp is called the *Local Virtual Time* (LVT).

When an a causality error is detected at an LP (next event to be processed is less than the LVT) the effects of the incorrectly processed event(s) must be undone. The process of undoing the effects is called a *rollback* and the event that triggers a rollback is called a *straggler event*. When a straggler event is detected at an LP, the first step taken during the rollback is to restore the LP's state back to a previous state before the incorrect event(s) were processed. Then the LP must "unsend" the events that were incorrectly sent by sending *negative events* or *anti-messages*. The negative events, when received by the receiving LP will stop the corresponding positive event from being processed or if the corresponding positive message has already been processed at the receiving LP then that LP must also rollback. This processes recursively occurs until all causality errors are corrected. The negative messages are never processed as normal events but serve only to annihilate an generated event produced by an incorrectly processed event.

Jefferson [4] describes how to support rollbacks with three main data structures:

1. Input Queue
2. Output Queue
3. State Queue

Every LP will have a separte input queue, output queue, and state queues. The input queue contains the unprocessed and processed events for the LP that it belongs to. The input queue must be sorted in timestamp order and the LP's must always process from the lowest unprocessed event. The LVT is always the largest timestamped processed event and is used to detect a straggler event. The output queue contains the events that have been sent by the LP that it belongs to which will allow the LP to send anti-messages during a

rollback. The state queue contains previous states of the LP and allows the proper states to be restored during a rollback.

The *Global Virtual Time* (GVT) of the simulation at a given point during the simulation is the minimum of all unprocessed events and the receive times of all events that have been sent but not received [4]. There are numerous algorithms for determining the GVT which will be discussed further in chapter 7. LP's cannot send events that are less than their LVT value and so the GVT acts as a lower bound on how far a rollback can occur. Because no LP's will ever rollback past the GVT value, it is often used to free memory that is no longer needed for the events in the input and output queues and the states in the state queue that have timestamps less than the GVT as well as committing I/O operations that cannot be undone. This process of freeing memory and committing I/O operations is known as *fossil collection*. Fossil collection does not have to be based on the GVT and several other methods of fossil collection have been developed which will also be discussed more in chapter 8.

The need to save the state of the LP's is one of the fundamental overheads in the Time Warp mechanism in terms of both the amount of time it takes to copy the LP's states to save them and the amount of memory that must be used to store them. Because of this, a number of different approaches have been developed to reduce the overhead of state saving such as periodic state saving, incremental state saving, and reverse computation.

## 2.3 Parallel Systems Architectures

Systems that support parallel processing come in many forms. They can generally be characterized by how processors and memories are grouped together. A shared memory system is a single machine which shares a common address space for all processes and can have either a single physical memory unit or multiple memory units. A cluster is a system comprised of multiple machines with separate process address spaces connected over an interconnection network.

### 2.3.1 Shared Memory Multiprocessor Systems

A **Symmetric Multiprocessor (SMP)** is a type of shared memory multiprocessor system where each processor has uniform access to a single shared memory through a common bus. SMP systems cannot scale

very large due to increasing memory contention as the number of processors increasing and for this reason, they usually have 8 or fewer processors. To increase available bandwidth in the system, each processor usually has one or two levels of private caches as well as a shared cache which act as a way limit the number of memory accesses. Figure 2.1 illustrates what a typical SMP system looks like.

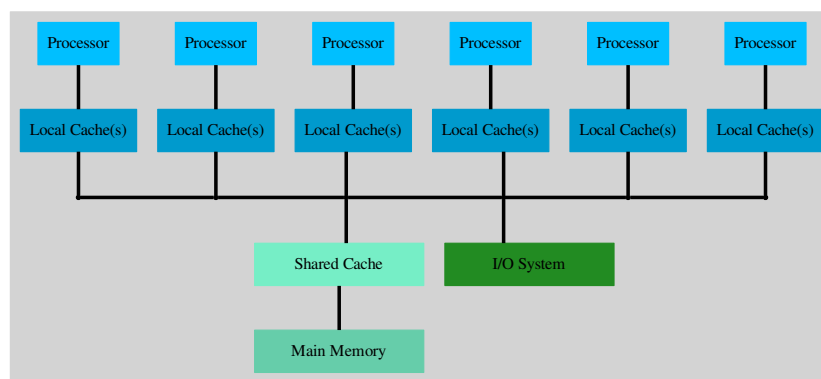


Figure 2.1: SMP System

**A Distributed Shared Memory** system, also known as a Non-Uniform Memory Access (NUMA) system has multiple memories that are distributed. In these systems, the memories are still shared between processors, but access to different memories may have different access times. An interconnection network connects processors and memories together with one or more processors per memory. Because memory access times can vary, software on NUMA systems usually try to keep memory accesses local to a processor. However, NUMA systems can scale much larger than SMP systems because contention to single memory does not necessarily increase with an increasing number of processors. Figure 2.2 illustrates what a typical NUMA system might look like.

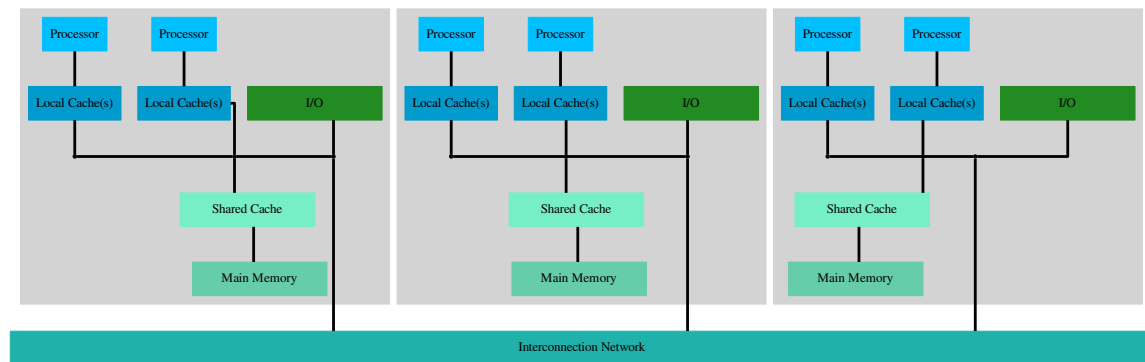


Figure 2.2: NUMA System

### 2.3.2 Clustered Systems

**A Beowulf Cluster** is a type of cluster which appears to the user as a single machine but is actually a loosely coupled set of machines connected together over a local network. A single program is executed by all machines concurrently by launching multiple processes on each machine. A program written for a beowulf cluster typically use some type of message passing to communicate among processes and typically uses parallel communication software such as MPI or PVM. Figure 2.3 illustrates a common realization of a beowulf cluster.

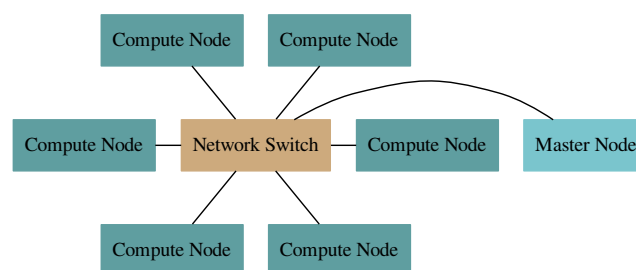


Figure 2.3: Beowulf Cluster

## 2.4 Parallel Systems Communication

Parallel applications are composed of multiple workers that operate independently in parallel and may have to exchange information. The workers in a parallel application can be a process, thread, or any other type of execution context and can be within a shared memory system or a cluster or any type of parallel system. Workers can communicate by either sending explicitly passing messages to each other by means of well defined message formats or by using shared data structures that all workers can access. The former method of communication is known as *message passing* and the latter method of communication is known as *shared memory* communication. Both communication methods are fundamentally different and both have strengths and weaknesses.

### 2.4.1 Message Passing

In a message passing system, workers are completely isolated in different address spaces and communicate only through serialized messages. The formats of the messages must be defined so that the message can be serialized and deserialized by the sender and the receiver, respectively. Message passing can either be synchronous or asynchronous. With synchronous message passing, the send/receive operations must be done in a specific order so that the sender/receiver workers operate together in a synchronized fashion. The send operation at the sender will block until the message is received at the receiver and the receive operation at the receiver will block until the message is fully received. That means the every worker must follow a predictable communication pattern. Workers cannot continue other operations during communication operations and may slow down the whole system. On the other hand, asynchronous communication allows workers to start a send and receive operations and immediately continue without blocking. To allow this, temporary queues must be used to hold pending operations. The workers do not have to follow a predictable communication pattern in this case because the messages will be queued and can be processed at any time and in any order. The main advantage of message passing, in general, is that the number of workers can be scaled to any size as long as the work is partitioned in the right way. Also, the workers can execute in address spaces on different machines and communicate over a local network connection. The biggest disadvantage, however, is an increased communication latency which can be vary large compared to the speed of computation. This makes message passing especially hard for fine-grained parallel applications.



An illustration of simple message passing is shown in figure 2.4 (a).

### Message Passing Interface (MPI)

MPI [6] is an extensive message passing API specification for parallel applications and supports both synchronous and asynchronous forms of communication. It is a standard specification for developers and MPI users and many current implementations exist. The most widely used implementations used in practice include MPICH and OpenMPI.

#### 2.4.2 Shared Memory

The workers in a parallel application can also share a common address space and communicate through shared data structures. The producer worker will insert data directly into the data structure and the consumer worker will remove the data and use it. This takes much less time to transmit data than with a message passing scheme. Furthermore, pointers can be used to eliminate copying overheads. Access to the shared data structures, however, must be protected so that multiple workers do not simultaneously access the same data which could cause unpredictable results. Access to the data structures is usually enforced using lock synchronization mechanisms that protect entire sections of code that are executed by different workers and can end up accessing the same data structures such as mutexes or semaphores. Shared memory data structures may suffer from performance if lots of workers contend for the lock at the same time. For this reason, it is very hard to scale systems that use only shared memory as a means of communication. An illustration of a simple shared memory system is show in figure 2.4 (b).

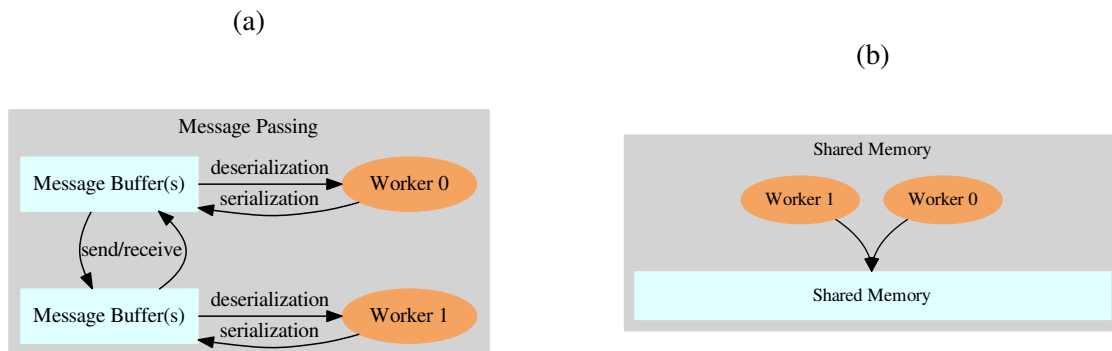


Figure 2.4: Message Passing and Shared Memory Communication

## Chapter 3

# Related Work

This chapter give an overview of some of the most popular Time Warp implementations. For each implementation the design will be described at a high level with a focus on the target architecture. The featured data structures and algorithms for each will also be described as well as the strengths and weaknesses.

### 3.1 Georgia Tech Time Warp (GTW)

Georgia Tech Time Warp was a general purpose Time Warp Simulator designed specifically for shared memory multiprocessors. It is not used anymore, however, because it was only written to target SparcStation and SGI PowerChallenge architectures which are now obsolete. Although GTW is not used any more, it's design has influenced the design of other simulators that are still used widely in practice. GTW simulation models run in a single processes, multi-threaded environment and uses only shared memory to communicate between threads that are bound to single processor.

The LP's for all models are statically allocated to a single thread so that events for the LP's are processed only on a single processor. The pending event set is distributed among threads with each having its own data structures for the set of LPs that belong to it. Since the threads can only run on a single processor, the data structures also belong to a single processor. The pending event set for each processor consists of three main data structures listed below [1]:

1. The *Message Queue* is a linked list that contains positive messages that are destined for the LP's

mapped to the owning processor. Access to the message queue must be synchronized because it can be accessed by tasks running on any processor.

2. The *Cancel Queue* is a linked list that serves exactly the same purpose as the message queue except that it contains only negative messages(anti-messages). Access to this queue must also be synchronized.
3. The *Event Queue* is used to hold unprocessed and processed events and is directly used to schedule events to be processed. The event queue is actually made up of different data structures, one for processed events and one for unprocessed events. The processed events are contained within a doubly linked list and the unprocessed events are contained within a priority queue which can be configured to be either a calendar queue or a skew heap depending on a user configuration.

When messages are sent between LP's, they are inserted directly in the message queue or the cancel queue depending on whether they are positive or negative. Each thread processes events by first moving events from the message queue to the event queue and processing rollbacks. Then, the messages from the cancel queue are removed and cancellations are processed and any more rollbacks are processed. One or more of the smallest events from the event queue are then processed and added to the processed event list. This procedure is repeated over and over again by all processors. Psuedocode for the main event processing loop in GTW is show in figure 1.

```
while eventQ is not empty do
    move messages from MsgQ to EvQ and process any rollbacks
    remove anti-messages from CanQ, process annihilations and rollbacks
    remove N smallest timestamped events E from EvQ
    process the N events
```

**Algorithm 1:** GTW Main Event Processing Loop [1] [2]

To avoid accessing the message queues and cancel queues too often, which is a contention point, a larger value of N can be used. GTW calls this batch processing and N is the batch interval. With batch processing, multiple events will be processed at a time without processing any rollbacks or cancellations.

Since GTW is only uses shared memory communication, anti-messages do not need to be explicitly sent. Only a pointer to the event that needs to be cancelled is necessary. Fujimoto calls this method *direct*

*cancellation*. Also by only using shared memory, GVT can be calculated very quickly using shared data structures instead of passing messages around to all processors. The downfall of GTW, however is that it was limited to only a single multiprocessor machine and it was only designed and optimized for specific architectures.

GTW also imposed some unnecessary requirements for the developer of particular simulation models. The partitioning of the LP's among processors must be done in the simulation model during initialization. That means that the model developer must understand some the features of the underlying architecture such as the number of processors, to effectively partition the LP's. Furthermore, the initial partitioning of the LP's is hard to dynamically balance during the simulation because there are no separate input queues for each LP but rather a single message queue to hold all unprocessed events for each processor.

## 3.2 Clustered Time Warp (CTW)

Clustered Time Warp [7] (CTW) uses a hybrid approach by processing events within a *cluster* of LP's sequentially and using the Time Warp mechanism between the clusters. This design was chosen because it works well for digital logic simulation which tend to have localized computation within a group of LP's. Furthermore, digital logic simulation tends to have low computational granularity and lot's of LP's which can lead to a lot of rollbacks and a large memory footprint in a traditional Time Warp simulator. CTW is implemented for shared memory multiprocessors but only uses shared memory for use with a custom message passing system so that it can better support NUMA architectures. It is not designed for use on a beowulf cluster.

Each cluster of LPs has a timezone table, an output queue, and a set of LP's which each have an input queue and a state queue. The timezones in the timezone table are divided by timestamps of the events received from LP's on different clusters. Whenever an event is received from a remote cluster, a new timezone is added. Only a single output queue is needed per cluster because anti-messages can only be sent between clusters and not between LP's on the same cluster.

When a straggler event arrives at a cluster, all of the LP's that have processed events that are greater than the timestamp of the straggler will be rolled back. This rollback scheme is called *clustered rollback*. The alternative to clustered rollback is *local rollback*. In a local rollback scheme the straggler event would be

inserted into the receiver LP's input queue and the LP will roll back when it is detected. Although clustered rollbacks may cause some LP's to be rolled back unnecessarily leading to slower computation, the approach was chosen for CTW because processed events will not have to be saved which requires less memory.

CTW uses a form of infrequent state savings with the timezone table used to determine the frequency. When an event is about to be processed for an LP, the timezone of the last processed event is looked up and if event that is about to be processed is in a different timezone then the state is saved. This approach in which all LP's save their state every time an event is processed in a new timezone regardless of whether it receives an event from a remote cluster is called *local checkpointing*. This method reduces the state saving frequency more than a *clustered checkpointing* approach in which only the LP that receives an event from a remote cluster saves its state. The local checkpointing approach was chosen for CTW because a larger state saving frequency can increase rollback computation and it can even lead to more memory consumption because more events must be saved for coast forwarding during state restoration.

### 3.3 Rensselaer's Optimistic Simulation System (ROSS)

ROSS [8] is a general purpose simulator that is capable of running both conservatively and optimistically synchronized parallel simulations as well as sequential simulations. It is most often used for optimistic parallel simulations which is implemented with the time warp mechanism. ROSS started as a reimplementation of GTW and is still modeled after it but has had many enhancements. The same basic event scheduling mechanism is used but ROSS supports different priority queue implementations and different algorithms are used for fossil collection, state saving, and gvt calculation. In addition, ROSS uses processes instead of threads and uses message passing explicitly with MPI instead of shared memory for communication among the processes.

Just as in GTW, ROSS maps every LP to a process and each process contains its own pending event set structures. No locks are needed explicitly within each process because there are no shared data structures among processes. The data structures are very similar to those used in GTW but have a different naming convention. The main data structures in ROSS are:

1. The *Event Queue* is analogous to the message queue in GTW. It contains the positive events for all

LP's in the corresponding process. In addition, an event queue is used to hold all remote events regardless of whether it is positive or negative. The event queue is implemented as a linked list.

2. The *Cancel Queue* is a linked list which is used to hold negative events for all LP's for the corresponding process. The cancel queue is used in the exact same way as GTW except that no locks are necessary.
3. The *Priority Queue* is analogous to the unprocessed event queue in GTW and contains events in timestamp order. ROSS also allows the priority queue to be implemented as a calendar queue, heap, splay tree, or avl tree depending on user configuration.

To reduce the time taken to fossil collect LPs, ROSS further divides the LPs in a single process into groups called Kernel Processes (KPs). All LPs in a KP are rolled back together and fossil collected together in a similar manner as a clustered rollback in CTW.

### 3.4 WARPED

WARPED is the predecessor of WARPED2 and serves as the basis for the design and architecture of WARPED2. WARPED, unlike the previous time warp simulators, follows Jefferson's classic model with LPs having their own input, output, and state queues. It started as a completely processed based solution with only message passing for communication. Eventually, with the development of multicore processors, each process was extended into multiple threads to further enhance concurrent processing of events. Over the years, though, with many students developing new algorithms, the complexity of WARPED became unmaintainable, mainly because of the great deal of indirection which, although made for a super configurable and modular design, became too complex for new students to learn it and enhance it. This has led to the development of WARPED2 which is detailed throughout the remainder of this document.

## 3.5 Others

### 3.5.1 The ROME OpTimistic Simulator (ROOT-Sim)

ROOT-Sim is another general purpose Time Warp Simulator that uses message passing via MPI [9]. Like WARPED, ROOT-Sim is a more classic Time Warp implementation with each LP having their own input, state and output queues. What sets ROOT-Sim apart from other time warp simulators is the internal instrumentation tool, Dynamic Memory Logger and Restorer (DyMeLoR) that can optimize memory usage. DyMeLoR can determine whether the simulation models are better fit for copy-state saving or incremental state saving and transparently switch between them during runtime. Another service that ROOT-Sim offers is the Committed and Consistent Global Snapshot (CCGS). After each GVT calculation, ROOT-Sim transparently rebuilds a global snapshot of all LP states. Each LP can access its portion of the global snapshot on every GVT calculation. With this service, a simulation model can implement any custom global snapshot algorithm.

### 3.5.2 ROSS-MT

ROSS-MT [10] is a multi-threaded version of ROSS which is optimized to use shared memory to communicate among threads. The use of message passing with MPI was completely removed and all events are sent by direct insertion into the event queues. To reduce the added contention on the event queues, they are further divided by possible senders. ROSS-MT also optimizes the memory management so that it is more NUMA aware.

## Chapter 4

# The WARPED2 Simulation Kernel

This chapter describes the software architecture of the WARPED2 simulation kernel. The main components are described and the dependencies between each of them. The modeling API is also described and an example model is shown to illustrate the API.

### 4.1 The Software Architecture of WARPED2

The WARPED2 simulation kernel uses a modular design to make it more configurable, extendable, and maintainable. All components are configured and created at startup individually and subcomponents are accessed through pointers. Furthermore, WARPED2 is written in C++ so each component can also be an object of derived subclass that implement a well-defined interface by a base class. The main central component is the event dispatcher which is responsible for calling the LP callback methods during the course of the simulation. The event dispatcher can be configured for sequential simulation or a time warp simulation. The sequential simulation does not depend on any other components and contains only a single list of unprocessed events. The time warp event dispatcher on the other hand depends on multiple components that implement specialized algorithms for event scheduling, state saving, cancellation, GVT, termination, interprocess communication, and statistics. The main components and how they depend on each other are illustrated in figure 4.1.



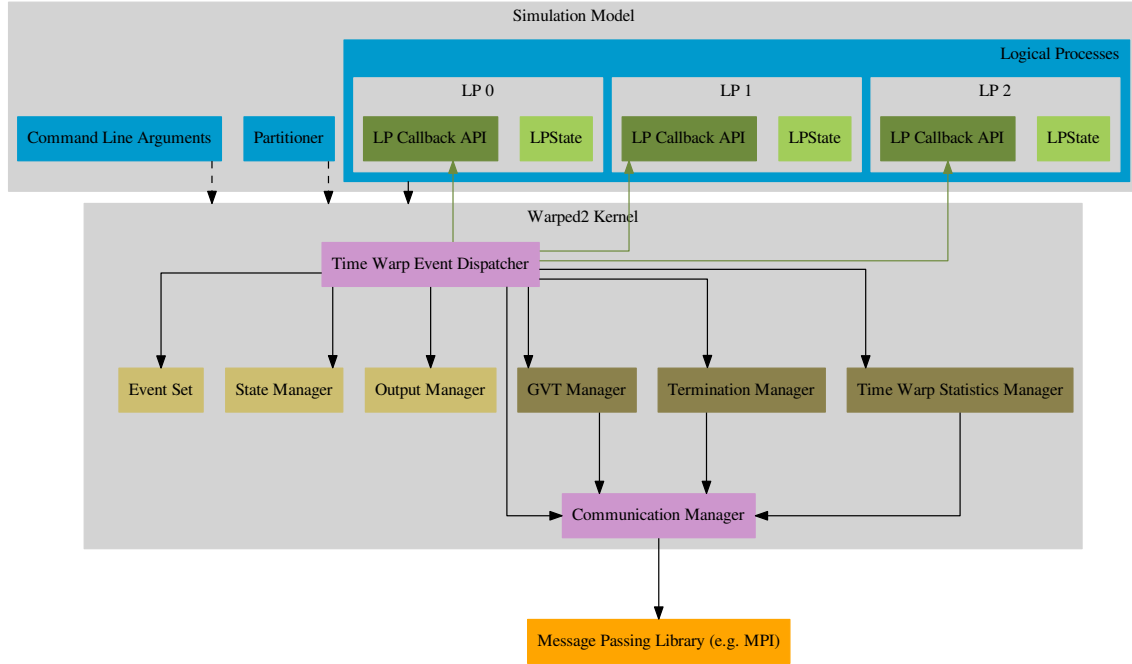


Figure 4.1: Time Warp Components in WARPED2

The time warp components can be categorized as either a local time warp component or a global time warp component. The local time warp components are used only for the local control mechanism of the individual LP's such as rolling back LPs and fossil collecting LPs whereas the global time warp components are concerned with the global control mechanisms such as GVT, termination detection and statistics counting. The global time warp components must be able to communicate with all other processes in the system so that it is possible to determine the global state of the system.

#### 4.1.1 Local Time Warp Components

**The Event Set** contains the data structures for all of the unprocessed and processed events for the LPs that are local to the process. This includes the input queues for each LP as well as a scheduling data structure. The event set provides methods for retrieving the next event, processing a rollback, and fossil collecting the processed events. All the data structures that have pending events must necessarily be thread safe.

**The Output Manager** contains all of the output queues and implements a single cancellation technique. The `OutputManager` base class provides a set of methods that a derived class must implement. The derived class must have methods for adding an event to an output queue, processing a rollback, and fossil collecting old output events. Currently, WARPED2 only implements aggressive cancellation but may support more cancellation techniques in the future.

**The State Manager** contains all of the state queues and implements a single technique for state saving and state restoration. Similar to the output manager, the state manager is based on a `StateManager` base class. The derived class provide methods for saving the state of an LP, restoring the state of an LP, and fossil collecting old states. Currently, WARPED2 only implements periodic state saving.

#### 4.1.2 Global Time Warp Components and Communication Manager

The communication manager provides an interface between the underlying message passing library and the global time warp components in the WARPED2 simulation kernel. Any interprocess communication must go through the communication manager, including remote events that must be sent to another process or received from another process. Any class that has to do interprocess communication must register message types and a receive callback function for each type with the communication manager.

The GVT manager is actually two classes that implement two algorithms for determining the Global Virtual Time of the simulation. Because of the nature of WARPED2, the GVT algorithm has a message passing algorithm to use between the set of processes and shared memory subalgorithm to used between worker threads. The termination manager implements an algorithm for determining when all processes become inactive and initiates termination when that occurs. A more detailed description of the GVT and termination detection algorithms used in WARPED2 are given in chapter 7. The Time Warp statistics manager keeps track of all local statistics and provides methods for global reductions on the them.

## 4.2 The Modeling API of WARPED2

The modeling interface of `warped2` is a set of abstract base classes that contain methods to be implemented in a derived class in the simulation model. The three main base class types that must be implemented are

---

```
WARPED_DEFINE_LP_STATE_STRUCT(ExampleState) {  
    unsigned int messages_received;  
};
```

---

Listing 4.1: Example WARPED2 State Definition

LogicalProcess, LPState, and Event. Optionally, the user may create a custom partitioner from the Partitioner base class. In the remainder of this section, each class is described in more detail and sample implementations are shown for each.

### 4.2.1 The LPState Structure

The state of the LPs must be defined with the `warped_define_lp_state_struct` macro which automatically derives from an intermediate structure that defines a necessary clone method so that the model developer does not have to. It is needed to ensure that the warped2 kernel can save a copy of the state and restore the state from a pointer to the LogicalProcess base class. A simple example of a LP state that contains just message counts is shown below in listing 4.1.

If the state contains complex data structures that contain pointers then the default copy constructor and default copy assignment operator will only perform shallow copies. In this case the user must implement a custom copy constructor or a custom copy assignment operator or both. The copy constructor will define the behavior for saving the state whereas the copy assignment operator will define the behavior for restoring the state. Note that the copy assignment operator will most likely not be needed since a shallow copy will usually suffice for restoring the state.

### 4.2.2 The Event Class

The event base class is used as the basis for creating model specific events. The user must implement at least two methods: (1) `receiverName()` and (2) `timestamp()`. It is necessary so that the name of the receiver and receive time, respectively, can be obtained for each instance of an event within the kernel. The user must also register all member variables with the serialization API so that a storage order can be defined for events that are sent and received through the message passing system. To do this, the `WARPED_REGISTER_SERIALIZABLE_MEMBERS` macro is provided. All member variable

---

```
class ExampleEvent : public warped::Event {
public:

    const std::string& receiverName() const { return receiver_name_; }
    unsigned int timestamp() const { return time_stamp_; }

    std::string receiver_name_;
    unsigned int timestamp_;

    WARPED_REGISTER_SERIALIZABLE_MEMBERS(cereal::base_class<warped::Event>(this
    ),
                                     receiver_name_, timestamp_)
};
WARPED_REGISTER_POLYMORPHIC_SERIALIZABLE_CLASS(ExampleEvent)
```

---

Listing 4.2: Example WARPED2 Event Definition

must be passed to this macro as well as `cereal::base_class<warped::Event>(this)` to ensure that all members that are inherited are also serialized. The order that the members are listed is completely arbitrary and does not matter. In addition, the derived event type must be registered using the `WARPED_REGISTER_POLYMORPHIC_SERIALIZABLE_CLASS` macro. A basic example of an event implementation is shown below in listing 4.2.

### 4.2.3 The LogicalProcess class

The most important class definition in the simulation model is the LogicalProcess class. The implementation of the LogicalProcess class defines the callback functions that the event dispatcher with the warped2 kernel calls and thus it defines the behavior of the simulation. The user must include a single LPState as a data member of the LogicalProcess and provide three callback method implementations:

1. The `initializeLP` method is called to perform any initializations of an LP that must be done prior to the start of the simulation and must return a set of initial events.
2. The `receiveEvent` method is called to perform the forward computation based on the event that is passed. The implementation of this method must process the event by updating the state of the LP and returning a set of new events with future timestamps.
3. The `getState` method provides a way for the warped2 kernel to get the current state of the LP so

---

```
class ExampleLP : public warped::LogicalProcess {
public:

    warped::LPState& getState() { return this->state_; }

    std::vector<std::shared_ptr<warped::Event> > initializeLP() override {
        this->registerRNG(this->rng_);
        std::vector<std::shared_ptr<warped::Event> > events;
        ...
        return events;
    }

    std::vector<std::shared_ptr<warped::Event>> receiveEvent(const warped::
        Event& event) {
        ++this->state_.messages_received;
        std::vector<std::shared_ptr<warped::Event> > response_events;
        ...
        return response_events;
    }

    ExampleState state_;
};
```

---

Listing 4.3: Example WARPED2 LogicalProcess Definition

that it can be saved in the state queue.

It is necessary that at least one LP has an initial event that is returned by `initializeLP`, otherwise no events can be received and simulation will terminate immediately. Also note that it will be called once for *every* LP instance so it is possible that initial events are returned only in some cases. An example of a `LogicalProcess` implementation is shown below in listing 4.3.

#### 4.2.4 The Partitioner class

The `warped2` kernel already provides a set of partitioners but the model developer can define their own partitioner that is customized for the model. The user must derive from the `Partitioner` base class and implement just a single method which takes a vector of all LPs and the number of partitions desired and returns a vector of vectors of LP's. In general, the partitioner should work for any number of partitions and not impose any constraints because the partition method is called back from the kernel. A simple template of a partitioner is shown in listing 4.4.

---

```
class ExamplePartitioner : public Partitioner {
    std::vector<std::vector<LogicalProcess*>>
    partition(const std::vector<LogicalProcess*>& lps, const unsigned int
        num_partitions) {
        std::vector<std::vector<LogicalProcess*>> parts;
        ...
        return parts;
    }
};
```

---

Listing 4.4: Example WARPED2 Partitioner Definition

### 4.2.5 Random Number Generation

If the simulation model must use random number generators, then they must all be registered with the warped2 kernel so that the state of the random number generator can be saved and restored in case of rollbacks and provide deterministic result. The random number generators can be any type as long as they implement the `<<` operator and `>>` operator to allow the kernel to save and restore the internal state of the random number generator. The random number generators in the C++11 standard libraries [11] all fit this requirement and can all be used so it is not necessary to always build a custom random number generator. To register the random number generator, the `registerRNG` template function must be used which is a member of the `LogicalProcess` class. All LP's must have separate random number generators and must be registered in the `initializeLP` callback function as shown in listing 4.3.

### 4.2.6 Command Line Arguments and the Kernel Entry Point

Once all the necessary structures and classes have been defined, the model's main function must be implemented which is where all calls into the kernel are made. First, the model can create specific command line arguments but they must be registered with the kernel. This must be done first so that it can be passed to the constructor of a `Simulation` instance and so that the command line arguments can be displayed without unnecessarily running a simulation. The kernel uses a third-party library called TCLAP for command line arguments. After setting up the command line arguments, all of the LP objects and optionally a partitioner object must be instantiated and passed to the kernel through the `simulate` method of `Simulation` object. Two versions of the `simulate` method is available, one for a model with a custom partitioner and one

without as listed below:

1. `void simulate(const std::vector<LogicalProcess*>& lps);`
2. `void simulate(const std::vector<LogicalProcess*>& lps,  
std::unique_ptr<Partitioner> partitioner);`

A sample implementation of a models main function is shown in listing 4.5.

---

```
int main(int argc, const char **argv) {
    unsigned int num_lps = 10000;

    TCLAP::ValueArg<unsigned int> num_lps_arg("o", "lp-count", "Number_of_lp's"
        , false,
            num_lps, "unsigned_int");
    std::vector<TCLAP::Arg*> cmd_line_args = { &num_lps_arg };
    warped::Simulation simulation {"Example_Simulation", argc, argv,
        cmd_line_args};

    num_lps = num_lps_arg.getValue();
    std::vector<ExampleLP> lps;
    for (unsigned int i = 0; i < num_lps; i++) {
        std::string name = std::string("LP_") + std::to_string(i);
        lps.emplace_back(name, 1, i);
    }

    std::vector<warped::LogicalProcess*> lp_pointers;
    for (auto& lp : lps) {
        lp_pointers.push_back(&lp);
    }
    simulation.simulate(lp_pointers);

    return 0;
}
```

---

Listing 4.5: Exmple WARPED2 Main Definition

### 4.3 Plans of Study

Experimental results will be presented throughout the remaining chapters as the various aspects of WARPED2 are discussed. All chapters except for chapter 9 will contain experimental results for only x86 SMP nodes and clusters and will be targeted towards understanding and determining the best design options and configurations. Chapter 9 will contain separate experiments and observations on the ARM big.LITTLE HMP platform.

The single node SMP experiments will be run on an Intel Xeon X5675 processor. The intel X5675 has 6 cores with hyperthreading so that 12 simultaneous threads are supported. Cluster experiments will be run on Intel Xeon E5410. The Intel Xeon E5410 is a quad core processor but each machine contains two sockets so that 8 cores are available. A summary of the architectural features and runtime systems for both machines are summarized in table A.1 in appendix A.

#### 4.3.1 Experiments on x86 SMP Nodes

#### 4.3.2 Experiments x86 Clusters

#### 4.3.3 Experiments ARM big.LITTLE Nodes



## Chapter 5

# Interprocess Communication and Partitioning

This chapter first provides an overview of how to implement a system with a hybrid communication model. Then various partitioning schemes that are implemented in WARPED2 are discussed and analyzed. Lastly, the message aggregation technique that is used is described and the behavior with various aggregate message counts are explored.

### 5.1 Multithreaded Message Passing with MPI

In a hybrid communication model, the choice of which threads and how many threads should be involved in message passing communication is an important design decision that can significantly impact performance and stability of the simulator.

The MPI standard specifies four level of thread support that each implementation may support:

**MPI\_THREAD\_SINGLE:** The application uses only a single thread.

**MPI\_THREAD\_FUNNELED:** The application is multithreaded but only a single thread will make MPI calls.

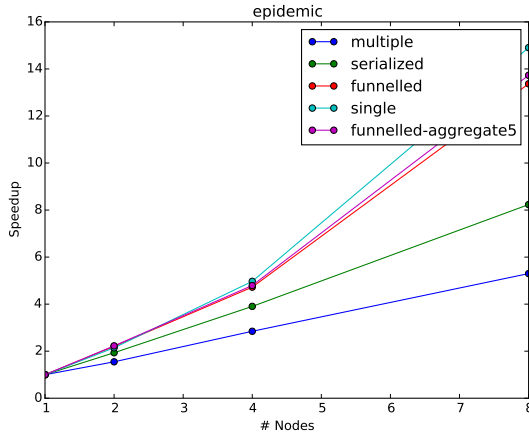
**MPI\_THREAD\_SERIALIZED:** The application is multithreaded but only a single thread at a time will make MPI calls.

**MPI\_THREAD\_MULTIPLE:** The application is multithreaded and threads have no restrictions.

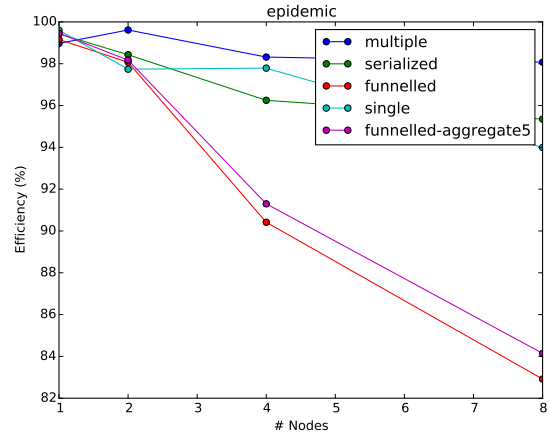
Funnelling the communication to the manager thread can increase latency because an extra step for temporarily queueing the message is necessary. Furthermore, with too fine of a granularity, the number of messages can pile up and create a bottleneck in the communication and cause instability. Serialized or full multiple thread communication, however, can increase contention for access to MPI library calls and slow down the worker threads. Furthermore, it forces worker threads to take more time for serialization, deserialization, and memory allocation/deallocation.

The plots in figure 5.1 summarize the performance of single, funnelled, serialized, and multiple thread communication in WARPED2 for the Epidemic, PCS, and Traffic models. Furthermore, funnelled communication with aggregate messages is also shown.

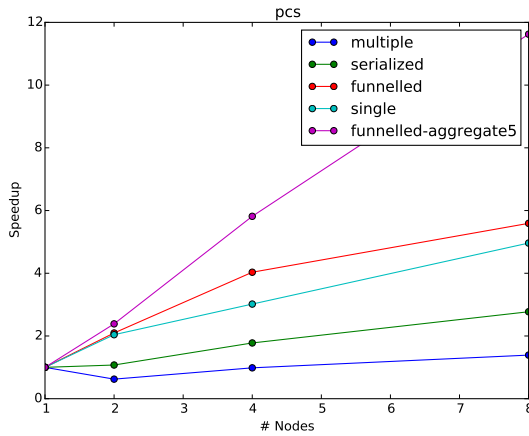
For serialized communication, a single global spinlock is used to protect access to all MPI library calls. For the multithreaded communication models, 7 worker threads and a manager thread are used per node and for single thread communication, 8 processes per node are used. Aggregated messages contain 5 ordinary messages.



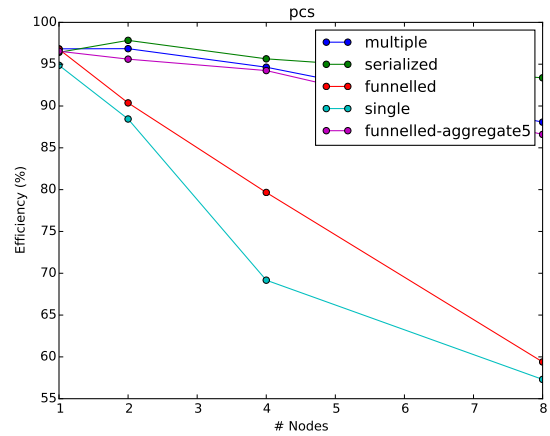
Speedup - Epidemic



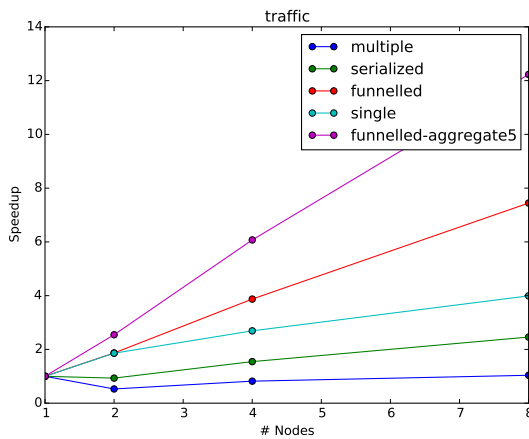
Efficiency - Epidemic



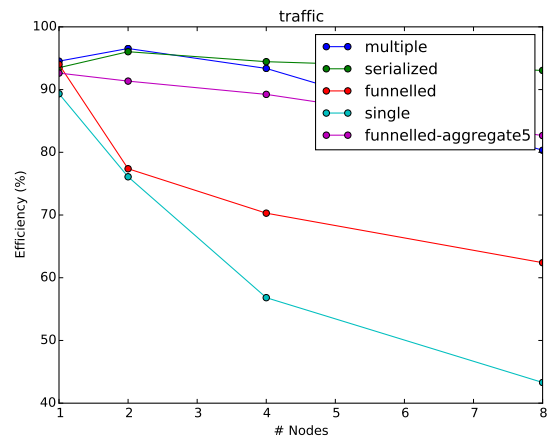
Speedup - PCS



Efficiency - PCS



Speedup - Traffic



Efficiency - Traffic

Figure 5.1: Communication with Various Simulation Models

## 5.2 Partitioning

### Round-Robin partitioning

The block size is the number of contiguous LPs that are allocated to a partition at a time. The block size can be between 1 and  $NumLPs/NumProcesses$  where the former is a true round-robin partitioning scheme and the latter is a block partitioning scheme.

A block size in between the extreme values can be used to

**Profile-Guided** partitioning is based on profile data that must be collected with a sequential simulation. The profile data contains a count of events for each communication pair of LPs. The partitioner uses that data to build a weighted graph and uses a graph partitioning tool to create partitions that minimize the cut-set.

Profile guided partitioning is good for any number of partitions!!!!



Round-Robin with blocksize = 1

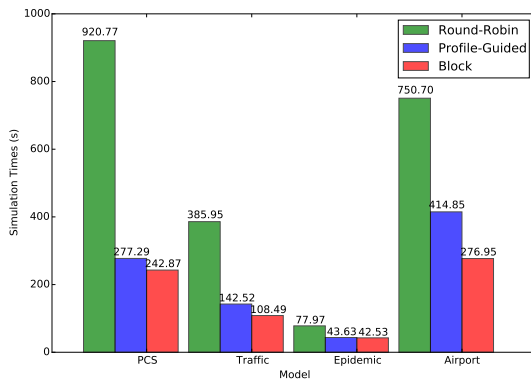


Round-Robin with blocksize = 2

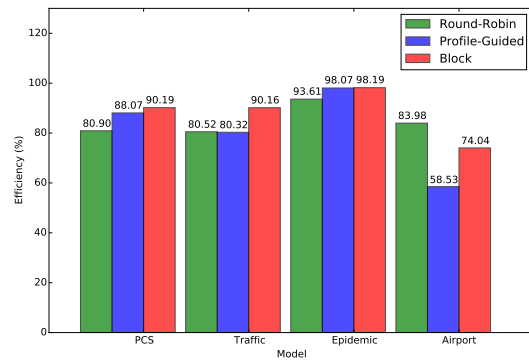


Round-Robin with blocksize =  $NumLPs/NumProcesses$

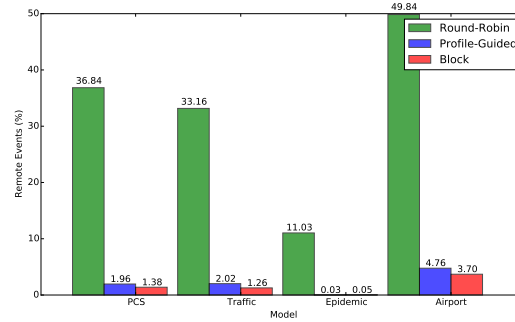
Figure 5.2: Round Robin Partitioning



Simulation Times (s)



Efficiency



% Remote Events

### 5.3 Message Aggregation

In general, MPI implementations are usually designed for coarse-grained parallelism with large message sizes.

The individual messages are aggregated into a single aggregate message until the aggregate message contains  $N$  individual messages or until a control message is included. A control message could be a GVT token, a termination token, a GVT update message, or termination status update message.

The combined message is started with a header which includes the number of individual messages and the lengths of each individual message. The location of each individual message can be found by adding the length of the header with the length of the previous message and the length of the header is easily determined by knowing how many individual message there are. Figure 5.3 illustrates the format of the aggregated message with the sizes of each field.

Size of the aggregated messages in bytes are:

$$4 * (N + 1) + \sum_{n=0}^{N-1} length[n]$$

An alternative way of implementing message aggregation would be to combine messages until a certain size is reached rather than a number of messages.

Another alternative approach, that would minimize the size of the aggregated message would be to use a special character to separate messages so that the header is not necessary.

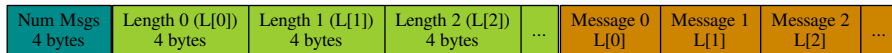


Figure 5.3: Aggregate Message Packet Format

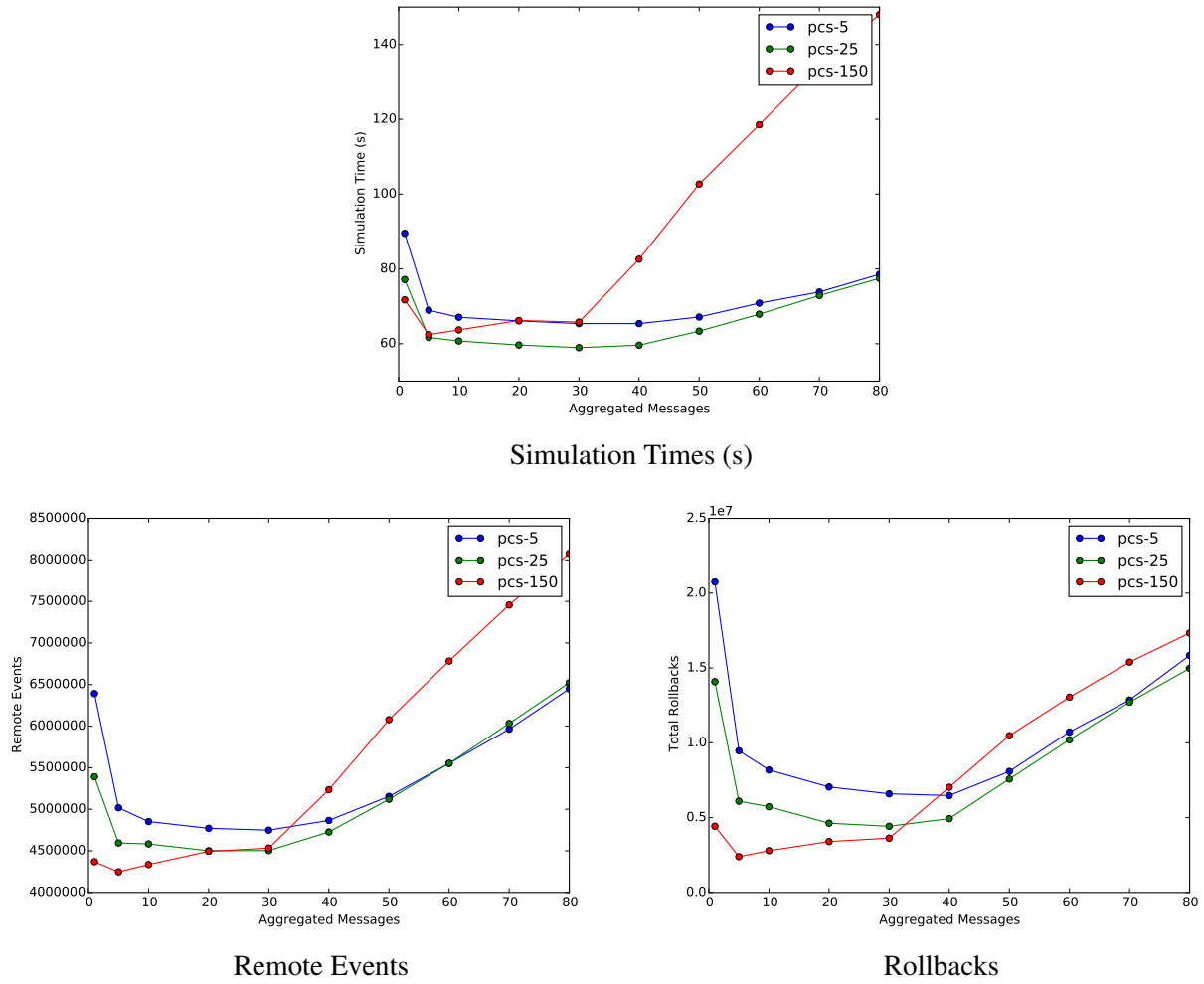


Figure 5.4: Aggregate Messages: PCS Model

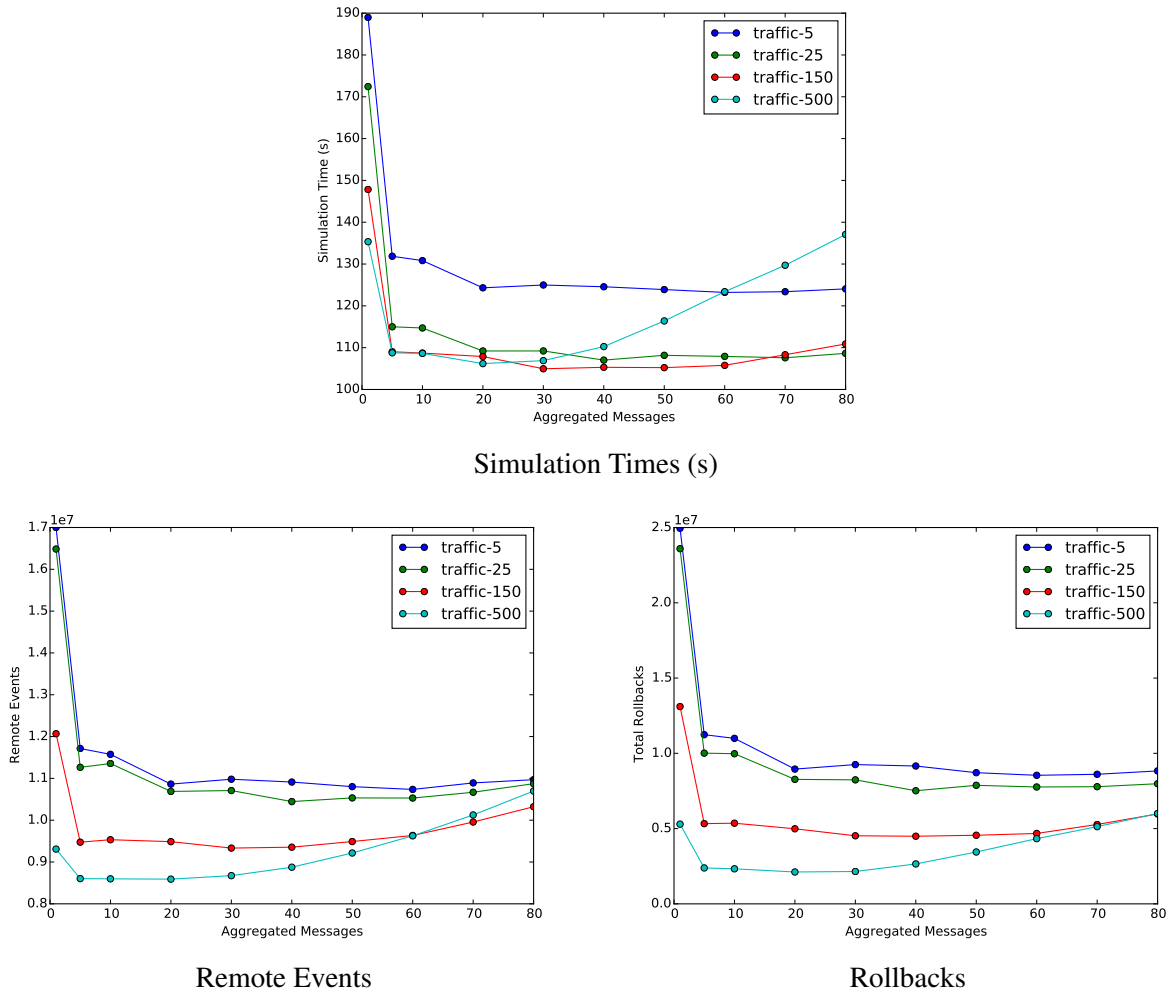


Figure 5.5: Aggregate Messages: Traffic Model



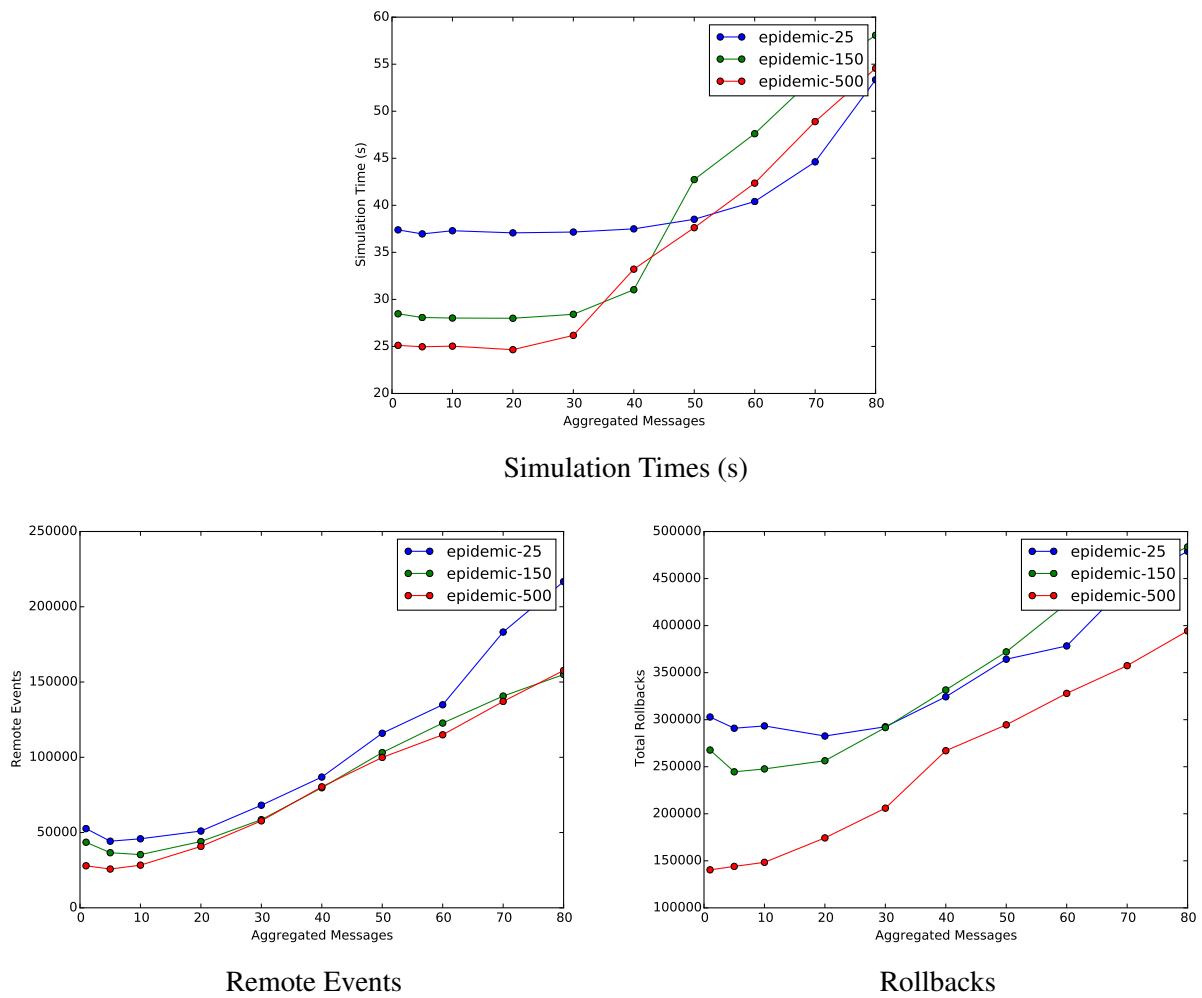


Figure 5.6: Aggregate Messages: Epidemic Model

## 5.4 Scaling up the Simulation Model

All previous simulation results have been from simulation models that were configured for 10,000 LPs. This section analyzes the behavior of the simulations when the number of LPs are scaled up to larger numbers on an 8 node cluster.

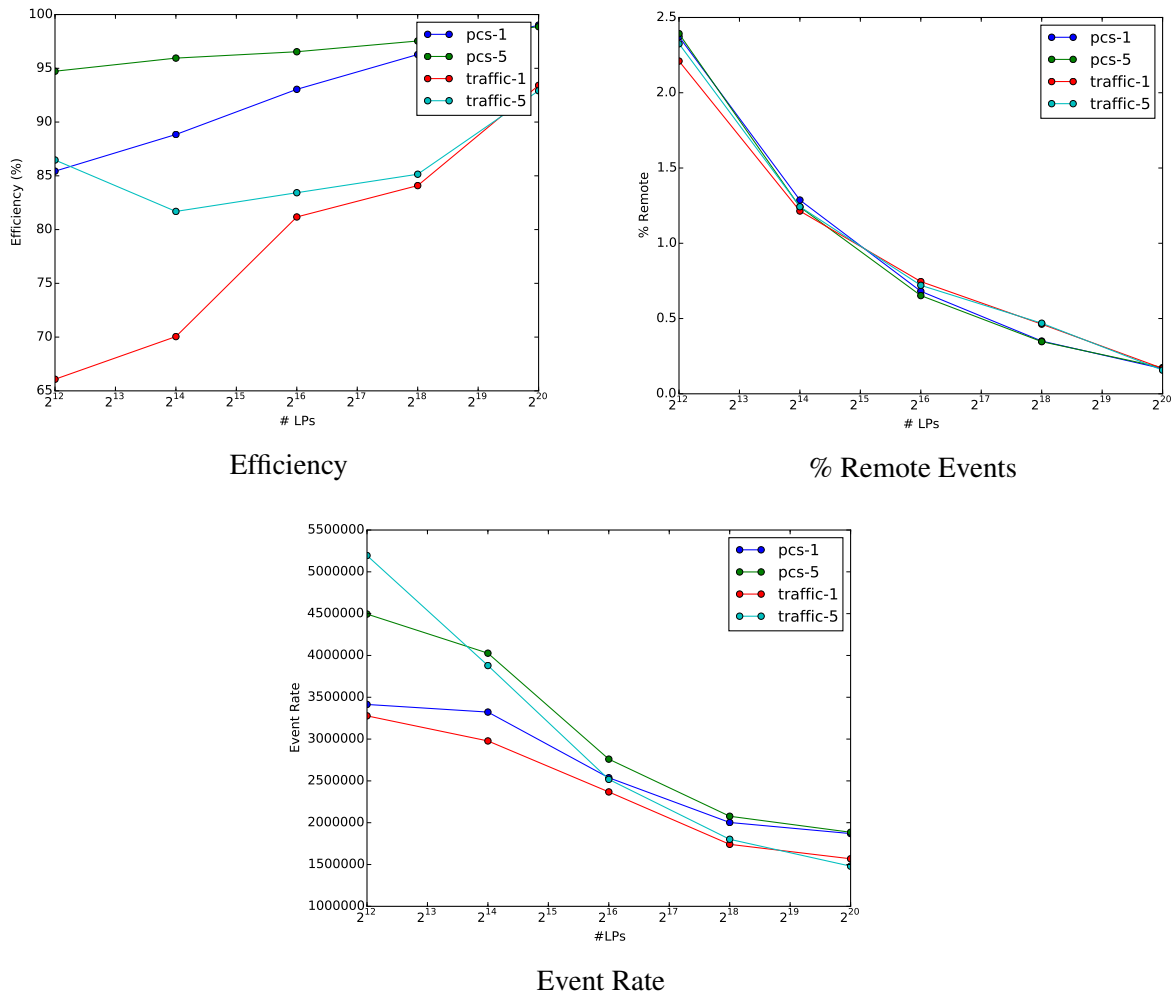


Figure 5.7: Scaling on Cluster: Efficiency and % Remote Events

## Chapter 6

# WARPED2 Data Structures and Organization

### 6.1 Pending Event Set Data Structures

The pending event set is the set of events that are ready to be processed at any given time. Every process contains a logically separate pending event set for a dedicated set of LPs. Events that are being sent to an LP that is local to the process are directly inserted into the pending event set. Events that must be sent to a remote process are inserted into a remote event queue where the manager thread will remove it, form an event message and send the message to the receiving process. The manager thread at the receiving process will then unpack the message and insert it into the pending event set. The pending event set is made up of a queue for each LP known as an *unprocessed queue* which is directly accessible from all threads and allows for the sending of events.

The unprocessed queue contains both positive events and anti-messages and remains sorted at all times. The anti-messages are given priority over their positive counterparts so that the anti-messages are always processed first and any unnecessary rollbacks can be prevented which could create a chain of rollbacks that could cause instability [12]. To avoid any unnecessary copying from the simulation model to the kernel and with the kernel, the unprocessed queue only contains pointers to the unprocessed events which are passed in by the simulation model.

A second type of data structure, an *LTSF (Lowest TimeStamp First) Queue*, provides order among events from multiple LPs. At most, a single event from each LP is *scheduled* into a single LTSF queue. Scheduling an event to an LTSF queue means only inserting a pointer to an event into the LTSF queue with no removal from the unprocessed queue. Every process has one or more LTSF queues which the worker threads obtain the lowest timestamped event to process.

A third type of data structure is also used to keep track of the events that have been scheduled or are currently being processed but organized by receiving LP. It is used for two main reasons. First, when inserting an event into the receiving LPs unprocessed queue, it provides a way to determine if a smaller event has already been scheduled for the LP. The new event can then be scheduled in place of the already scheduled event. Second, it serves to prevent multiple worker threads from processing events from the same LP which could cause out of order committing of events without rolling back as would be necessary.

Figure 6.1 illustrates the pending event set data structures in WARPED2.

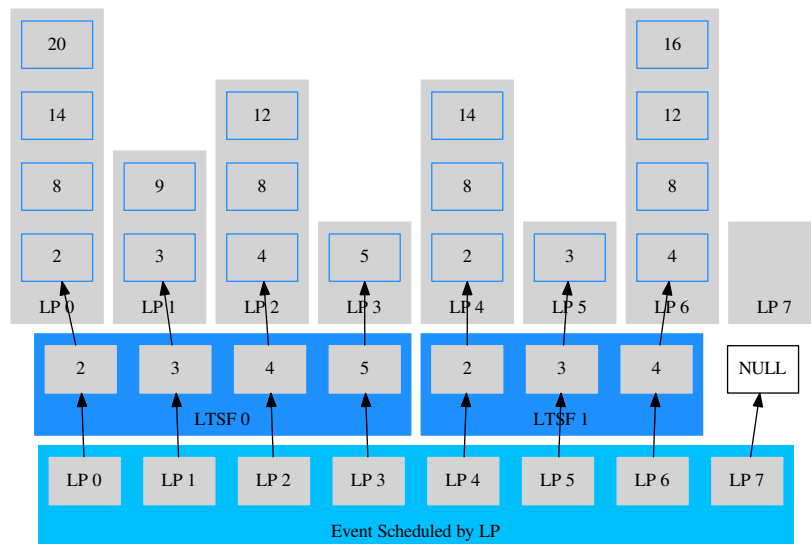


Figure 6.1: WARPED2 Pending Event Set Data Structures

## 6.2 Processing Events

One or more of the worker threads are assigned to each LTSF Queue to process events from and they all follow the exact same procedure. First, an event is removed from an LTSF queue to be processed but remains in the unprocessed queue until it is completely processed or cancelled out. The event is first checked against the last processed event of the receiving LP to see if it is a straggler, and rolled back if necessary. An anti-message is also considered to be a straggler if its positive counterpart was the last processed event for the LP since WARPED2 makes the assumption that message order will always be preserved. So if the event is an anti-message, the positive counterpart is assumed to be in the LPs unprocessed queue after rolling back and is cancelled out and a new event is scheduled into the LTSF queue if available. If the event is positive then it is processed normally, the state of the LP is saved, and new events are sent to other LPs. The event is then moved into the processed queue and a new event is scheduled into the LTSF queue. To summarize, the worker thread processing loop is shown in pseudocode in algorithm 2.

```

while termination not detected do
1    $e \leftarrow \text{getNextEvent}()$ 
    $lp \leftarrow \text{receiver of } e$ 

   if  $e < \text{last processed event for } lp$  then
     | rollback  $lp$ 

   if  $e$  is an anti-message then
3   | cancel event with  $e$ 
     | schedule new event for  $lp$ 
     | continue

   process event  $e$ 
   save state of  $lp$ 
2   send new events
3   replace scheduled event for  $lp$ 

```

**Algorithm 2:** WARPED2 Main Event Processing Loop

When events are inserted into the unprocessed queues, they are compared to the currently scheduled event for the LP. If there is no currently scheduled event or the new event is less than the currently scheduled event, then the new event is scheduled to prevent a rollback from occurring.

### 6.2.1 Ordering of Events

The time warp mechanism assumes that every event is labeled with a totally ordered clock value based on virtual time [4]. This is important so that causal dependencies are preserved and so that simulation results are deterministic [13]. To ensure this, WARPED2 uses a 4-tuple scheme which provide a total ordering of events:

1. Receive Time
2. Send Time
3. Sender LP Name
4. Generation

The last three serve as a tie breaker for simultaneous receive times. The send time is necessary to ensure correct causal dependencies. It is analogous to a Lamport logical clock [14] in real time distributed systems but for virtual time systems. The send time will work as long as LPs only send a single event with the same send time, receive time, and receiver LP. Otherwise, a more strict ordering of sends would be required. However, WARPED2 forbids this behaviour in the simulation model. The sender LP name is necessary to ensure that order is determined between events received with the same receive time and send time from different senders. Without it, it is possible that different results could occur on different runs of the simulation [13]. The generation is necessary for distributed memory systems to differentiate between the same events which could be resent after rolling back [13]. In WARPED2 it is implemented as a single counter per LP and keeps track of the number of events that have been sent from the LP. The count is then tagged in the event when it is sent and used for comparison at the receiver.

### 6.2.2 Performance Tuning Parameters

To allow WARPED2 to run on a large range of machines with different architectures and characteristics, a number of parameters are provided for the user to tune the simulation mechanism for their needs. In the current implementation, the four main parameters that will determine performance on a single SMP machine

are the number of worker threads, the number of LTSF Queues, the LTSF partitioning method, and the state saving period.

### Worker Threads and LTSF Queues

The number of worker threads that should be used will depend on the number of processors of the machine so it is left as a tunable parameter. WARPED2 also allows the user to choose the number of LTSF queues. The user can specify any number of LTSF queues, however, it must be smaller than the number of worker threads and also be a factor of the number worker threads.

If all worker threads share a single LTSF queue then a huge contention point is created and performance will suffer. At the opposite extreme, however, with all worker threads processing events from a separate LTSF queue, the critical path of the simulation will be spread out and the number of rollbacks can increase significantly. The extent to which these matter varies depending on the characteristics of the simulation model. Furthermore, they may also vary as the data structures and algorithms in WARPED2 evolve to minimize contention.

The plots shown in figure 6.2 illustrate the effects of increasing the number of LTSF queues with 10 worker threads. For the analysis of LTSF queues, speedup will be defined as:

$$Speedup_{LTSF} = runtime_1 / runtime_n$$

where  $runtime_1$  is the run time for a single LTSF queue and  $runtime_n$  is the runtime for  $n$  LTSF queues. The speedup is a better measure over runtime because it shows relative performances and multiple simulation models can be compared. Another measure called the *efficiency* is defined as:

$$Efficiency = events_{committed} / events_{processed} * 100$$

where  $events_{committed}$  is the number of events that are processed and not rolled back and  $events_{processed}$  is the total number of events that are processed whether they are rolled back or not. Thus, the efficiency is a measure of the percentage of events that are processed and not rolled back.

Even though the number of total rollbacks increase with more LTSF queues, there is still a growing

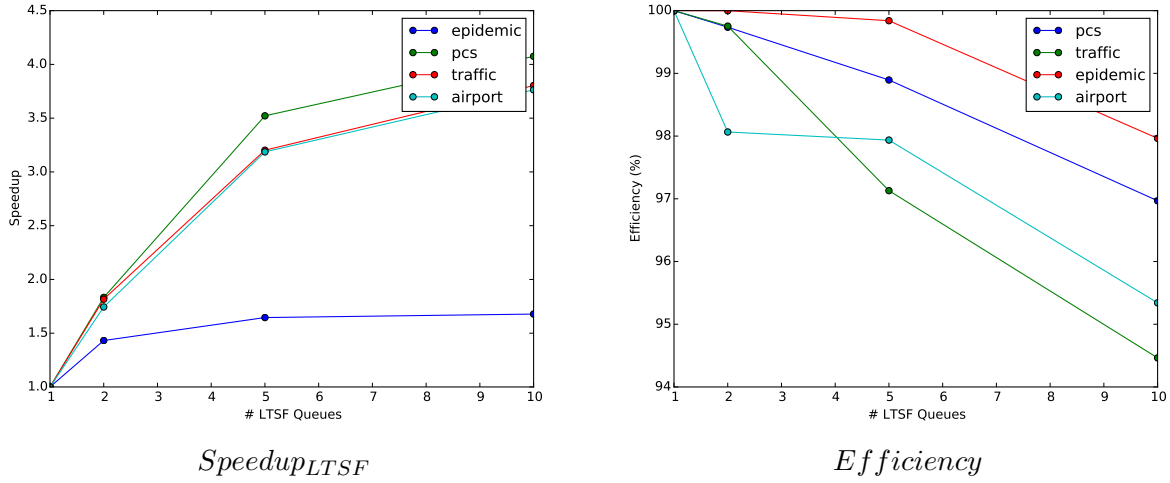


Figure 6.2: LTSF Queues: Performance on SMP Machine

speedup. That means that the contention of the LTSF queue locks outweighs the extra time needed to process more rollbacks. By looking back at figure 2, we can see all the critical points where worker threads can access the LTSF queues. The lines with a number on the margin indicate possible access to the LTSF queue. For every event the LTSF queue is accessed at least 3 times: (1) obtaining the next event, (2) sending new events, and (3) rescheduling new events. However, access to the LTSF cannot be minimized further. Access to the LTSF queue will always be necessary to obtain the next event and reschedule events and since the LTSF queue is only accessed while sending an event if the event is a straggler, it will save a future access to the LTSF queue when the straggler is scheduled while also preventing a rollback.

It is also worth noting that these simulation models are configured for only 10,000 LPs and with larger models, the efficiency would be expected to be better. Therefore, scaling up the number of worker threads and LTSF queues together may not hinder performance if the simulation model is also scaled up since efficiency and contention will not get worse.

### LTSF Queue Partitioning

The LPs can also be partitioned between LTSF queues in various ways just like partitioning between processes. In fact, there is only a single partitioning parameter for both levels of partitioning. This makes sense because the LTSF queue partitioning will be negligible compared to the interprocess but it allows the user to choose the LTSF queue partitioning directly on a single node simulation.



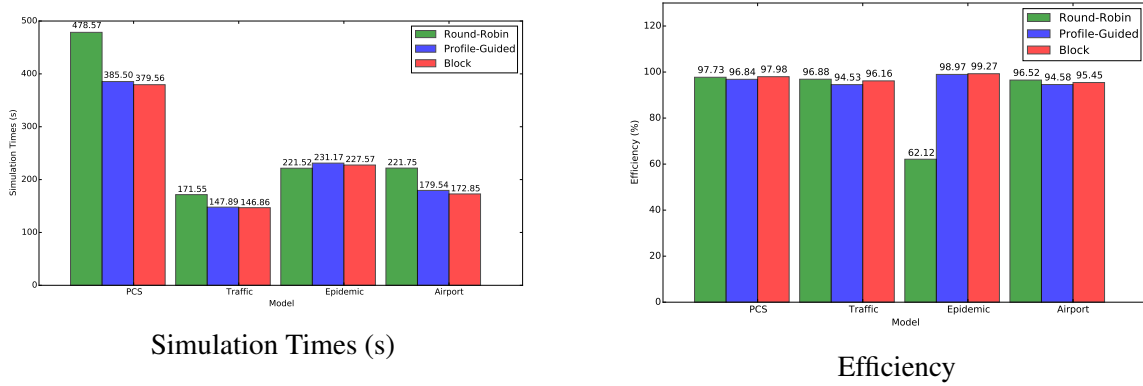


Figure 6.3: LTSF Partitioning: Intel® Xeon® E5410 Processor

Figures 6.3 and 6.4 show the effects of LTSF queue partitioning on simulation runtime and efficiency for the Intel® Xeon® E5410 processor and Intel® Xeon® X5675 processor, respectively.

The simulations on the Intel® Xeon® E5410 are configured with 8 LTSF queues, and 8 worker threads and the simulations on the Intel® Xeon® X5675 are configured with 10 LTSF queues, and 10 worker threads.

Just as with the interprocess partitioning, the block partitioning scheme tends to give the best average performance. However, only the epidemic model has any significant difference in average efficiency. This can be explained by noting that the epidemic model has a very slow event rate compared to any other model so the efficiency is easy to diminish when the number of rollbacks increase.

Another interesting observation is that the round-robin partitioning is much worse on the Intel® Xeon® E5410 for the PCS, traffic, and airport models but not on the Intel® Xeon® X5675. This, however, is simply

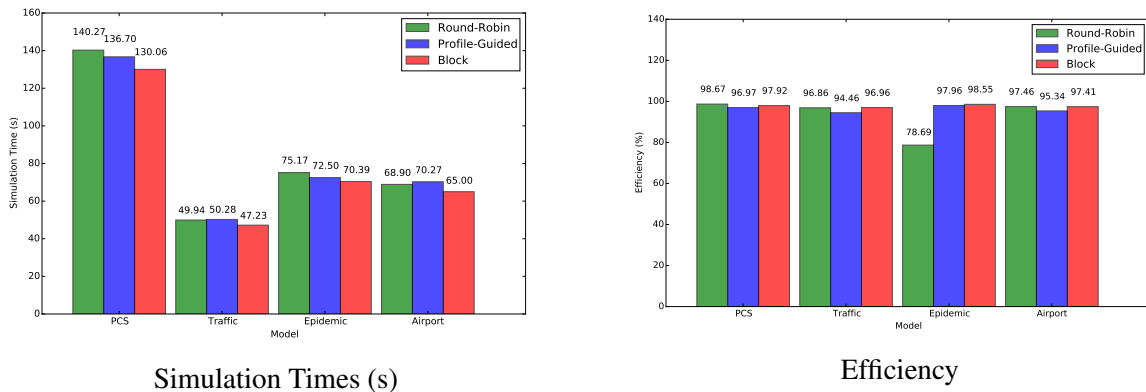


Figure 6.4: LTSF Partitioning: Intel® Xeon® X5675 processor

a matter of the simulation model properties, and configurations of the model and kernel. The LPs of these models are connected as a grid of 100 x 100 LPs. Since the number of LTSF queues is a factor of the grid dimensions, the round-robin partitioning will still provide north-south locality. The same is not true with 8 LTSF queues so the simulations will be more unbalanced.

### State Saving Period

Instead of saving the state of the LPs after every processed event processed, WARPED2 allows the user to choose a state saving period value,  $N$ , so that each LP only has its state saved every  $N$  events. A larger value of  $N$  reduces the amount of time taken to copy states and thus speeds up forward computation. The downside is that it also increases rollback time since not all states are available to roll back to, making it necessary to process more events to get to the necessary state in a process known as *coast forwarding*. The total time spent rolling back, however, is small compared to the time taken for forward computation for moderate values of  $N$ .

Figures 6.5 and 6.6 show the speedup over copy state saving and the efficiency as the state saving period is increased for all models for a single SMP machine and an 8 node cluster.  $Speedup_{CSS}$  is defined as:

$$Speedup_{CSS} = runtime_1 / runtime_N$$

where  $runtime_1$  is the runtime with every state saved and  $runtime_N$  is runtime with the state saving period of  $N$ .

### 6.2.3 Protecting Access to LTSF Queues

Worker threads must be able to access shared data structures without the possibility of race conditions. To deal with this, WARPED2 uses mutexes to serialize worker thread access to any section of code that accesses the LTSF queue. This method, however can significantly slows down access to the LTSF queues. In the default configuration, blocking mutexes are used which means that if a thread cannot acquire a the lock, it will be put to sleep and rescheduled for execution at a later time. However, this method can waste a lot of extra time if access to the critical section is quick. All of these accesses are quick so the lock should only held for a short period of time.

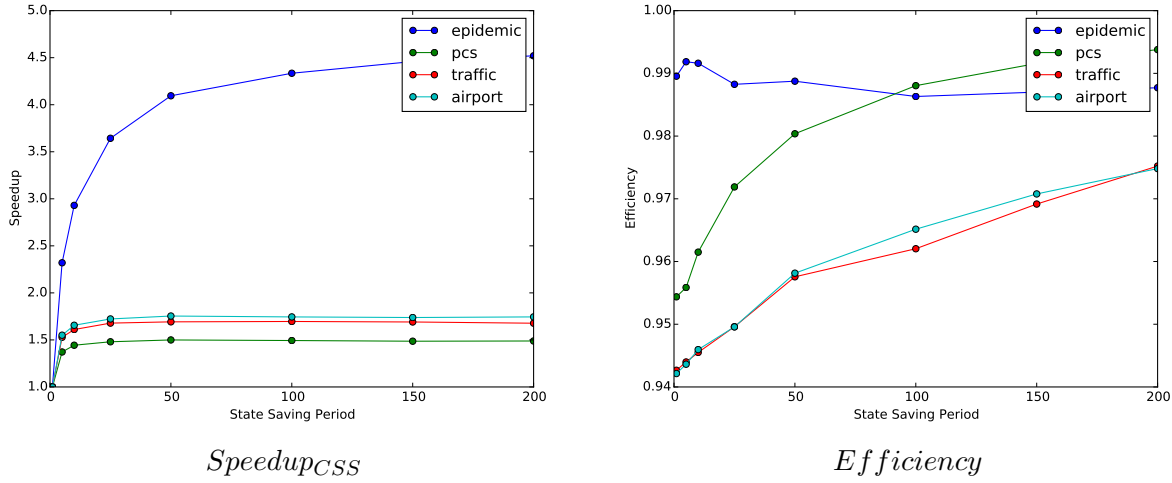


Figure 6.5: Periodic State Saving: Performance on SMP Machine

A *spinlock* is a type of mutex that does not force threads to be rescheduled but instead continue to attempt to acquire the lock over and over until it successfully acquires the lock or the timeslice of the thread expires. The downside of spinlocks is that the CPU time can be wasted, especially as the number of contending threads increase. Furthermore, if the number of total threads is larger than the number of available processors, spinlocks are even worse because a thread can be involuntarily pre-empted while holding the lock and entire timeslices can be wasted.

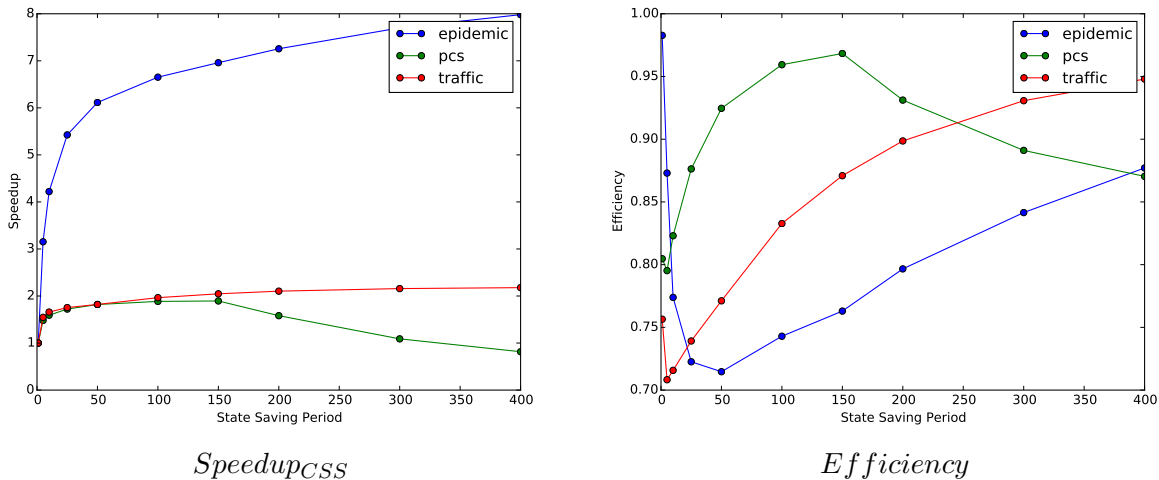


Figure 6.6: Periodic State Saving on Cluster: Speedup and Efficiency

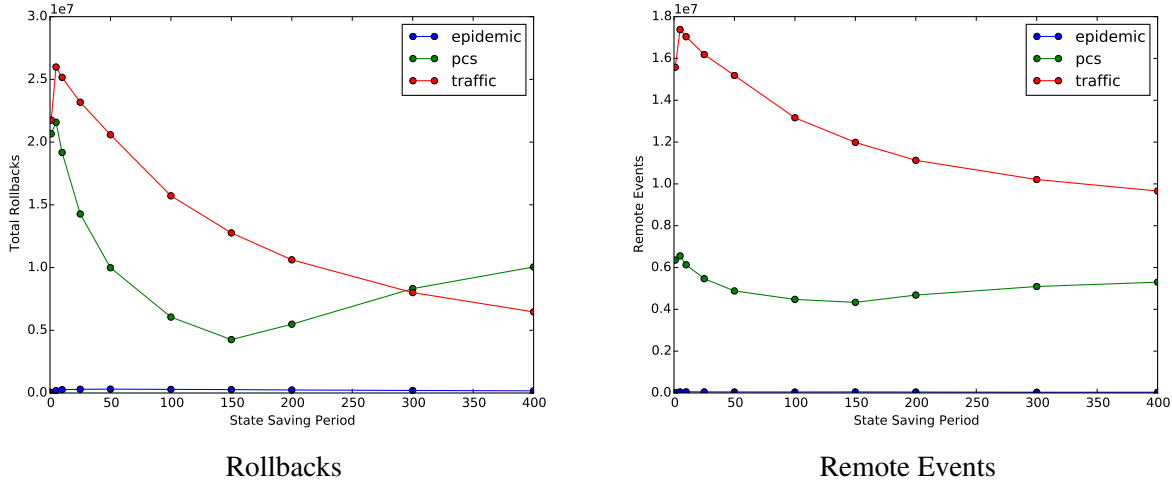


Figure 6.7: Periodic State Saving on Cluster: Remote Events and Rollbacks

### Spinlocks in WARPED2

WARPED2 allows the user to optionally build the kernel to use spinlocks instead of blocking mutexes. It is not set as the default because spinlocks do not work well in all cases such as with a system that is running more threads than processors. There are many ways to implement spinlocks with all varying attributes and complexities. Ticket locks were chosen for WARPED2 because they are simple to implement, provide fairness to all threads, and generally have good performance for a small number of threads [15]. A short pseudocode algorithm to illustrate the ticket lock acquisition and release procedures is shown below in algorithm 3.

#### lock

```

    my_number ← fetchAndIncrement(next_number)
    repeat until ticket = my_number

```

#### unlock

```

    fetchAndIncrement(ticket)

```

Algorithm 3: Ticket Lock Procedures [3]

To acquire the lock, each thread atomically increments a counter and takes the old value as a "ticket" to provide first in first out order. Then when the ticket value reaches that of the waiting thread, access to the critical section is given. To release the lock, the thread simply increments the ticket number and allows the

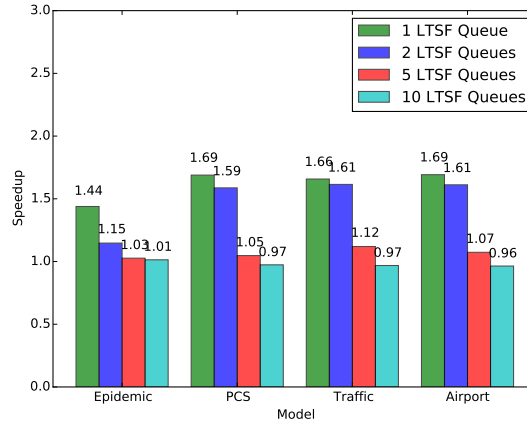


Figure 6.8: Speedup with Spinlocks

next thread to access the critical section. If there is no contention for the lock, the next number will be equal to the ticket number and a thread can access the critical section immediately.

All previous experiments were run with the default blocking mutexes in which threads will yield the processor when a lock is contended. To reduce the average lock acquisition latency, the kernel can be configured with spinlocks instead. Figure 6.8 shows the speedup for all models and varying number of LTSF queues. As expected, the speedup is much greater with more contention when there are more worker threads per LTSF queue. It can also be seen that all models except for epidemic have very similar speedups. This is probably due to the high cost of saving such a large complex state. Contention becomes less of a concern if the event processing is slowed down by other computation.

### 6.3 Data Structures to Support Rollback and Cancellation

Jefferson [4] described the rollback and cancellation process in terms of three data structures: the input queue, output queue, and state queue. The data structures that are implemented in WARPED2 follows pretty closely to Jefferson's description except that in Jefferson's approach, the input queue holds both unprocessed and processed events whereas WARPED2 has two separate data structures to allow easier scheduling of new events.

The state queues contain a 3-tuple value for each state. The first value is a pointer to a memory location which holds the saved copy of the LP state. The second value is a pointer to the event that produced the

state of the LP and is used for comparison against a straggler event on a rollback to determine which state to restore or against the GVT during fossil collection. The third value is the state of the LPs random number generators at the time the state was saved. The random number generator states are saved in a linked list and are also restored during a rollback to ensure deterministic results.

Similar to the state queue, the output queue also contains a tuple of values. In the output queue though, only a pointer to a source event and a sent events are used. In the same way as the state queue, the source event is used for comparison against a straggler event during a rollback but is used to determine which events should be sent as anti-messages.

The processed queues only contain a pointer to the processed events. The dereferenced events from the processed queue are the same as dereferenced source events from output queue and state queue so that no copying of the events is needed. Furthermore, if events are sent withing the same process, the same events can be pointed to from the output queue and an unprocessed or processed event from another LP. The organization of the processed queue, state queue and output queue for a single LP is illustrated in figure 6.9.

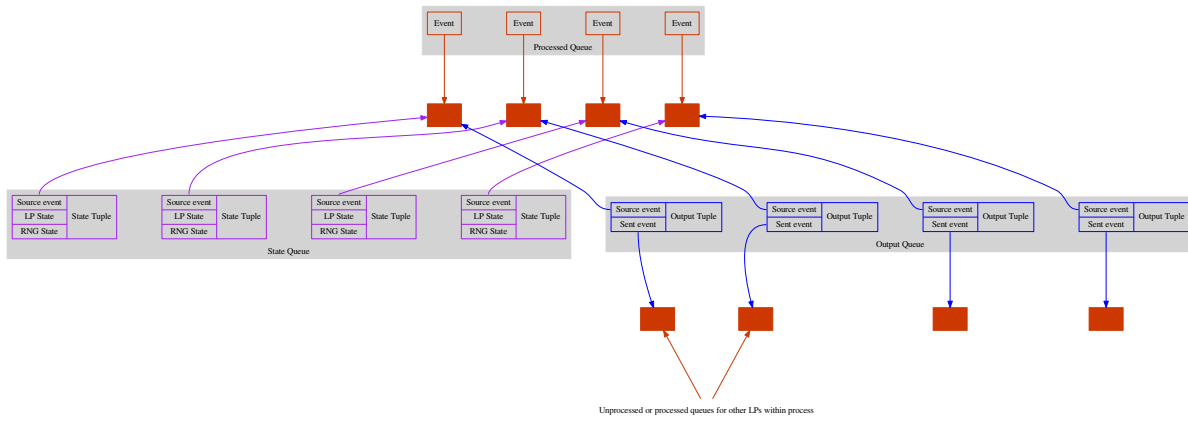


Figure 6.9: Rollback and Cancellation Data Structures in WARPED2

Although the organization of these data structures prevents copying of events, there are also some limitations. First, it introduces a lot of indirect memory accesses which could slow down access to memory, especially in a multiprocessor system. Secondly, it adds some complexity when freeing memory used by events. To solve this problem though, WARPED2 uses the smart pointers offered by the standard C++11 libraries which employ a reference counting scheme.

## Chapter 7

# GVT and Termination Detection

Algorithms that compute the Global Virtual Time (GVT) and detect termination conditions are both examples of algorithms that can be solved by determining the global state of a distributed system. The global state of a system is defined as the combination of all the local states of the processes in the system and all messages in transit. Global state determination algorithms are also commonly used for deadlock detection, garbage collection, debugging, and checkpointing for failure recovery in distributed systems.

### 7.1 Global Snapshots

**Chandy and Lamport** [16] described a basic global state algorithm by using *global snapshots* for distributed systems that use only FIFO channels for communication. To start the algorithm, an initiator process records its state and sends a control token out of all outgoing channels. When a process receives a control token, and it hasn't yet recorded its state, the process records its state and sends more control tokens out of all its outgoing channels. The algorithm terminates at each process when it has received a token through all of its incoming channels.

**Lai and Yang** [17] describe an algorithm for non-FIFO systems which computes a consistent cut by piggybacking a control bit onto basic messages. The control bit is used to indicate whether or not the sending process has recorded its state. The processes can be explained in terms of colors. A process that has not recorded its state is colored white and a process that has recorded its state is red. White processes send

white messages and red processes send red messages. The control bit in the messages indicates the color. All processes are initially white and turn red when a red message arrives. When a red message arrives at a white process, the process must record its state BEFORE actually receiving the message. The snapshot only relies on basic messages and no control tokens are used. This algorithm has several drawbacks. First, it assumes that every process will eventually receive a red message and record its state which is not guaranteed. Second, to ensure transient messages are recorded, the processes must record all incoming and outgoing messages and send them to other processes within the basic messages. That way the transient messages can be calculated by differences in incoming and outgoing messages.

**Mattern** [18] extended the algorithm by Lai and Yang by adding a separate control token which is used to create two cuts by circulating the control token to every process twice. The control token is used to color the processes instead of the basic messages. This guarantees that every process will eventually record its state because the token is always circulated. Furthermore, processes do not have to keep track of sent and received messages. Instead counters can be used to keep track of the differences in sent and received white messages at each process. The control token can then accumulate the counts. When the white message counts accumulate to zero when the token arrives back at the initiator process, it can be determined that the snapshot is complete. These counters can be *vector counters* or *scalar counters*. Vector counters keep track of messages to/from each process individually whereas scalar counters keep track of just a single count at each process. When the algorithm is complete, only the initiator process can produce a global snapshot so if all processes must use the snapshot, it must be broadcasted to the other processes.

## 7.2 GVT

Although GVT algorithms can be implemented with the basic global snapshot algorithms described above, it is usually more efficient to build custom solutions on top of the basic algorithms. In a GVT algorithm, the local minimum clock of process is the local state and the basic messages are events that are sent between processes. This section first describes the key ideas that must be considered when developing a GVT algorithm. Then, a few of the classic GVT algorithms and modern GVT algorithms that are commonly used in practice today are described. Finally, the algorithm that is implemented in WARPED2 is described as well as



the reasons for choosing the approach.

GVT algorithms can be synchronous or asynchronous. In a synchronous GVT algorithm, since all other computation will be blocked, event processing will be halted. Synchronous GVT algorithms are usually very simple to implement but halting the event processing can be very costly. On the other hand, asynchronous GVT algorithms calculate GVT concurrently with event processing. For this reason, asynchronous GVT algorithms perform much better than synchronous GVT algorithms but are harder to implement because special cases must be considered.

There are two special cases that must be considered in any GVT algorithms:

**Transient Message Problem:** is caused by messages(events) that have been sent by the sending process but not yet recieved by the receiving process. If not carefully considered in a GVT algorithm, these *transient messages* can be completely missed which could lead to an erroneous GVT calculation if they contain the minimum timestamp of all other events in the system.

**Simultaneous Reporting Problem:** is caused because processes can report their local minimum clock at different points in real time. Consideration must be taken into account to ensure that a process does not report its local minimum clock value and then receive an event from a process that has not yet reported its local minimum clock value, completely missing the event.

There are many synchronous and asynchronous GVT algorithms that have been developed over the years that all have some method of either solving these problems.

### 7.2.1 Asynchronous GVT Algorithms

#### Message Passing Algorithms

Most of the time warp systems in use today are based on message passing and classically GVT algorithms have been designed for message passing systems. In this section Samadi's and Mattern's message passing algorithms are discussed and in the next section Fujimoto's shared memory GVT algorithms is discussed as well as the Seven O'Clock algorithm which is an extension of Fujimoto's shared memory algorithm extended for distributed memory systems.

**Samadi's Algorithm** [19] in the most general form uses acknowledgements for all events that have been received. All processes must track all events that have been sent but have not been acknowledged. Furthermore, the received messages must also be tracked so that acknowledgements can be sent. All transient message can then be calculated from the tracked messages. A process initiates the GVT algorithm by broadcasting a start message to all processes. After this start message is received by a process, it is marked(colored) and all acknowledgements sent from a marked process are also marked(colored). All processes then calculate their local minimum by taking the minimum of the unacknowledged received events, the marked acknowledgments sent, and the local simulation clock. Marking the acknowledgements sent after the start of the GVT calculation ensures that the simultaneous reporting problem will not occur.

**Mattern's Algorithm** [18] for GVT calculation is an extension on his general snapshot algorithm that was described above. The white message counts are used to determine whether transient messages are still in the system. They also serve as the basis for determining when the snapshot is complete which occurs when the accumulated white message count of all processes is zero. To ensure that the simultaneous reporting problem does not occur, red messages received at a white process are recorded and used in the local minimum value. The algorithm is initiated by sending a control token to all processes in some defined order. The token accumulates the white message counters, local minimum clocks, and minimum red message timestamps. When the accumulated count reaches zero and the token is back at the initiator process, the GVT is approximated using the minimum of the accumulated clocks and timestamps.

**Other GVT Algorithms** that are based on message passing model are usually based on the same ideas from Samadi's algorithm or Mattern's algorithm. Many algorithms are just extensions or variations of the algorithms that aim to optimize it in some particular way.

### Shared Memory Algorithms

**Fujimoto's Algorithm** [2] is a fast GVT algorithm that exploits properties of shared memory architectures. In most shared memory architectures, processors cannot observe memory operations in different orders. For this reason, it is not possible to have transient messages if a shared data structure is used to communicate between tasks running on different processors. Furthermore, a shared flag variable can be

used to initiate the GVT algorithm. However, it is still possible that the flag can be read at different times, so the simultaneous reporting problem can still occur. To solve the simultaneous reporting problem, two things must be done, First, the start flag is checked after sending events and recorded if the sending task has not yet reported its local minimum value and is eventually used when the reporting is done. Second, the start flag must be read into a temporary local variable before obtaining a new event to process.

**Seven O’Clock Algorithm** [20] is an extension of Fujimoto’s algorithm for distributed memory systems. Although the algorithm can be used in message passing systems, the algorithm does not use any messages and is still uses shared memory ideas. The key idea in the algorithm is that all processors in a distributed system all have a consistent view of wall clock. Hence, the processors can carry out an operation atomically, without having to explicitly interact. The atomicity can be achieved by using cycle level counters which are available on most modern architectures. Unlike Fujimoto’s algorithm though, transient messages can still be missed in the calculation. To solve that problem, each processor must wait a small time interval which is calculated based on the worst round trip time for network transactions.

### 7.2.2 Synchronous GVT Algorithms

**Global Reductions** provide a good way to do a synchronous minimum calculation which and is a common way to implement a synchronous GVT algorithm. ROSS implements a synchronous GVT algorithm which uses global reductions [21]. First, to prevent the transient message problem, a global reduction on message counts of each process is performed until it reaches zero which is guaranteed because the reduction acts as a barrier for each process. The message counting is necessary because asynchronous communication is used for sending and receiving events. When the messages count reaches zero a global reduction is performed on the local minimum timestamp of all processes to give a GVT value which is broadcast to all processes. This algorithm is efficient when time warp simulation are run on large supercomputing platforms like the blue gene machine which perform collective operations very quickly.

### 7.2.3 GVT in WARPED2

To calculate the GVT in WARPED2, Fujimoto’s shared memory algorithm is used between worker threads and a variation of Mattern’s algorithm with scalar message counters is used between processes. Fujimoto’s

algorithm is a subalgorithm of Mattern's algorithm. The details of the Mattern's algorithm implementation in WARPED2 is detailed below as well as how it is used with Fujimoto's algorithm. More details on Fujimoto's algorithm will not be detailed but they can be found by reading [2].

In the WARPED2 implementation, scalar counters are used to keep track of message that are sent that carry the initial color. That means that each process keeps track of the number of sent messages minus the number of received messages to and from all other process as a single count. The reason that scalar counters are used over vector counters is that vector counters require that communication is stopped after receiving a control token until all known transient messages that are bound for the receiving process are received. With scalar counters, this is not possible because all counts are combined into a single counter. However, with scalar counters it is possible that more than two rounds of the control token will be necessary. This is usually not a problem in practice though since two rounds is usually sufficient. Also, each process must maintain a counter for both white messages *and* red messages so that counters can be consistent between multiple runs of the algorithm. The roles of the colors must also be switched between successive runs.

**processEvents**

```

while termination not detected do
   $localGVTFlag_i \leftarrow localGVTFlag$ 
   $e \leftarrow getNextEvent()$ 
   $lp \leftarrow \text{receiver of } e$ 
  if e is straggler then
    Rollback
    continue
  if  $localGVTFlag_i$  and (workerThreadi hasn't reported local min) then
     $reportThreadMin(timestamp_e)$ 
   $newEvents \leftarrow processEvent(e)$ 
  foreach  $ne$  in  $newEvents$  do
     $sendEvent(ne)$ 

```

**receiveRemoteEvent**

```

 $GVTReceiveEventUpdate(timestamp_e, color_e)$ 
 $lp \leftarrow \text{receiver of } ne$ 
  Insert into Unprocessed Queue for  $lp$ 

```

**sendEvent**

```

if ne is remote event then
   $color \leftarrow GVTSendEventUpdate()$ 
  Insert into Send Queue
else
   $lp \leftarrow \text{receiver of } ne$ 
  Insert into Unprocessed Queue for  $lp$ 
if  $localGVTFlag_i$  and (workerThreadi hasn't reported local min) then
   $reportThreadSendMin(timestamp_{ne})$ 

```

**Algorithm 4:** GVT: Worker Threads

**progressGVT**

```

if Local GVT is needed then
  if (localGVTFlag equals 0) and (haven't started local GVT) then
    Toggle Color
     $ts_{min} \leftarrow \infty$ 
     $clock_{min} \leftarrow \infty$   $msgcount \leftarrow msgcount_{initial}$ 
     $msgcount_{initial} \leftarrow 0$ 
  else if (localGVTFlag equals 0) and (started local GVT) then
     $localMin = \infty$ 
    foreach worker thread, i do
       $localMin \leftarrow \min(localMin, localMin_i)$ 

    if processCount  $\neq 1$  then
      SendToken( $i, (i + 1) \bmod N, localMin, \min(ts_{min}, clock_{min}), msgcount$ )
    else
       $gvtApprox \leftarrow localMin$ 

```

**receiveToken**

```

if  $ID_{process}$  does not equal 0 then
  if  $color = color_{initial}$  then
     $ts_{min} \leftarrow \infty$ 
    Toggle Color
  Accumulate  $ts_{min}$  and  $msgcount$ 
   $localGVTFlag \leftarrow numWorkerThreads$ 
   $clock_{min} \leftarrow \min(clock_{min}, mclock)$ 
else
  if  $mclock$  equals 0 then
     $gvtApprox \leftarrow \min(mclock, msend)$ 
    Broadcast  $gvtApprox$ 
  else
    Accumulate  $ts_{min}$  and  $msgcount$ 
     $localGVTFlag \leftarrow numWorkerThreads$ 
   $clock_{min} \leftarrow \infty$ 

```

**Algorithm 5:** Asynchronous GVT: Manager Thread

```

progressGVT
  localGVTFlag  $\leftarrow$  true
  THREAD_BARRIER

  while true do
    Flush send queue
    Receive messages
    sumAllreduce (localMsgCount, globalMsgCount)

    localMin =  $\infty$ 
    foreach worker thread, i do
      localMin  $\leftarrow$  min (localMin, min (localMini, sendMini))

    minAllreduce (localMin, GVT)

    localGVTFlag  $\leftarrow$  false
    THREAD_BARRIER

```

**Algorithm 6:** Synchronous GVT: Manager Thread

## 7.3 Termination Detection

Termination detection, like GVT, is a problem of determining the global state of the system. Termination is a *stable property* of a distributed system, meaning that once termination conditions occur, the system will remain with termination conditions forever until further action is taken.

A process in the system can be in one of two states at any time: active or passive. A process is considered active if some basic computation still remains and passive otherwise. When all processes in the system become passive and no messages are left in transit, then the system should be terminated. The purpose of the termination detection algorithm is to determine when this occurs. A passive process can become active with the arrival of an *activation message*. For parallel discrete event simulation the basic computation is event processing and the activation messages are events. A termination detection algorithm for any parallel discrete event simulation must satisfy the following properties:

**Safety Property:** Termination will not be detected if any unprocessed event is still present in system which includes all local pending event sets and events still in transit.

**Liveness Property:** Termination will be detected at some finite amount of time after all events have been processed.

Just like GVT algorithms, termination detection algorithms can be implemented with message passing or shared memory communication. Termination algorithms vary widely because different systems can define termination conditions in such different ways. Furthermore, termination usually does not affect system performance as long as it does not block event processing so correctness is more important than optimization. The rest of this section will only focus on the algorithms that are implemented in WARPED2.

### 7.3.1 Termination in WARPED2

The termination detection algorithm in WARPED2 is actually two independent algorithms, a message passing algorithm and a shared memory algorithm. In the opposite manner as the GVT algorithm, the shared memory algorithm is actually used to initiate the message passing algorithm.



### Shared Memory algorithm

The purpose of the shared memory algorithm is to determine when all of the worker threads in a process become passive (inactive). The algorithm is fairly straightforward and uses just a shared counter variable to keep track of the number of active worker threads and a boolean array to keep track of which worker threads are active. Worker threads update their own state when they change and atomically update the counter. The manager thread periodically checks to see if the counter has reached zero. If the count has reached zero and the process is the master, then the interprocess message passing algorithm is initiated.

The safety property is not necessarily achieved because it is possible that the last active thread sends an event to another LP which will be processed by another thread. Then if the sending thread becomes inactive, all threads can be temporarily inactive and the manager thread can falsely detect termination. However, it is very unlikely that the sending thread will report itself as passive before the receiving thread gets the event and reports itself as active. Furthermore, the manager thread is forced to check the active thread counter at least twice with no changes in any worker thread state. The liveness property is achieved because the manager thread periodically checks the active worker thread count so the inactivity of all worker threads will eventually be discovered.

### Message Passing Algorithm

The algorithm that is carried out among processes is an asynchronous message passing algorithm based on Mattern's "sticky flags" algorithm [18]. Just like the GVT algorithm, the token is passed in a logical ring to increasing process ids. However, the initiator process of the algorithm can change with each execution of the algorithm. The initiator of each successive execution is the first active process that the token reaches during circulation. For the first circulation, the initiator is process 0.

Each process has two state indicators, one for the actual state and one for the *sticky state*. When the sticky state of all processes are simultaneously passive, then the simulation can terminate safely. The sticky state becomes active when the actual state becomes active but sticks to active when the actual state becomes passive. It can only change to passive on the arrival of a token which forces the token to circulate two times with no process becoming active and ensures the safety property. Without this scheme, a process that has already forwarded the token because it was passive could receive an activation message and then the sender

of the activation message could become passive before receiving the token and false termination could then be detected.

The algorithm is started when the initiator process becomes passive which is determined by the shared memory algorithm. When a process receives the token it becomes the initiator but loses its role as initiator when it sends the token. That way, only a single process can be the initiator. Furthermore, since the token always stops at an active process, the token is always guaranteed to start again when it becomes passive which also guarantees the liveness property. When the token is received back at the initiator with a passive state, then termination is signaled to all processes. The procedure is illustrated in algorithm 7.

```

initiator  $\leftarrow$  true
if statesticky = PASSIVE and stateprocess = initiator then
  | if mstate = PASSIVE then
  | | Signal termination
else if statesticky = PASSIVE then
  | initiator  $\leftarrow$  false
  | SendToken (mstate, initiator)
statesticky  $\leftarrow$  stateactual

```

**Algorithm 7:** Termination Token Receive Procedure

It should also be noted that this algorithm is only guaranteed to work if the message order is preserved between any two processes. If that is not the case, then message counters or acknowledgements are necessary to ensure transient activation messages are not missed.

## Chapter 8

# Memory Management

This chapter will focus on main topics related to memory management in a time warp system and their implementations in WARPED2. The topics will include state saving, fossil collection and memory allocation. Different methods for each of these topics have different time and memory requirements and vary greatly in complexity.

### 8.1 State Saving

#### 8.1.1 State Saving Techniques

**Copy-state Saving** is the classic state saving technique that saves every past state of every LP and requires that the all states are copied and saved into the state queues. This method requires a lot of extra memory and requires a lot of extra computation to copy the states and insert them into the state queue. For this reason, copy-state saving is usually used in conjunction with other technique or not at all.

**Periodic State Saving** is another state saving technique where the state of each LP is saved only once every  $N$  events, where  $N$  is a number greater than one. Periodic state saving can significantly reduce the overhead of the time taken to copy the state of the LPs into the state queue. However, due to state history being lost, more processed event have to be saved so that the state of LPs can be restored correctly. These saved events must be reprocessed to restore the state in a process known as *coast forwarding*. During coast forwarding the events are processed normally but only to update the state. The only difference is that no

events are sent during the coast forwarding phase. The downside is that the rollback length is increased due to the loss in state saving. However, periodic state saving is a significant improvement over the base copy-state saving approach since the reduced time in state saving far outweighs the extra rollback time for increased rollback length.

**Incremental State Saving** is a technique that aims to reduce the amount of memory needed to store past states of the LPs. Instead of saving entire snapshots of the past states, the differences in specific state variables are saved. This method requires that metadata describing which variables are modified for each event. Therefore, this approach works well as long as only a small portion of the state variables change during the forward execution of events.

**Reverse Computation** is a modern approach to state saving that does not actually save any past states of the LPs. Instead, the *control state* of the forward computation is saved as a set of bits that describe the forward control flow. By saving the control flow of the forward execution for each event, the state variables that are modified and how they are modified are known and can be reversed. This is the case at least if all state variables are reversible without the need to know the histories of the variables. Also, a reversible random number generator is required. The major problem with this approach is the necessity to understand forward and reverse computation precisely in the implementation of the simulation model.

### 8.1.2 State Saving in WARPED2

WARPED2 currently implements periodic state saving because it is simple to understand and does not require that model developers know anything about it. Furthermore, it can increase performance and decrease memory requirements for any type of simulation model.

## 8.2 Fossil Collection

When the events and states in the processed queues, output queues, and state queues are no longer needed, the memory that they consume is no longer needed and can either be freed or reused. Fossil collection is the process of reclaiming memory for future use. There are many methods of fossil collection that have been

used in practice.

### 8.2.1 Fossil Collection Techniques

**GVT-based Fossil Collection** is the traditional method of fossil collection. Since rollbacks cannot occur past the GVT, it can be used as a marker to indicate what can be fossil collected. Fossil collection is triggered after every GVT calculation and all memory is explicitly freed so that it can be reallocated for future use.

**On-the-fly Fossil Collection** is a method of fossil collection used in GTW which does not use GVT as a starting point and memory is not explicitly freed. Instead, after events are processed, they are added to one of the per-processor free memory lists. New events can then be allocated from a list as long the first event in the list has a timestamp less than the GVT. If it does not then the current event being processed is either aborted until the GVT reaches a higher value or the list is searched for a lower timestamp event. This approach however, has been shown to produce instability caused by imbalances in rollbacks which creates imbalances in the free lists, and ultimately leads to a lot of aborted events or a long search time.

**Optimistic Fossil Collection** is another method of fossil collection which, like on-the-fly aims to reuse memory instead of explicitly freeing it. However, optimistic fossil collection is based on a statistical bound for rollbacks. It is still possible that memory that was fossil collected could still be used. This situation is called an OFC fault. Therefore, methods such as global state checkpointing must also be implemented to recover from these faults.

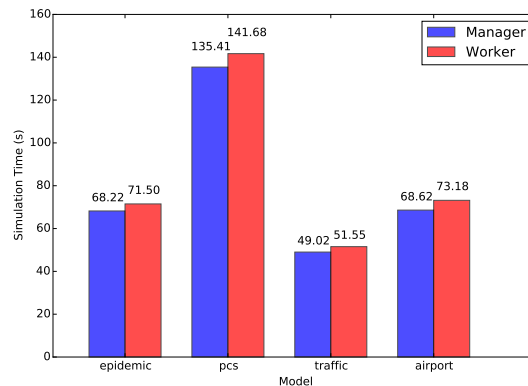
### 8.2.2 Fossil Collection in WARPED2

Currently, WARPED2 implements a GVT-based fossil collection. The GVT is calculated by the manager thread, but the worker threads carry out the fossil collection. Each LP keeps track of the last GVT value that it was last fossil collected and after each processed event, it is compared to the current GVT value, and the LP is fossil collected if the value is smaller than the GVT.

There are two ways that fossil collection can be achieved in WARPED2. The first way is to have the worker threads fossil collect one LP at a time as they process events. The last fossil collect time can be

Model	Maximum Time
PCS	10,000
Traffic	100,000
Epidemic	100,000
Airport	100,000

(a) Termination Times



(b) Simulation Run Times

Figure 8.1: Performance Comparison for Fossil Collection Methods

recorded for each LP so that the worker threads can know when fossil collection is needed by comparing the times to the current GVT. The second way is to have the manager thread fossil collect all LPs. Once the GVT is calculated, all LPs can be fossil collected all at once.

With manager thread fossil collection, the worker threads are relieved from unnecessary work that can slow down event processing but extra locks will be needed to protect access to the processed queue, output queue, and state queue for each LP. Worker thread fossil collection will be the more scalable approach for both the number of LPs and the number of processes since the manager thread must perform other tasks such as GVT and interprocess communication.

Figure 8.1 shows how the simulation times compare in a single SMP machine with 10 worker threads, 10 LTSF Queues, a GVT period of 100ms, and a state saving period of 25 events.

The results show that on average, having the worker threads do fossil collection yields slightly better performance, but not much. It is uncertain what will happen when the manager thread has more LPs to fossil collect or if it had remote messages to send. The time taken to fossil collect with more LPs could interfere with sending and receiving remote messages or with calculating the GVT.

One observation is that unstable behavior will occur with manager thread fossil collection if the state saving period is set very low with model that have a large state size such as with epidemic. The GVT calculation tends get longer and longer as the simulation progresses. This could be due to fossil collection interfering with the GVT calculation which would further allow memory to build up faster than it can be freed. Each fossil collection period and GVT period will grow and grow in a positive feedback loop until

memory in the system runs out.

## 8.3 Memory Allocation

Parallel discrete event simulation systems must frequently allocate and free memory for events and states. This can be a huge overhead if used memory and free memory is not maintained in an efficient way. Furthermore, memory must be allocated to and deallocated from each process by the operating system as needed which can be even more costly.

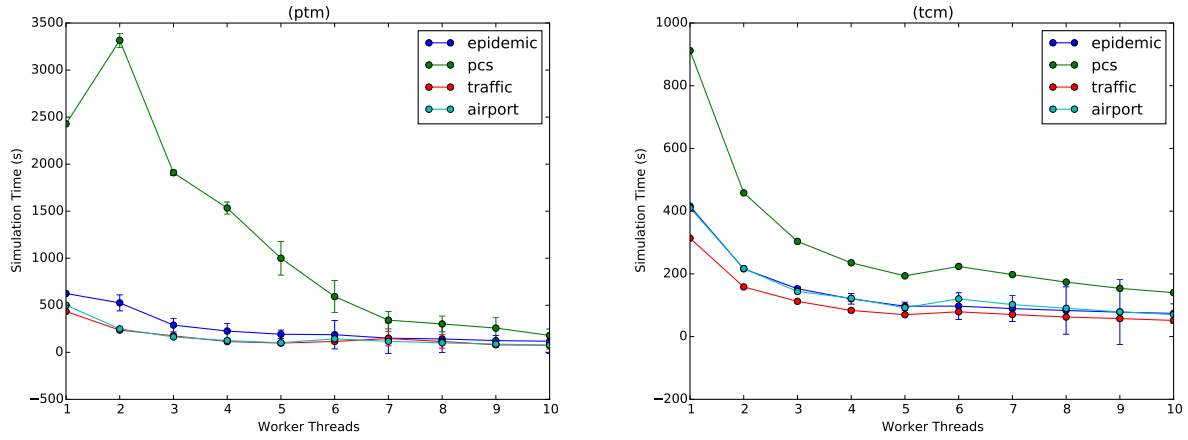
In a standard system, the application will never know when memory is allocated from the operating system or returned to it because it is usually handled by runtime libraries for the programming language that is used which provide an API for allocation/deallocation for each specific process. To completely remove any OS overheads, some time warp systems allocate a large fixed sized memory space before the start of the simulation and use their own memory management schemes. In these time warp systems, the simulation kernel must provide a way for the simulation models to allocate memory for events that need to be sent instead of calling the standard programming language APIs.

Dynamic memory management in WARPED2 is achieved through the standard C++ library interface and does not pre-allocate any memory. The simulation model is responsible for the allocation of all states and events. The kernel only receives pointers to the states and events through callback functions and accesses them indirectly. By doing this, we allow simulation model developers to implement their own memory management schemes or override the default malloc with allocators such as TCMalloc, JEMalloc, Hoard, etc.

### 8.3.1 Thread-Caching Malloc (TCMalloc)

TCMalloc is a memory allocator that is designed to efficiently allocate memory in multi-threaded applications like WARPED2. In TCMalloc, separate thread-local caches of free memory are maintained to avoid contention when allocating and freeing memory.

All results shown before now have all been with TCMalloc



PTMalloc2

TCMalloc

Figure 8.2: Memory Allocator Performance Comparison

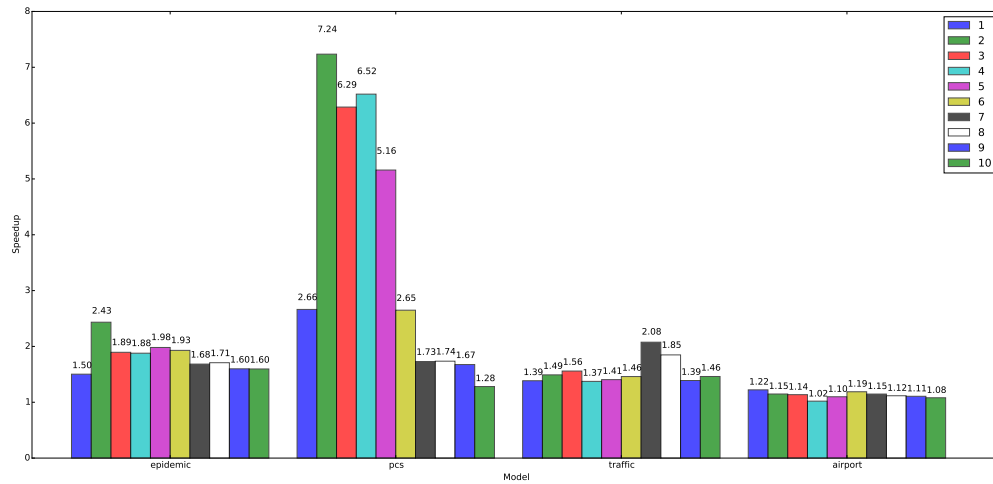


Figure 8.3: TCMalloc Speedup with Varying Worker Threads



## 8.4 Memory Tuning Parameters in WARPED2

To allow WARPED2 to run on machines that might have different memory constraints, some parameters are available for fine tuning the memory footprint of the simulation. The two main parameters that make a difference with regard to memory are the state saving period and the GVT period.

### 8.4.1 Number of LTSF Queues

### 8.4.2 State Saving Period

The state saving period is just as important for reducing memory requirements as it is for speeding up event processing. A larger state saving period means that fewer states must remain in memory but also means

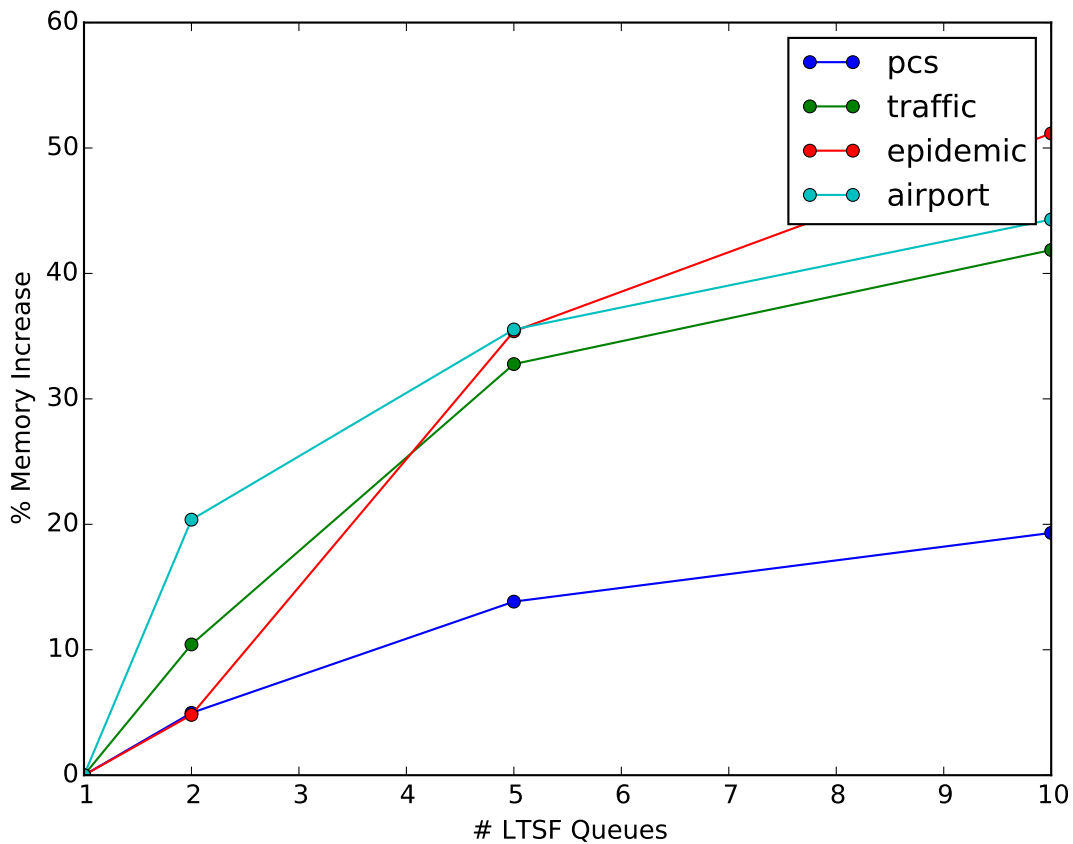


Figure 8.4: LTSF Queues: % Increase in Average Peak Memory

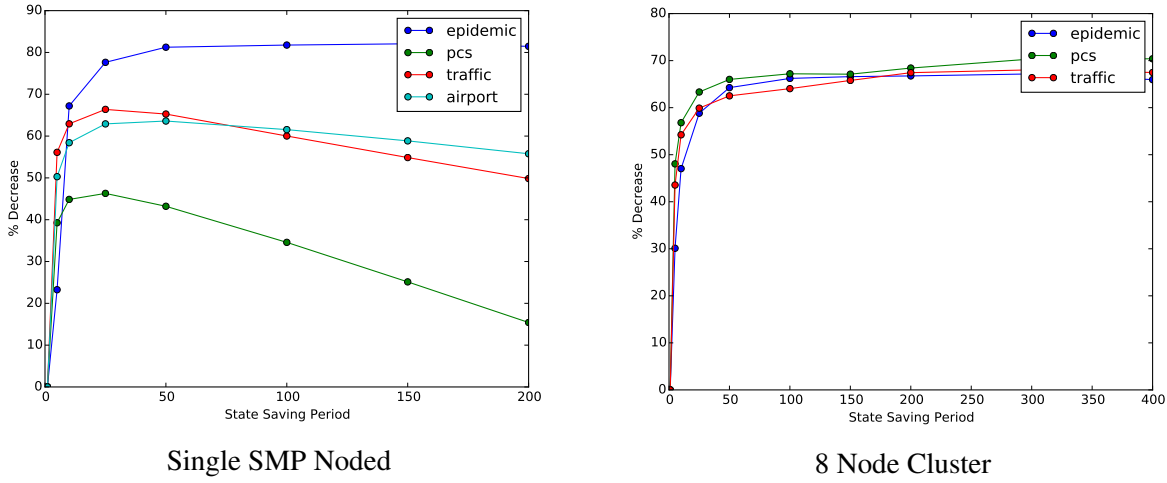


Figure 8.5: Periodic State Saving: % Decrease in Average Peak Memory on SMP Machine

that more processed events must be saved. Therefore the amount of memory required will depend on the relative sizes of states and events. Not only that, but it will also depend on the extent to which the simulation follows the critical path. If more rollbacks can occur, then fewer events and states will need to be saved. Users should pay attention to this when scaling up the number of LTSF queues and the number of processes, which will spread out the critical path more.

It is apparent these results that the benefits of increasing the state saving period are very good for lower periods due to less copying and lower memory requirements. However, with larger periods the increased number of coast forward events will start to counteract the copying and memory overheads. The memory requirements will increase due to the extra processed events saved for coast forwarding. The increased rollback time doesn't seem to make much difference even at large periods but the memory footprint increases linearly so the ideal period should be the point at which the memory requirements are the lowest. The ideal period will be different for all models because it will depend on the relative size of the events and states. The period for the PCS, Traffic, and Airport models appears to be the best around 25 but for the Epidemic model which has a large state to event ratio will be much larger.

### 8.4.3 GVT Period

The GVT period is a time value given in milliseconds which dictates how often to start a new GVT calculation. A smaller GVT period means that fossil collection will be more efficient and the memory consumption

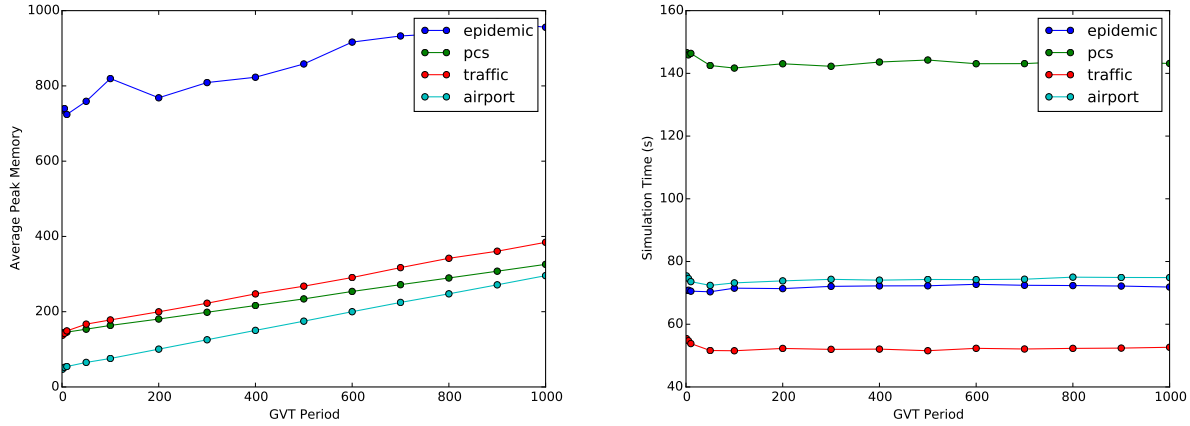


Figure 8.6: Memory and Performance Comparison for Various GVT Periods

will stay lower. However, the GVT period is only a lower bound since a new GVT calculation cannot be started while a previous one is still in progress. This has to be taken into account especially when scaling up to a larger number of processes since a token must be circulated to all processes at least twice.

Figure 8.6 shows the effect of an increasing GVT interval on average peak memory and performance with fossil collection by the worker threads, and the same configurations as the previous experiments. The peak memory is the point in time right after GVT is calculated since fossil collection is initiated by a GVT calculation. As expected, the average peak memory increases with an increased GVT period for all models. The performance of the simulations, on the other hand, is not affected by the GVT period at all except for extremely low values at 1ms which is smaller than should ever need to be used. Even with an extremely small period, the performance is only worsened by a very small amount.

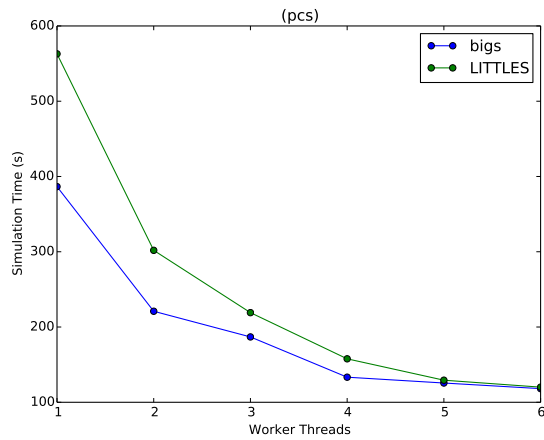
## **Chapter 9**

# **ARM big.LITTLE Platform**

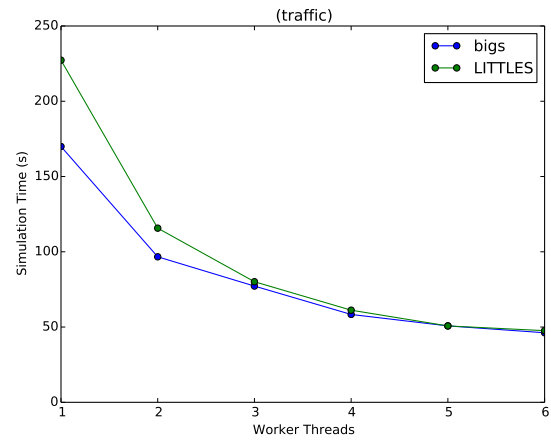
		Exynos 5422	
		Cortex-A15	Cortex-A7
Processor	ISA	ARMv7	
	# Cores	4	4
	# Threads	8	
	Frequency	2.0 GHz	1.4 GHz
	L1 Data Cache	32kB	32kB
	L1 Inst Cache	32kB	32kB
	L2 Cache	2MB	512kB
Memory	Type	2 x LPDDR3-933	
	Max Bandwidth	14.9 GB/s	
Runtime	OS Kernel	Linux 3.10.82	
	C Library	Ubuntu EGLIBC 2.19-0ubuntu6.1	
	Compiler	GCC v4.8.2	
	MPI	OpenMPI v1.8.5a1	
	Memory Allocator	TCMalloc v2.4	

Table 9.1: ODROID XU3 Platforms

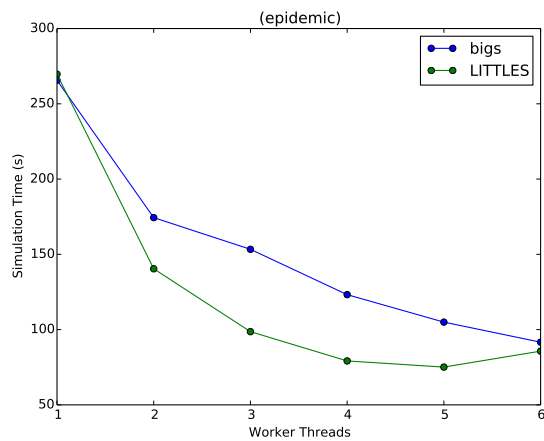
## 9.1 Comparison of Big and LITTLE Performances



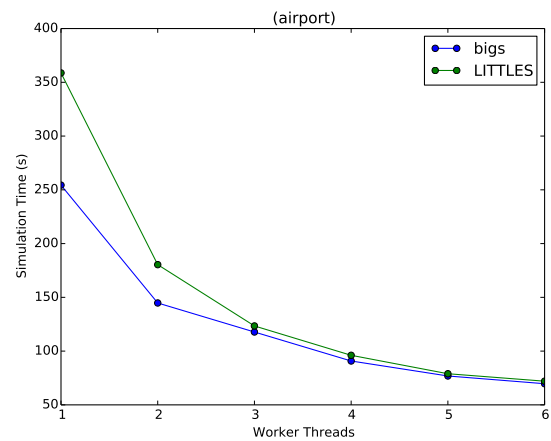
PCS



Traffic

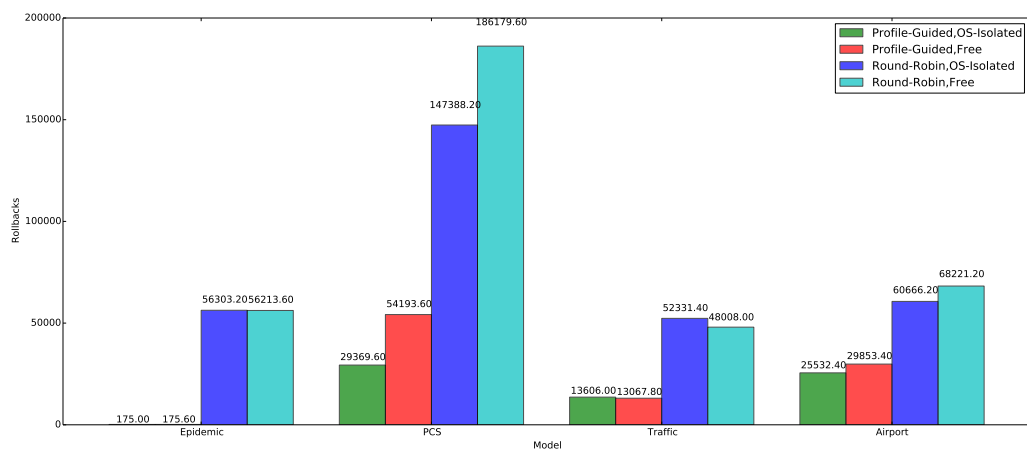
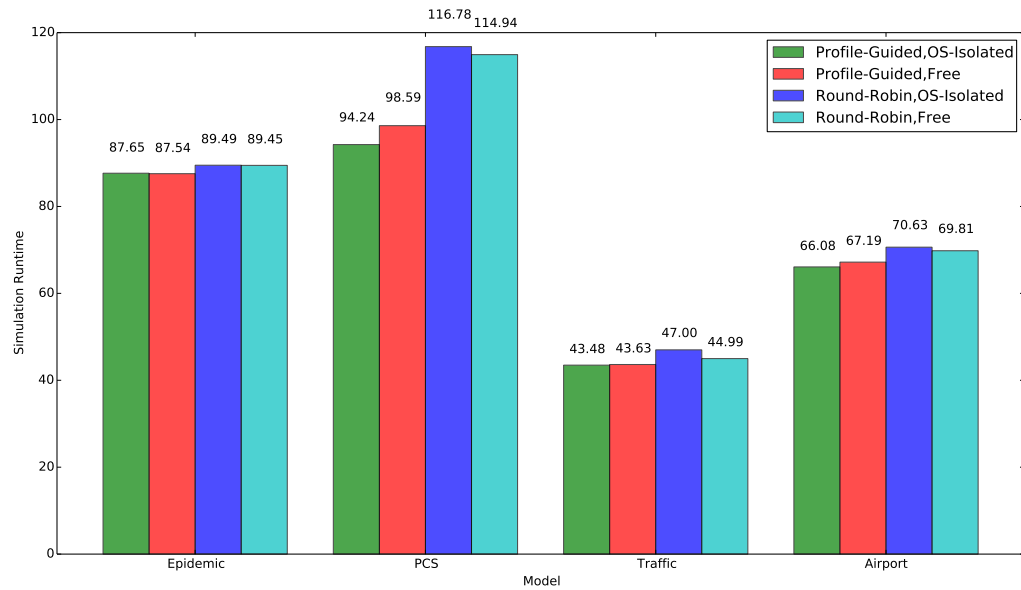


Epidemic

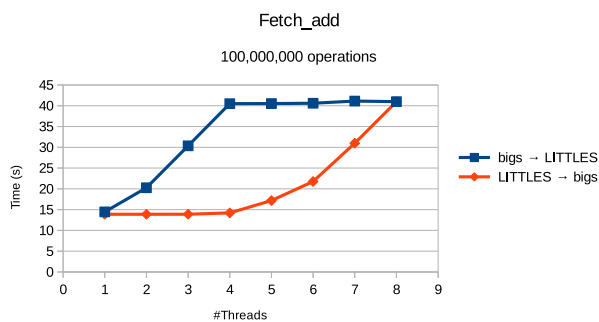


Airport

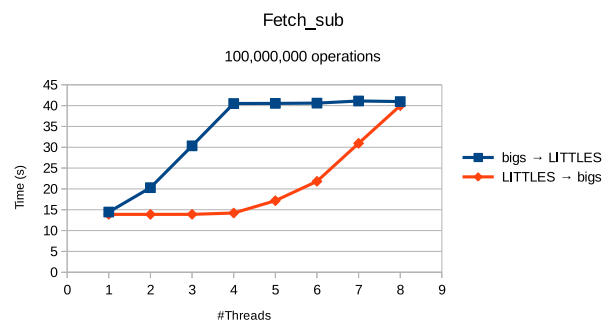
## 9.2 CPU Affinity and LTSF Queue Partitioning



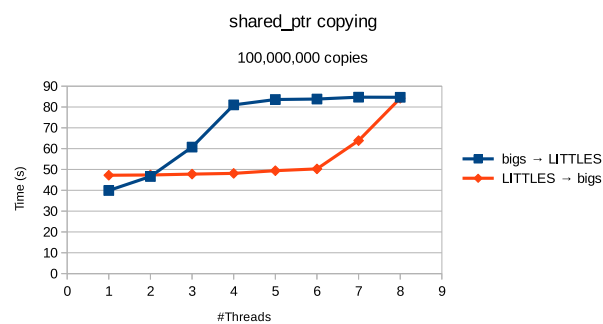
### 9.3 Atomic Instructions on ODROID XU3



Fetch and add



Fetch and subtract



Shared\_ptr copy



## **Chapter 10**

# **Conclusions and Suggestions for Future Research**

### **10.1 Summary of Findings**

### **10.2 Detailed Conclusions**

### **10.3 Suggestions for Future Work**

## Appendix A

### x86 SMP Nodes and Cluster

		Intel® Xeon® X5675	Intel® Xeon® E5410
Processor	ISA	x86_64	x86_64
	# Cores	6	8
	# Threads	12	8
	# Sockets	1	2
	Frequency	3.06 GHz	2.33 GHz
	L1 Data Cache	32kB	32kB
	L1 Inst Cache	32kB	32kB
	L2 Cache	256kB	6MB
	L3 Cache	12MB	N/A
Runtime	OS Kernel	Linux 3.16.0-4-amd64	3.5.0-41-generic
	C Library	Debian GLIBC 2.19-18+deb8u1	Ubuntu EGLIBC 2.15-0ubuntu20.1
	Compiler	GCC v4.9.2	GCC v4.8.5
	MPI	MPICH v3.1	MPICH v1.5
	Malloc	TCMalloc v2.4	TCMalloc v2.4

Table A.1: x86 Assessment Platforms

## Appendix B

# Simulation Models

### B.0.1 PCS

The model describes a type of wireless communication network known as a Portable Cellular Service (PCS) network. A PCS network provides services for a number of *portables* which are the subscribers to the network. The service area of the network is divided into *cells* and a single *port* covers a single cell which has a certain number channels that it can allocate for calls. When an incoming or outgoing call is made, a port must allocate a channel from the port and if one cannot be allocated then the call is *blocked* [22]. The only type of logical processes in the PCS simulation are the cells. The cells in the WARPED2 model form a rectangular grid as shown in figure B.1. Portables can only move to other cells from an adjacent cell with a wrap around occurring at the edges.

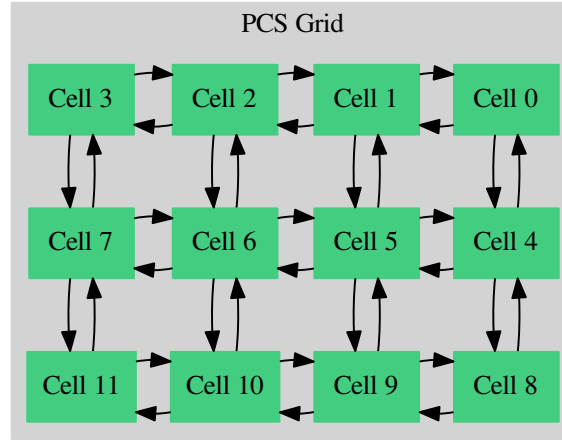


Figure B.1: PCS Model Logical Processes

The portables stay within a cell for a period of time which follows a poisson distribution before moving to another cell. If the all cells are busy in the cell that the portable is moving to then the call is dropped. This is called a *handoff block*.

Every LP keeps track of the following state variables:

- Number of idle channels
- Number of call attempts
- Number of channel blocks
- Number of handoff blocks

Call arrivals to a portable also follow a poisson distribution. The cells in the model generate all of their own incoming calls in a self-initiating process. Four types of events are used to model the network: (1) NextCall, (2) CallCompletion, (3) PortableMoveOut, and (4) PortableMoveIn . The NextCall, CallCompletion, and PortableMoveOut events are all sent to self whereas the PortableMoveIn event is sent to an adjacent cell based on a random variable with uniform probability.

## B.0.2 Traffic

The traffic model describes a grid of intersections and the movement of cars through the intersections and between the intersections. All intersections are four way intersections and have three lanes in each direction. The LPs in the traffic model are the intersections and form a rectangular grid in the same way as that of the PCS model.

The simulation starts with a uniform distribution of cars at each intersection and each car is assigned a destination which it finishes at. With each arrival at an intersection the car always goes in the direction that gets it closer to its destination. The number of cars going out of an intersection in the same direction is limited to a maximum number due to traffic on the destination road. An incoming car that cannot travel in a specific direction is forced back in the same direction it came from and tries another route. For each intersection, the state consists of:

- Number of cars coming into the intersection from each direction
- Number of cars going out of the intersection to each direction
- Total cars arrived at the intersection
- Total cars finished at the intersection

A car goes through three event phase in every intersection as shown in figure B.2. The three types of events are: (1) Arrival, (2) Direction Select, and (3) Departure . The arrival and departure events are mainly used for simple state updates and timing of the next events. The direction select event is where the complexity of the simulation is and determines the direction that the car should go in order to reach its destination or whether the car should take another route due to traffic.

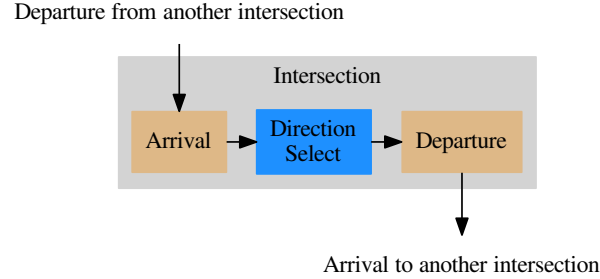


Figure B.2: Intersection Event Sequence

All events in the traffic model follow an exponential distribution and a single event is generated with the processing of each event. The departure and direction select events are self-generated but the arrival event is from an adjacent intersection.

### B.0.3 Epidemic

The epidemic model describes the spreading of an infectious disease across a set of geographic locations in a region and across a set of regions. The logical processes in the simulation are geographic locations which represent a portion of the population. The interactions between people in different geographic locations and regions are modeled with a diffusion network. An abstract view of the simulation model is shown below in figure B.3.

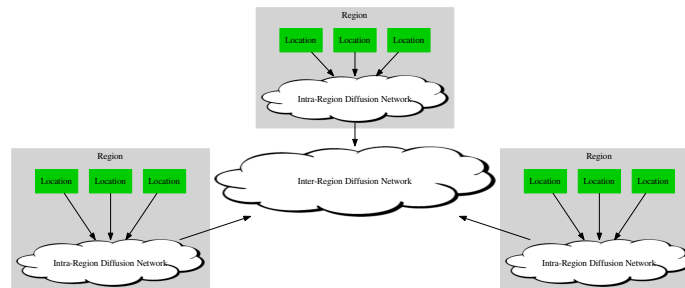


Figure B.3: Epidemic Model

The model is based on reaction-diffusion model [23]. A reaction process models the behavior of the disease within a person as well as the transmission of the disease between people within the same location. A probabilistic reaction function defines the behavior of the disease between individuals [24]. The disease within an individual is modeled by a Probabilistic Timed Transition System (PTTS) [24] which is a finite state machine describing states of the disease and the transitions between states. Together, the reaction function and PTTS models of the disease form the *disease model*. The diffusion network models the social interactions of people between locations and between regions [24].

### **B.0.4 Airport**

The airport model describes the departure and arrivals of airplanes between a connected set of airports. The logical processes represent airports and the events are simply departures and arrivals. For simplicity, the airports are connected in a rectangular grid and can only fly to airports to the north, east, south, or west of the airport departed from. The time spent between takeoff and landing and between landing and takeoff both follow an exponential distributions.

## Appendix C

# Simulation Model Configurations

For all experimental analysis in the following sections, the simulation models will be configured with the parameters shown in figures C.1 and C.2 unless noted otherwise.

Model	Parameter	Value
PCS	# Cells X	100
	# Cells Y	100
	Max Channels per Cell	15
	Mean Call Interval	200
	Mean Call Duration	50
	Mean Move Interval	100
	# Portables per Cell	50
Traffic	# Intersections X	100
	# Intersections Y	100
	# Cars per Intersection	25
	Mean Interval	400
Airport	# Airports X	50
	# Airports Y	50
	Mean Ground Time	50
	Mean Flight Time	200
	# Airplanes per Airport	50

Figure C.1: Model Parameters: PCS, Traffic, and Airport



Model	Parameter Type	Parameter	Value
Epidemic	Diffusion	K	8
		$\beta$	0.1
	Disease	Transmissibility	0.12
		Latent Dwell Time	200
		Latent Infectivity	0
		Incubating Dwell Time	100
		Incubating Infectivity	0.3
		Infectious Dwell Time	400
		Infectious Infectivity	1.0
		Asympt Dwell Time	200
		Asympt Infectivity	0.5
		Prob ULU	0.2
		Prob ULV	0.9
		Prob URV	0.5
		Prob UIV	0.1
		Prob UIU	0.3
		Location Refresh Interval	50
	Region	# Regions	1000
		Min Locations per Region	10
		Max Locations per Region	10
	Location	Min Persons per Location	100
		Max Persons per Location	100
		Min Travel Time to Hub	50
		Max Travel Time to Hub	400
		Min Loc Diffusion Interval	200
		Max Loc Diffusion Interval	500

Figure C.2: Model Parameters: Epidemic

# Bibliography

- [1] S. Das, R. Fujimoto, K. Panesar, D. Allison, and M. Hybinette, “GTW: a Time Warp system for shared memory multiprocessors,” in *Proceedings of the 1994 Winter Simulation Conference* (J. D. Tew, S. Manivannan, D. A. Sadowski, and A. F. Seila, eds.), pp. 1332–1339, Dec. 1994.
- [2] R. M. Fujimoto and M. Hybinette, “Computing global virtual time in shared-memory multiprocessors,” Aug. 1994.
- [3] Wikipedia, “Ticket lock — wikipedia, the free encyclopedia,” 2015. [Online; accessed 5-October-2015].
- [4] D. Jefferson, “Virtual time,” *ACM Transactions on Programming Languages and Systems*, vol. 7, pp. 405–425, July 1985.
- [5] R. Fujimoto, “Parallel discrete event simulation,” *Communications of the ACM*, vol. 33, pp. 30–53, Oct. 1990.
- [6] M. P. Forum, “Mpi: A message-passing interface standard,” tech. rep., Knoxville, TN, USA, 1994.
- [7] H. Avril and C. Tropper, “Clustered time warp and logic simulation,” in *Proceedings of the 9th Workshop on Parallel and Distributed Simulation*, pp. 112–119, 1995.
- [8] C. D. Carothers, D. Bauer, and S. Pearce, “Ross: A high-performance, low memory, modular time warp system,” *Journal of Parallel and Distributed Computing*, pp. 53–60, 2000.
- [9] A. Pellegrini, R. Vitali, and F. Quaglia, “The rome optimistic simulator: Core internals and programming model,” in *Proceedings of the 4th International ICST Conference on Simulation Tools and Tech-*

- niques, SIMUTools '11, (ICST, Brussels, Belgium, Belgium), pp. 96–98, ICST (Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering), 2011.
- [10] D. Jagtap, N. Abu-Ghazaleh, and D. Ponomarev, “Optimization of parallel discrete event simulator for multi-core systems,” in *Parallel Distributed Processing Symposium (IPDPS), 2012 IEEE 26th International*, pp. 520–531, May 2012.
- [11] cppreference, “Pseudo-random number generation,” 2011.
- [12] B. D. Lubachevsky *et al.*, “Rollback sometimes works...if filtered,” in *Winter Simulation Conference*, pp. 630–639, Society for Computer Simulation, Dec. 1989.
- [13] R. Rönngren and M. Liljenstam, “On event ordering in parallel discrete event simulation,” in *Proceedings of the Thirteenth Workshop on Parallel and Distributed Simulation, PADS '99*, (Washington, DC, USA), pp. 38–45, IEEE Computer Society, 1999.
- [14] L. Lamport, “Time, clocks, and the ordering of events in a distributed system,” *Communications of ACM*, vol. 21, pp. 558–565, July 1978.
- [15] Lockless Inc., “Spinlocks and read-write locks,” 2010.
- [16] K. M. Chandy and L. Lamport, “Distributed snapshots: Determining global states of distributed systems,” *ACM Transactions on Computer Systems*, vol. 3, pp. 63–75, Feb. 1985.
- [17] T. Lai and J. Yang, “On distributed snapshots,” *Information Processing Letters*, vol. 25, pp. 153–158, May 1987.
- [18] F. Mattern, “Efficient algorithms for distributed snapshots and global virtual time approximation,” *Journal of Parallel and Distributed Computing*, vol. 18, pp. 423–434, Aug. 1993.
- [19] B. Samadi, *Distributed Simulation, Algorithms and Performance Analysis*. PhD thesis, Computer Science Department, University of California, Los Angeles, CA, 1985.
- [20] D. Bauer, G. Yaun, C. D. Carothers, M. Yuksel, and S. Kalyanaraman, “Seven-oclock: A new distributed gvt algorithm using network atomic operations,” in *Proceedings of the Workshop on Parallel and Distributed Simulation (PADS) 05*, pp. 39–48, IEEE Computer Society, 2005.

- [21] A. O. Holder and C. Carothers, “Analysis of time warp on a 32,768 processor ibm blue gene/l super-computer,” in *Proceedings of the 2008 European Modeling and Simulation Symposium (EMSS '08)*, 2008.
- [22] Y.-B. Lin and P. Fishwick, “Asynchronous parallel discrete event simulation,” *Systems, Man and Cybernetics, Part A: Systems and Humans, IEEE Transactions on*, vol. 26, pp. 397–412, Jul 1996.
- [23] K. S. Perumalla and S. K. Seal, “Discrete event modeling and massively parallel execution of epidemic outbreak phenomena,” *Simulation*, vol. 88, pp. 768–783, July 2012.
- [24] C. Barrett, K. Bisset, S. Eubank, X. Feng, and M. Marathe, “Episimdemics: An efficient algorithm for simulating the spread of infectious disease over large realistic social networks,” in *High Performance Computing, Networking, Storage and Analysis, 2008. SC 2008. International Conference for*, pp. 1–12, Nov 2008.