

**Course of Study and Scheme of Examination**  
**B.E. Electrical Engineering**  
**SEMESTER – V**

Scheme: BE(EE)\_JULY2009

Implemented from academic session 2009-10

S.No	Course Category	Course Code (New)	Subject	Period Per Week				Distribution of Marks					
								Theory Exam	Practical Exam	Internal Assessment			Total
										MST	TW	Total	
				L	T	P	C	I	II	III			I+II+III
1.	DC-9	EE501	Electrical Machine-II	3	1	2	6	100	50	20	30	50	200
2.	DC-7	EE502	Electronic Instrumentation	3	1	2	6	100	50	20	30	50	200
3.	DC-8	EE503	<u>Signals &amp; Systems</u>	3	1	0	4	100	-	20	-	20	120
4.	DID-5	EE504	<u>Digital Electronics &amp; Logic Design</u>	3	1	2	6	100	50	20	30	50	200
5.	HS-3	EE505	<u>Principles of Management and Managerial Economics</u>	3	1	0	4	100	-	20	-	20	120
6.		EE506	Electrical Engg. Simulation Lab	0	0	4	4	-	50	-	50	50	100
7.	NECC-5	EE507	Self Study	0	0	1	1	-	-	-	30	30	30
8.	NECC-6	EE508	Seminar/Group Discussion etc.	0	0	1	1	-	-	-	30	30	30
<b>Total</b>				<b>9</b>	<b>3</b>	<b>12</b>	<b>24</b>	<b>500</b>	<b>200</b>	<b>100</b>	<b>200</b>	<b>300</b>	<b>1000</b>

**Minimum Pass Marks**  
**(A) Theory: 35 Percent**  
**(B) Practical: 50 Percent**

**Duration:**

**[C] Duration of Theory Paper 3 hrs.**