

Example: Add 6-bit numbers -17_{10} and 23_{10}

From before:

$$\begin{aligned} -17_{10} &= 10111_2 \\ 23_{10} &= 01011_2 \end{aligned}$$

$$\begin{array}{r}
 11111111 \\
 101111 \\
 \underline{010111} \\
 (1) \quad 000110
 \end{array}$$

6-bit solution

Important: In unsigned addition, this carry-out means an overflow has occurred

2's-complement addition: always ignore end carry!
(may or may not indicate overflow)

2's-complement range for n-bit numbers

<u>n</u>	<u>range</u>
4	$-8 \leq N \leq 7$
16	$-32768 \leq N \leq 32767$
32	$-2,147,483,648 \leq N \leq 2,147,483,647$

Overflow: Only possible if adding numbers of the same sign

Example: $23_{10} + 23_{10}$ using 6 bits, Express answer in signed decimal notation (e.g., -10, +27, etc.)

One way to recognize overflow:

(b)

0	1	1	1
	0	1	1
	0	1	1
	0	1	1
	1	0	1

Answer: 101110

whoops! looks negative

Answer is clearly wrong. Convert to signed decimal anyway.

- negative so perform 2's-complement negation to find its' magnitude

$$\begin{array}{r} 101110 \longrightarrow 010001 \\ + \quad \quad \quad 1 \\ \hline 010010 \end{array} = 16 + 2 = 18_{10} \quad (\text{answer} = -18_{10})$$

$$\text{so } 23_{10} + 23_{10} = -18? \quad (\text{wrong!})$$

Overflow never occurs when adding numbers of opposite sign

Another way to recognize overflow

$$\begin{array}{r} \text{0} \quad \text{1} \quad \text{1} \quad \text{1} \quad \text{1} \\ \text{010111} \quad (+23) \\ + \text{010111} \quad (+23) \\ \hline \text{101110} \quad (-18) \end{array}$$

check last two carry bits

If the carry-in and carry-out on the last bit

- are opposite \rightarrow overflow!
- are the same \rightarrow no overflow

Some important codes

So far, we have discussed binary codes, namely unsigned binary, sign/magnitude, and 2's-complement,

Other important codes include ASCII for text, BCD codes for base-10 numbers, Gray codes.

Binary-Coded Decimal (BCD) codes

These codes are useful in human-machine interfaces - a convenient way of representing decimal numbers

Decimal number

BCD code

0

0000

← each BCD code word
is 4 bits

1

0001

2

0010

3

0011

:

:

8

1000

9

1001

Codes 1010 - 1111 not used;
Invalid BCD codes

Example: Represent the decimal number 2468_{10} in BCD

We have 4 digits, and each one gets a 4-bit code

0010 0100 0110 1000
2 4 6 8

A very common device for displaying decimal numbers is called
a seven-segment display



← a segment; typically a LED that
can be switched on and off

Found in some scoreboard displays, clock radios, appliances.
- forms digits by appropriately illuminating segments.

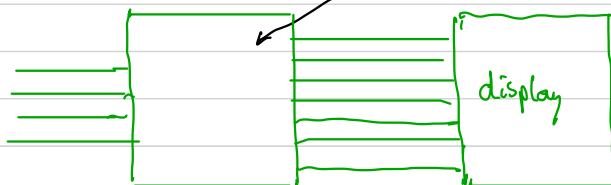
e.g.,



(five)

BCD to 7-segment decoder

4-bit
BCD code



Gray code

A very important code. A 3-bit Gray code looks like

<u>Number</u>	<u>Binary code</u>	<u>Gray code</u>
0	000	000
1	001	001
2	010	011
3	011	010
4	100	110
5	101	111
6	110	101
7	111	100