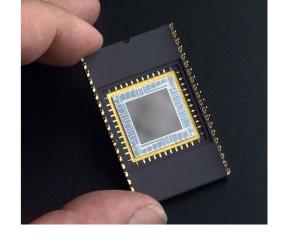


## Flow Diagram for Computer Imaging



Interline Transfer CCD Architecture 4-Pixel Array Photodiode Parallel CCD/Photodiode Shift Register Parallel Shift Register Clock Parallel Direction Serial Shift Output Amplifier Register Clock Serial CCD Control Figure 1 Shift Register

## **CCD** Chip

http://electronicdesign.com/displays/digital-camera-chip-set-delivers-high-quality-images-50-mpixelss