Wang Zhui

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EDUCATION

- **Northwestern Polytechnical University** — Xi'an, Shaangxi
  Bachelor degree in Electromagnetic Field and Wireless Technology ; GPA: 3.5 — *Sep. 2013 – Jul. 2017*
- **Coursera**:
  - **Electronic**: Digital Electronic
  - **C Peogramming**:

SKILLS

- **Signal Intergrity Simulation** — Xi'an, Shaangxi
  Pcb about — *Sep. 2016 – Jul. 2017*
  - **Board Power Intergrity Simulation**: AD78 PCB board PI simulation
  - **100G serdes Signal intergrity Simulation**: 100G serdes transmition line S-paramenters and Eye-graphic Simulation
  - **Throgh Hole Via Impedance**: Find the via, which imdepance is 50ohm

- **Linuxusing and Programming** — Suzhou, JiangSu
  Software Design Network — *Sep. 2016 Jul. 2017*
  - **Shadowsock vps build**: Building VPN within shadowsocks, so i can free surf on the Internet, unless the government seal all the IP of foreign serve. Im really exciting about this. I can get more knowledge on YouTube.
  - **Jekyll Website**: www.wangzhzui.info,welcome to my website.this website is build by liquid language, compile by jekyll.

- **Latex** — Suzhou, JiangSu
  Write Article — *sep.2017 – Now*
  - **ATCA testreport**: I have wrote a test report about atca, whitch have hardware test and software test. I start get to know about the usage of chapter section paragraph table grahics.and get the solution of using chinese. I gradually learned document typesetting basic package after a period of study. I have write a quick guide about start latex using
  - **Product quick start guide**: I have write a quick start guide about the hardware introduce about my companys Smart Network Card
  - **Vitae**: i want to make HR surprise about my beautiful vitae,so i get to know how to write a vitae within latex

EXPERIENCE

- **Northwestern Polytechnical University** — Xian, ShaangXi
  Bachelor degree in Electromagnetic Field and Wireless Technology ; GPA: 3.5 — *Sep. 2013 – Jul. 2017*
  - **Electronics design competition**: Second Prize of the competition
  - **Mathematical modeling**: Third Prize
- **Teraspek** — Suzhou, JiangSu
  Electronics Engineer — *Mar. 2017 – Now*
  - **AdvancedTCA Test**: Advanced Telecommunications Computing Architecture

PROJECTS

- **Siwave simulation SI&PI**: Via, 100G serdes Clock USB3.0 Signal.
- **PCB Board ripair**: Resolve the probelm of pcb board Power-on and Linux system Boot-up.
- **Product test**: Atca test and report.
- **Web**: Personal website.

REQUIRE

- **Job require**: I am exciting about......

Thanks for your reading