# AC6366C Datasheet

# Zhuhai Jieli Technology Co.,LTD

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## **AC6366C Features**

### **CPU**

- 32-bit DSP supports hardware Float Point Unit (FPU)
- Up to 160MHz programmable processor
- 64Vectored interrupts
- 4 Levels interrupt priority

### **DSP Audio Processing**

- SBC, AAC Audio decodes supported for BT audio
- mSBC voice codecs supported for BT phone
- Supports MP2, MP3, WMA, APE, FLAC, AAC, MP4, M4A, WAV, AIF, AIFC audio decoding
- Packet Loss Concealment (PLC) for voice processing
- Acoustic echo cancellation/suppression (AEC,AES)
- Single/Dual MIC Environmental Noise Cancellation (ENC)
- Multi-band DRC limiter
- 10-band EQ configuration for voice Effects

#### **Audio Codec**

- Two channels 16-bit DAC, SNR >= 95dB
- One channels 16-bit ADC, SNR >= 90dB
- Sampling rates of 8KHz/11.025KHz/16KHz/22.05KHz/24KHz/32KHz/44.1KHz/48KHz are supported
- One analog MIC amplifier, build-in MIC bias generator
- Supports two PDM digital MIC inputs
- Two channels Mono analog MUX
- Supports cap-less, single-ended, and differential mode at the DAC path
- Supports 16ohm and 32ohm Speaker loading

### Bluetooth

- Compliant with Bluetooth V5.1+BR+EDR+BLE specification
- Meet class1 class2 and class3 transmitting

- power requirement
- Support GFSK and π/4 DQPSK all packet types
- Provides +6dbm transmitting power
- receiver with -90dBm sensitivity
- Fast AGC for enhanced dynamic range
- Supports

  a2dp\avctp\avdtp\avrcp\hfp\spp\smp\att\gap\
  gatt\rfcomm\sdp\l2cap profile

### Peripherals

- One full speed USB 2,0 OTG controller
- Six multi-function 32-bit timers, support capture and PWM mode
- Three full-duplex basic UART, UART0 and UART1 supports DMA mode
- Two SPI interface supports host and device mode
- One hardware IIC interface supports host and device mode
- 10-bit ADC for analog sampling
- External wake up/interrupt on all GPIOs

### **PMU**

- Low voltage LDO for internal digital and analog circuit supply
- 3uA current consumption in the soft-off mode
- Built-in LDO for the core, I/O, Bluetooth and flash
- VBAT is 2.2V to 4.5V
- VDDIO is 2.2V to 3.4V

#### **Temperature**

- Operating temperature: -40°C to +85°C
- Storage temperature: -65°C to +150°C

#### **Packages**

**QFN32(4mm\*4mm)** 

#### **Applications**

Bluetooth IOT

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## 1. Pin Definition

## 1.1 Pin Assignment

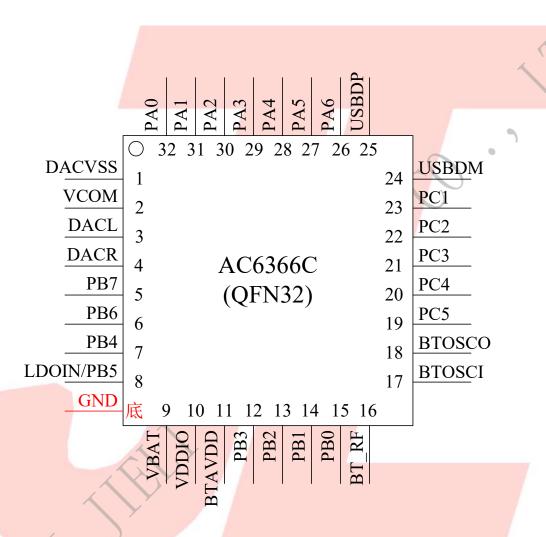


Figure 1-1 AC6366C QFN32 Package Diagram

## 1.2 Pin Description

Table 1-1 AC6366C\_QFN32 Pin Description

PIN NO.	Name	I/O Type	Drive (mA)	Function	Other Function
1	DACVSS	P	/		DAC Ground;
2	VCOM		/		
3	DACL	0	/	A y	DAC Left Channel;
4	DACR	0	/	Y	DAC Right Channel;
5	PB7	I/O	24/8	GPIO	AMUX1R: Analog Channel1Right; SPI2DOA: SPI2 Data Out(A); IIC_SDA_C: IIC DAT(C); ADC9: ADC Input Channel 9; PWM5: Timer5 PWM Output; UART1RXA: Uart1 Data In(A);
6	PB6	I/O	24/8	GPIO	AMUX1L: Analog Channel1 Left; SPI2CLKA: SPI2 Data Out(A); IIC_SCL_C: IIC SCL(C); ADC8: ADC Input Channel 8; TMR3: Timer3 Clock Input; UART1TXA: Uart1 Data Out(A);
7	PB4	I/O	24/8	GPIO	ADC7: ADC Input Channel 7; CLKOUT1; UART2TXC: Uart2 Data Out(C); UART2RXC: Uart2 Data In(C);
8	PB5	I/O	8	GPIO (High Voltage Resistance)	PWM3: Timer3 PWM Output; SPI2_DIA: SPI2 Data In(A); CAP1: Timer1 Capture; UART0TXC: Uart0 Data Out(C); UART0RXC: Uart0 Data In(C);
	LDOIN	P	/		Battery Charger In;
9	VBAT	P	/		Battery Power Supply;
10	VDDIO	P	/		IO Power 3.3v;
11	BTAVDD	P	/		BT Power;
12	PB3	I/O	/	GPIO	SD0DAT_D: SD0 Data(D); ADC6: ADC Input Channel 6; PWM2: Timer2 PWM Output; UART2RXB: Uart2 Data In(B);

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13	PB2	I/O	8	GPIO (High Voltage Resistance)	SD0CMD_D: SD0 Command(D); SPI1DIA: SPI1 Data In(A); CAP0: Timer0 Capture; UART2TXB: Uart2 Data Out (B);
14	PB1	I/O	24/8	GPIO (pull up)	Long Press Reset; SPI1DOA: SPI1 Data Out(A); ADC5: ADC Input Channel 5; TMR2: Timer2 Clock Input; UART0RXB: Uart0 Data In(B); SPDIF_IN_D: Sony/Philips Digital Interface Input(D);
15	РВ0	I/O	8	GPIO (High Voltage Resistance)	SD0CLK_D:SD0Clock(D)  SPI1CLKA:SPI1 Clock(A);  UART0TXB:Uart1 Data Out(B);  TMR5:Timer5 Clock Input;  SPDIF_IN_C: Sony/Philips Digital Interface Input(C);
16	BT_RF	/		7./	BT Antenna
17	BTOSCI	I			BT OSC In
18	BTOSCO	О			BT OSC Out
19	PC5	I/O	24/8	GPIO	SD0CLK_AE: SD0 Clock(AE) SPI1DOB: SPI1 Data Out(B); IIC_SDA_B: IIC SDA(B); ADC12: ADC Input Channel 12; TMR1: Timer1 Clock Input; UART2RXD: Uart2 Data In(D);
20	PC4	I/O	24/8	GPIO	SD0CMD_A: SD0 Command(A); SPI1CLKB: SPI1 Clock(B); IIC_SCL_B: IIC SCL(B); ADC11: ADC Input Channel 11; PWM1: Timer1 PWM Output; UART2TXD: Uart2 Data Out (D);
21	PC3	I/O	24/8	GPIO	SD0DAT_A: SD0 Data(A); SPI1DIB: SPI1 Data In(B); CAP2: Timer2 Capture; UART0TXD: Uart0 Data Out (D); UART0RXD: Uart0 Data In(D);
22	PC2	I/O	24/8	GPIO	ALNK_MCLK_B: ALNK Master Clock(B); ADC10: ADC Input Channel 10; CAP5: Timer5 Capture; UART1RXB: Uart1 Data In(B);
23	PC1	I/O	24/8	GPIO	TMR0: Timer0 Clock Input; UART1TXB: Uart1 Data Out(B);

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24	USBDM	I/O	4	USB Negative Data (pull down)	IIC_SDA_A: IIC SDA(A); SPI2_DOB: SPI2 Data Out(B); ADC14: ADC Input Channel 14; UART1RXD: Uart1 Data In(D);
25	USBDP	I/O	4	USB Positive Data (pull down)	IIC_SCL_A: IIC SCL(A); SPI2_CLKB: SPI2 Clock(B); ADC13: ADC Input Channel 13; UART1TXD: Uart1 Data Output(D);
26	PA6	I/O	24/8	GPIO	ALNK_DAT3_A: Audio Link Data3_A; ALNK_LRCK_B: Audio Link Word Select(B); IIC_SDA_D: IIC SDA(D); ADC4: ADC Input Channel 4; CAP4: Timer4 Capture; UART0RXA: Uart0 Data In(A); SPDIF_IN_B: Sony/Philips Digital Interface Input(B);
27	PA5	I/O	24/8	GPIO	ALNK_DAT2_A: Audio Link Data2_A; ALNK_SCLK_B: Audio Link Serial Clock(B); IIC_SCL_D: IIC SCL(D); PWM0: Timer0 PWM Output; UART0TXA: Uart0 Data Output(A); SPDIF_IN_A: Sony/Philips Digital Interface Input(A);
28	PA4	ľO	24/8	GPIO	PLNK_DAT1: PLNK Data1; ALNK_LRCK_A: Audio Link Word Select(A); ALNK_DAT3_B: Audio Link Data3_B; SD0CMD_CE: SD0 Command(CE) AMUX0R: Analog Channel0 Right; UART1_RTS: Uart1 Request to send; ADC3: ADC Input Channel 3; TMR4: Timer4 Clock Input; UART2RXA: Uart2 Data In(A);
29	PA3	I/O	24/8	GPIO	PLNK_SCLK: PLNK Serial Clock; ALNK_SCLK_A: Audio Link Serial Clock(A); ALNK_DAT2_B: Audio Link Data2_B; SD0DAT_C: SD0 Data(C); AMUX0L: Analog Channel0 Left; UART1_CTS: Uart1 Clear to send; ADC2: ADC Input Channel 3; PWM5: Timer5 PWM Output; UART2TXA: Uart1 Data Output(D);

30	PA2	I/O	24/8	GPIO	ALNK_MCLK_A: ALNK Master Clock_A; ALNK_DAT1_B: Audio Link Data1_B; SD0CLK_C: SD0 Clock(C); MIC_BIAS: Microphone Bias Output CAP3: Timer3 Capture;
31	PA1	I	24/8	GPIO	MIC: MIC Input Channel; ADC1: ADC Input Channel 1; PWM4: Timer4 PWM Output; UART1RXC: Uart0 Data In(C);
32	PA0	I/O	/	GPIO	ALNK_DATO_A: Audio Link DataO_A; ALNK_DATO_B: Audio Link DataO_B; SDPG: SD Power Supply; ADCO: ADC Input Channel 0; CLKOUTO; UART1TXC: Uart1 Data Output(C);
	Substrate	GND	- /	Substrate	

## 2, Electrical Characteristics

## 2.1 Absolute Maximum Ratings

Table 2-1

Symbol	Parameter	Min	Max	Unit
Topt	Operating temperature	-40	+85	°C
Tstg	Storage temperature	-65	+150	°C
VBAT	Supply Voltage	-0.3	4.5	V
LDOIN	Charger Voltage	-0.3	6	V
V <sub>3.3IO</sub>	3.3V IO Input Voltage	-0.3	3.6	V

Note: The chip can be damaged by any stress in excess of the absolute maximum ratings listed below

## 2.2 Recommended Operating Conditions

Table 2-2

Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
VBAT	Voltage Input	2.2	3.7	4.5	V	
LDOIN	Charger Voltage	4.5	5	5.5	V	- V
V <sub>VDDIO</sub>	Voltage output	2.2	3.0	3.4	V	VBAT = 4.2V, 100mA loading
V <sub>BT_AVDD</sub>	Voltage output	- 1	1.3		V	VBAT=4.2V, 100mA loading
$I_{vddio}$	Loading current	<b>\</b> -	_//	150	mA	VBAT = 4.2V

## 2.3 Battery Charge

**Table 2-3** 

Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
LDO_IN	Charge Input Voltage	4.5	5	5.5	V	_
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	Voluge					
VCharge	Charge Voltage	4.15	4.2	4.25	V	_
$I_{ m Charge}$	Charge Current	20		300	mA	Charge current at fast charge mode
$I_{\mathrm{Trikl}}$	Trickle Charge Current	20	45	70	mA	$V_{BAT}\!\!<\!\!V_{Trikl}$

## 2.4 IO Input/Output Electrical Logical Characteristics

Table 2-4

IO input ch	IO input characteristics									
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions				
$V_{\rm IL}$	Low-Level Input Voltage	-0.3	-	0.3* VDDIO	V	VDDIO = 3.3V				
$ m V_{IH}$	High-Leve <mark>l Input</mark> Volta <mark>ge</mark>	0.7* VDDIO	-	VDDIO+0.3	V	VDDIO = 3.3V				
IO output	characteristi <mark>cs</mark>									
$V_{ m OL}$	Low-Level Output Voltage	-	_	0.33	V	VDDIO = 3.3V				
V <sub>OH</sub>	High-Level Output Voltage	2.7	_	/-/	V	VDDIO = 3.3V				

## 2.5 Internal Resistor Characteristics

Table 2-5

Port		General Output	High Drive	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PB1,PB	1~PA6 4,PB6,PB7 1~PC5	8mA	24mA	10K	10K	1、PB1 default pull up 2、USBDM & USBDP default pull down
PA0 PB3	Output 0 Output 1	8mA	24mA 64mA	10K	10K	3、PB0,PB2,PB5 can pull-up resistance to 5V 4、internal
PB0, I	PB2, PB5	8mA	<b>&gt;</b>	10K	10K	pull-up/pull-down resistance   accuracy
US	SBDP	4mA	/ _	1.5K	15K	±20%
US	BDM	4mA	<i>J-</i> /-	180K	15K	

## 2.6 DAC Characteristics

Table 2-6

Parameter	Min	Тур	Max	Unit	Test Conditions
Frequency Response	20	_	20K	Hz	
THD+N	_	-75	_	dB	1KHz/0dB
S/N	_	95	_	dB	10Kohm loading
Crosstalk	_	-90	_	dB	With A-Weighted Filter
Output Swing		1		Vrms	
					1KHz/-60dB
Dynamic Range		90		dB	10Kohm loading
					With A-Weighted Filter
DAC Output Power	11		_	mW	32ohm loading

## 2.7 ADC Characteristics

Table 2-7

Parameter		Min	Тур	Max	Unit	<b>Test Conditions</b>
Dynamic Range			80		dB	1KHz/-60dB
S/N		_	90	91	dB	
THD+N		_	-70	_	dB	1KHz/-60dB
Crosstalk		_	-90	_	dB	1

## 2.8 BT Characteristics

### 2.8.1 Transmitter

**Basic Data Rate** 

Table 2-8

Parameter		Min	Тур	Max	Unit	Test Conditions
RF Transmit Power			4	6	dBm	
RF Power Control Range			20		dB	25°C,
20dB Bandwidth			950	O	KHz	Power Supply
	+2MHz	1	-40	Y	dBm	
Adjacent Channel	-2MHz		-38		dBm	VBAT=5V
Transmit Power	+3MHz	(	-44		dBm	2441MHz
	-3MHz		-35		dBm	

**Enhanced Data Rate** 

Table 2-9

Parameter		Min	Тур	Max	Unit	Test Conditions
Relative Power			-1		dB	
π/4 DQPSK	DEVM RMS		6		%	
7	DEVM 99%		10		%	25°C,
Modulation Accuracy	DEVM Peak		15		%	Power Supply
	+2MHz		-40		dBm	VBAT=5V
Adjacent Channel	-2MHz		-38		dBm	2441MHz
Transmit Power	+3MHz		-44		dBm	
	-3MHz		-35		dBm	

### 2.8.2 Receiver

### **Basic Data Rate**

**Table 2-10** 

Paramete	Min	Тур	Max	Unit	Test Conditions	
Sensitivity			-90		dBm	
Co-channel Interference Rejection			-13		dB	
	+1MHz		+5		dB	25℃,
	-1MHz		+2		dB	Power Supply
Adjacent Channel	+2MHz		+37		dB	VBAT=5V
Interference Rejection	-2MHz		+36		dB	2441MHz
	+3MHz		+40		dB	
	-3MHz		+35		dB	

### **Enhanced Data Rate**

**Table 2-11** 

Paramete	Min	Тур	Max	Unit	Test Conditions	
Sensitivity			-90		dBm	
Co-channel Interference Rejection			-13	2	dB	
	+1MHz		+5		dB	25°C,
	-1MHz		+2		dB	Power Supply
Adjacent Channel	+2MHz		+37		dB	VBAT=5V
Interference Rejection	-2MHz		+36	y	dB	2441MHz
	+3MHz		+40		dB	
	-3MHz	7	+35		dB	

## 3. Package Information

## 3.1 QFN32(4mm\*4mm)

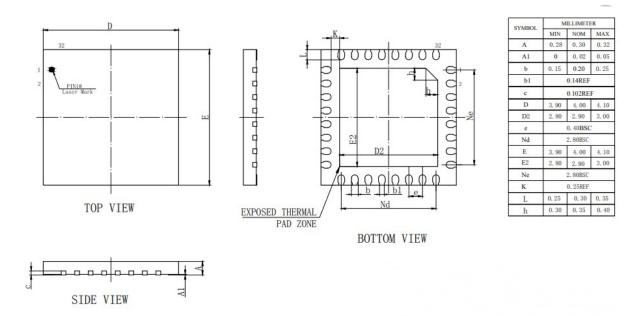


Figure 3-1 AC6366C\_QFN32 Package

# 4. Revision History

Date	Revision	Description
2020.08.26	V1.0	Initial Release

