

Modules Explanation

A. Control

It reads the opcode to identify these 8 types of instruction into R-type and non R-type. Depending on this identification, we can assign different values of *ALUOp_o* and *ALUSrc_o* respectively. Moreover, since all types of instruction modify the content of register, *RegWrite_o* will always be set to 1.

B. ALU control

It decides the computation behavior of ALU based on the value of *funct3*, *funct7* and *ALUOp_i*, which comes from the Control module. As one of them mentioned above changes, it will reassign the value of *ALUCtrl_o*, which has 8 types corresponding to 8 different instructions.

C. Sign extend

It reads the 12 bits long data and does sign extension based on the 11th digit of it, which makes it a 32 bits long data.

D. ALU

It will read two 32 bits long data and do the required operation according to the value of *ALUCtrl_i*, which we assign in the ALU control module. Moreover, since each time 2 input data or *ALUCtrl_i* change it has to take action, I put it in an always block.

E. MUX32

It helps us to identify which data should be chosen to go to ALU, and it selects it based on *select_i*, which is actually *ALUSrc_o* from the Control module. As I mentioned above, it only divides 8 types of instructions into the group of R-type and the group of remaining. The former requires the data from register2, while the latter requires the data from the Sign extend module.

F. Adder

Adder module simply adds current PC with 4. Hence, it requires current PC value in 32 bits long and reads 4 as a 32 bits long data, too. After that, it just sums them up.

Development Environment

I use Ubuntu 20.04 and iverilog as compiler. Besides, I use VS code to edit it (I think it is useless information.)