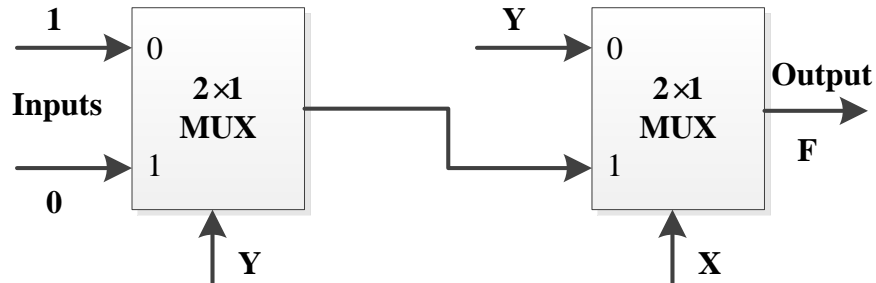1. Quiz for Chapter 3&5
1) PLA consists of _programmable_ AND arrays and _programmable_ OR arrays.

   B

2) Please find the logic expression of output F in the figure.

Answer：F = X'Y + XY' = X ⊕ Y

3) The signed-magnitude of 8-bit binary number $(-32)_{10}$ is__10100000__; its 1s complement is__11011111__; its 2s complement is__11100000__.

4) Perform the arithmetic operation (-35) - (-24) in 7-bit binary using signed-complement arithmetic.

Answer：
-35 = 1011101
-24 = 1101000

$$
\begin{array}{rr}
-35 & 1011101 \\
- (-24) & + \quad 0011000 \\
\hline
= -11 & = \quad 1110101
\end{array}
$$