



Wind River Diab Compiler Target Configuration Reference, 5.9.7

31 January 2020

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TABLE OF CONTENTS

1. Target Configuration.	1
1.1. About Target Configuration.	1
2. Architectures and Configurations.	2
2.1. ARM Target Configuration Options.	2
2.2. CKCORE Target Configuration Options.	4
2.3. ColdFire Target Configuration Options.	5
2.4. M*CORE Target Configuration Options.	8
2.5. MC68K Target Configuration Options.	9
2.6. MCS Target Configuration Options.	10
2.7. MIPS Target Configuration Options.	11
2.8. MIPS-IV Target Configuration Options.	13
2.9. PAsemi Target Configuration Options.	14
2.10. Pentium Target Configuration Options.	16
2.11. PowerPC Target Configuration Options.	17
2.12. PowerPC Compressed Target Configuration Options.	22
2.13. PowerPC VLE Target Configuration Options.	22
2.14. PPC e6500 Target Configuration Options (32-bit).	25
2.15. PPC 64-bit Target Configuration Options.	27
2.16. RH850 Target Configuration Options.	28
2.17. SH Target Configuration Options.	29
2.18. SPARC Target Configuration Options.	31
2.19. TriCore Target Configuration Options.	32

1. TARGET CONFIGURATION

1.1. About Target Configuration

The compiler tools use a target configuration to set the compilation and linking parameters for a given target processor.

A complete target configuration includes the following:

- target processor
- object module format
- type of floating point support
- execution environment, which sets the default libraries for input/output and target operating system support

The following tables describe the available options.

For information about the different ways to specify a target configuration, see the Wind River Diab Compiler User's Guide for your architecture.

 **Note:** Some combinations of processor, floating point support, object module format, and environment may not be valid. Use the `dctrl -t` command to determine if a combination is valid.

2. ARCHITECTURES AND CONFIGURATIONS

2.1. ARM Target Configuration Options

ARM target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

ARM	ARM968E-S	ARM968E-ST
ARMCORTEXA15	ARMCORTEXA15T2	ARMCORTEXA5
ARMCORTEXA5T2	ARMCORTEXA53	ARMCORTEXA53T2
ARMCORTEXA7	ARMCORTEXA7T2	ARMCORTEXA8
ARMCORTEXA8T2	ARMCORTEXA9	ARMCORTEXA9T2
ARMCORTEXM0	ARMCORTEXM0P	ARMCORTEXM1
ARMCORTEXM3	ARMCORTEXM4	ARMCORTEXR4
ARMCORTEXR4T2	ARMCORTEXR5	ARMCORTEXR5T2
ARMCORTEXR7	ARMCORTEXR7T2	ARMT
ARMT2	ARMV5E	ARMV5ET
ARMV6	ARMV6K	ARMV6MT
ARMV7	ARMV7A	ARMV7AT2
ARMV7MT2	ARMV7R	ARMV7RT2
ARMV8A	ARMV8AT2	ARMX
ARMXT		

ARM V8

ARMV8A	ARMV8AT2	ARMCORTEXA53
ARMCORTEXA53T2		

Object Module Formats

Code	Description
E	ARM(XSCALE)/Thumb ELF Object Format
F	ARM(XSCALE)/Thumb AAELF Object Format
L	ARM(XSCALE)/Thumb ELF little-endian Object Format
M	ARM(XSCALE)/Thumb AAELF little-endian Object Format

Floating Point Support

Code	Description
A	Advanced SIMD/NEON
N	No Floating Point
S	Software Floating Point
V	Vector Floating Point
X	Vector Floating Point and Advanced SIMD/NEON

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
vxworks55	vxworks55 - Interface with VxWorks 5.5
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5

Code	Description
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
vxworks7	vxworks7 - Interface with VxWorks 7
rtp7	rtp7 - Real Time Process for VxWorks 7
other	other - Enter text string when requested

2.2. CKCORE Target Configuration Options

CKCORE target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

MCORE310	CK510E	CK610
CK610E		

Object Module Formats

Code	Description
E	M*CORE ELF ABI big-endian Object Format
L	M*CORE ELF ABI little-endian Object Format

Floating Point Support

Code	Description
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
other	other - Enter text string

2.3. ColdFire Target Configuration Options

ColdFire target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

MCF51AC128	MCF51AC256	MCF51AG128
MCF51AG96	MCF51CN128	MCF51JE128
MCF51JE256	MCF51JF128	MCF51JF32
MCF51JF64	MCF51JM128	MCF51JM32
MCF51JM64	MCF51JU128	MCF51JU32
MCF51JU64	MCF51MM128	MCF51MM256
MCF51QE128	MCF51QE32	MCF51QE64
MCF51QF128	MCF51QF32	MCF51QF64
MCF51QH128	MCF51QH32	MCF51QH64
MCF51QM128	MCF51QM32	MCF51QM64
MCF51QU128	MCF51QU32	MCF51QU64
MCF51QW256	MCF5200	MCF5202
MCF5203	MCF5204	MCF5206
MCF5206e	MCF5207	MCF5208

MCF52100	MCF5211	MCF52110
MCF5212	MCF5213	MCF5214
MCF5216	MCF52210	MCF52211
MCF52212	MCF52213	MCF52221
MCF52223	MCF52230	MCF52231
MCF52232	MCF52233	MCF52234
MCF52235	MCF52236	MCF52252AF80
MCF52252CAF66	MCF52254AF80	MCF52254CAF66
MCF52255CAF80	MCF52256AG80	MCF52256CAG66
MCF52256CVN66	MCF52256VN80	MCF52258AG80
MCF52258CAG66	MCF52258CVN66	MCF52258VN80
MCF52259CAG80	MCF52259CVN80	MCF52274
MCF52277	MCF5232	MCF5233
MCF5234	MCF5235	MCF5248
MCF5249	MCF5251	MCF5253
MCF5270	MCF5271	MCF5272
MCF5274	MCF5275	MCF5280
MCF5281	MCF5282	MCF53010
MCF53011	MCF53012	MCF53013
MCF53014	MCF53015	MCF53016
MCF53017	MCF5307	MCF5327
MCF5328	MCF53281	MCF5329
MCF5307	MCF5307	MCF5307
MCF5307	MCF5307	MCF5400
MCF5407	MCF5407E	MCF54410

MCF54415	MCF54416	MCF54417
MCF54418	MCF54450	MCF54451
MCF54452	MCF54453	MCF54454
MCF54455	MCF547x	MCF548x
MCFV1	MCFV2	MCFV3
MCFV4	MCFV4E	MCFV4M

Object Module Formats

Code	Description
F	ColdFire ELF Object Format

Floating Point Support

Code	Description
N	No Floating Point
S	Software Floating Point
H	Hardware Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem.
vxworks55	vxworks55 - Interface with VxWorks 5.5
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4

Code	Description
rtp	rtp - Real Time Process for VxWorks 6.x
other	other - Enter text string when requested

2.4. M*CORE Target Configuration Options

M*CORE target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

MCORE	MCORE200	MCORE210
MCORE210	MCORE210	MCORE210
MCORE210	MCORE210	MCORE310
MCORE330	MCORE340	

Object Module Formats

Code	Description
E	M*CORE ELF ABI big-endian Object Format
L	M*CORE ELF ABI little-endian Object Format

Floating Point Support

Code	Description
F	Single Hardware, Double Software Floating Point
G	All Single Hardware Floating Point
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem. See Release Notes
other	other - Enter text string

2.5. MC68K Target Configuration Options

MC68K target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

CPU32	CPU32+	MC68000
MC68008	MC68010	MC68020
MC68040	MC68060	MC6830X
MC68322	MC68349	MC68356
MC68360		

Object Module Formats

Code	Description
F	MC68k ELF Object Format
E	MC68k COFF Object Format

Floating Point Support

Code	Description
N	No Floating Point
S	Software Floating Point
H	Hardware Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem. See Release Notes
other	other - Enter text string

2.6. MCS Target Configuration Options

MCS target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

MCS

Object Module Formats

Code	Description
E	MCS ELF big endian
L	MCS ELF little endian

Floating Point Support

Code	Description
N	No Floating Point

Execution Environments

Code	Description
simple	simple - Only character I/O
other	other - Enter text string when requested

2.7. MIPS Target Configuration Options

MIPS target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

BCMSB1	MIPS1	MIPS16
MIPS2	MIPS3	MIPS32
MIPS32R2	MIPS4	MIPS64
MIPS64GLRN	R3000	R3041
R3051	R3052	R3071
R3081	R3900	R4000
R4100	R4200	R4300
R4600	R4640	R4650
R4700	R5000	R5432
R5464	RM9K	

Object Module Formats

Code	Description
E	MIPS ELF Object Format
F	MIPS No Small-Data ELF Object Format
L	MIPS ELF little-endian Object Format
M	MIPS No Small-Data ELF little-endian Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem. See Release Notes
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5
vxworks66	vxworks66 - Interface with VxWorks 6.6

Code	Description
rtp	rtp - Real Time Process for VxWorks 6.x
other	other - Enter text string
vxworks55	vxworks55 - Interface with VxWorks 5.5

2.8. MIPS-IV Target Configuration Options

MIPS-IV target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

BCMSB1	MIPS64	MIPS64GLRN
R5432	R5464	RM9K

Object Module Formats

Code	Description
S	MIPS ELF Object Format, N32 ABI
T	MIPS No Small-Data ELF Object Format, N32 ABI
U	MIPS ELF little-endian Object Format, N32 ABI
V	MIPS No Small-Data ELF little-endian Object Format, N32 ABI
W	MIPS ELF Object Format
X	MIPS No Small-Data ELF Object Format
Y	MIPS ELF little-endian Object Format
Z	MIPS No Small-Data ELF little-endian Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
other	other - Enter text string
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
psos	psos - Interface with pSOSystem.
vxworks55	vxworks55 - Interface with VxWorks 5.5
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5

2.9. PAsmi Target Configuration Options

PAsmi target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

PA6T

Object Module Formats

Code	Description
E	PowerPC ELF EABI Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
S	Software Floating Point
V	Vector Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem. See Release Notes
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5

Code	Description
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
other	other - Enter text string

2.10. Pentium Target Configuration Options

Pentium target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

X86

Object Module Formats

Code	Description
L	X86 Elf little-endian Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
vxworks60	vxworks60 - Interface with VxWorks 6.0

Code	Description
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
other	other - Enter text string

2.11. PowerPC Target Configuration Options

PowerPC target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

 Note: For E200Z*N series, N means non-VLE mode.

PPC	PPC603	PPC604
PPC401	PPC403	PPC405
PPC440	PPC440GX	PPC440X5
PPC460S	PPC464	PPC464FP
PPC470S	PPC476	PPC476FP
PPC505	PPC509	PPC534
PPC535	PPC553	PPC555
PPC561	PPC563	PPC565
PPC566	PPC740	PPC745
PPC750	PPC750CL	PPC750CX

PPC750CXe	PC750CXr	PPC750FL
PPC750FX	PPC750GL	PPC750GX
PPC755	PPC801	PPC821
PPC823	PPC823E	PPC850
PPC852	PPC852T	PPC853
PPC853T	PPC855	PPC855T
PPC857	PPC857T	PPC857DSL
PPC859	PPC859DSL	PPC859T
PPC860	PPC862	PPC866
PPC870	PPC875	PPC880
PPC885	PPC970	PPC970FX
PPC970MP	PPC5121	PPC5121e
PPC5123	PPC5125	PPC7400
PPC7410	PPC7440	PPC7441
PPC7445	PPC7447	PPC7447A
PPC7448	PPC7450	PC7451
PPC7455	PPC7457	PPC8240
PPC8241	PPC8245	PPC8250
PPC8255	PPC8260	PPC8264
PPC8265	PPC8266	PPC8270
PPC8275	PPC8280	PPC8306
PPC8306S	PPC8308	PPC8309
PPC8313	PPC8313	PPC8313E
PPC8314	PPC8314E	PPC8315
PPC8315E	PPC8321	PPC8321E

PPC8323	PPC8323E	PPC440GX
PPC8343	PPC8343E	PPC8347
MPC8347E	PPC8349	PC8349E
PC8358	PPC8358E	PPC8360
PPC8360E	PPC8365	PPC8377
PPC8377EWLAN	PPC8378	PPC8378E
PPC8379	PC8379E	PCE500MC
PPCE500	PPCE500V2	PPC5500
PPC5510	PC5516Z1	PPC5532
PPC5533	PPC5534	PPC5553
PPC5554	PPC5561	PPC5565
PPC5566	PPC5567	PPC5632M
PPC5633M	PPC5634M	PPC5643
PPC5644	PPC5644B	PPC5645B
PPC5646B	PPC5645S	PPC5668G
PPC5674F	PPC8247	PPC8248
PPC8271	PPC8272	PPC8533
MPC8533E	PPC8535	MPC8535E
PPC8536	PPC8536E	PPC8540
PPC8541	MPC8541E	PPC8543
MPC8543E	PPC8544	MPC8544E
PPC8545	MPC8545E	PPC8547
MPC8547E	PPC8548	MPC8548E
PPC8555	PPC8555E	PPC8560
PPC8567	PPC8568	MPC8568E

PPC8569	MPC8569E	PPC8572
MPC8572E	PPC8610	PPC8640
PPC8641	PPC8641D	MGT5200
PPC5200B	SPC563M54	SPC563M60
SPC563M64	P1010	P1014
1020	P1011	P1021
P1012	P1022	P1013
P1023	P1017	P2020
P2040	P3041	P4040
P4080	P5010	P5020
PPCE200Z1N	PPCE200Z3N	PPCE200Z335N
PPCE200Z4N	PPCE200Z4DN	PPCE200Z6N
PPCE200Z7N	PPCE300	PPCE6500

Object Module Formats

Code	Description
E	PowerPC ELF EABI Object Format
D	PowerPC COFF Object Format
F	PowerPC No Small-Data ELF EABI Object Format
L	PowerPC ELF EABI little-endian Object Format
M	PowerPC No Small-Data ELF EABI little-endian Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point

Code	Description
S	Software Floating Point
F	Single Hardware, Double Software Floating Point
G	All Single Hardware Floating Point
V	Vector Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem. See Release Notes
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
rtp7	rtp7 - Real Time Process for VxWorks 7
other	other - Enter text string
vxworks55	vxworks55 - Interface with VxWorks 5.5
vxworks7	vxworks7 - Interface with VxWorks 7

2.12. PowerPC Compressed Target Configuration Options

PowerPC Compressed target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

PPC533	PPC536	PPC555
PPC560	PPC562	PPC564
PPC566		

Object Module Formats

Code	Description
C	PowerPC ELF EABI Compressed Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
other	other - Enter text string

2.13. PowerPC VLE Target Configuration Options

PowerPC VLE target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

 Note: For E200Z*V series, V means VLE mode.

PPC5510	PPC5516Z0	PPC5516Z1
PPC5533	PPC5534	PPC5561
PPC5565	PPC5566	PC5567
PC5601P	PPC5602B	PPC5602C
PPC5602P	PPC5602PE	PPC5602S
PPC5603B	PPC5603C	PPC5603P
PPC5603PE	PPC5604B	PPC5604C
PPC5604P	PPC5604PE	PPC5604S
PPC5606S	PPC5645S	PXD10
PPC5632M	PPC5633M	PPC5634M
PPC5643	PPC5644	PXD20
PXS20	PPC5668G	PXN20
PXN21	PPC5640A	PPC5641A
PPC5642A	PPC5643A	PPC5644A
PPC5645A	PPC5646A	PPC5647A
PPC5648A	PPC5649A	PPC564xA
PPC5644B	PPC5645B	PPC5646B
PPC5644C	PPC5645C	PPC5646C
PPC5640L	PPC5641L	PPC5642L
PPC5643L	PPC5644L	PPC5645L
PPC5646L	PPC5647L	PPC5648L
PPC5649L	PPC564xL	PPC5640F

PPC5641F	PPC5642F	PPC5643F
PPC5644F	PPC5645F	PPC5646F
PPC5647F	PPC5648F	PPC5649F
PPC564xF	PPC5673F	PPC5674F
PPC5673K	PPC5674K	PPC5675K
PPC5676R	PXR40	PXS30
SPC560B40	SPC560C40	SPC560B44
SPC560C44	SPC560B50	SPC560C50
SPC560P44	SPC560P50	SPC560S50
SPC560S60	SPC563M54	SPC563M60
SPC563M64	PPC5726L	PPC5742P
PPC5743P	PPC5744P (1)	PPC5744P (2)
PPC5744K (1)	PPC5744K (2)	PPC5746G
PPC5746M (1)	PPC5746M (2)	PPC5746R
PPC5747C	PPC5747G	PPC5748C
PPC5748G	PPC5775K	PPC5777C
PPC5777M (1)	PPC5777M (2)	SPC572L64
SPC574K72	SPC57EM80	SPC58EG84
SPC58NE84	SPC58NN84	
PPCVLE	PPCE200Z0V	PPCE200Z0HV
PPCE200Z1V	PPCE200Z3V	PPCE200Z335V
PPCE200Z4V	PPCE200Z4DV	PPCE200Z6V
PPCE200Z7V	PPCE200Z0HN2PV	PPCE200Z210N3V
PPCE200Z215N3V	PPCE200Z215AN3V137	PPCE200Z225N3V
PPCE200Z225BN3V	PPCE200Z410N3V	PPCE200Z420N3V

PPCE200Z4201N3V	PPCE200Z4204N3V	PPCE200Z425N3V
PPCE200Z425BN3V	PPCE200Z4251N3V	PPCE200Z4256N3V
PPCE200Z710N3V	PPCE200Z720N3V	PPCE200Z7260N3V
PPCE200Z759N3V	PPCE200Z760N3V	

Object Module Formats

Code	Description
E	PowerPC ELF EABI Object Format
F	PowerPC No Small-Data ELF EABI Object Format

Floating Point Support

Code	Description
N	No Floating Point
S	Software Floating Point
F	Single Hardware, Double Software Floating Point
G	All Single Hardware Floating Point
H	Hardware Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
other	other - Enter text string when requested

2.14. PPC e6500 Target Configuration Options (32-bit)

e6500 target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

PPCE6500

Object Module Formats

Code	Description
E	PowerPC ELF EABI Object Format
F	PowerPC No Small-Data ELF EABI Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
S	Software Floating Point
V	Vector Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5

Code	Description
vxworks66	vxworks66 - Interface with VxWorks 6.6
vxworks7	vxworks7 - Interface with VxWorks 7
rtp	rtp - Real Time Process for VxWorks 6.x
rtp7	rtp7 - Real Time Process for VxWorks 7
other	other - Enter text string

2.15. PPC 64-bit Target Configuration Options

PPC 64-bit target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

PPCLP64	PPCE6500
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Object Module Formats

Code	Description
F	PowerPC No Small-Data ELF EABI Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
V	Vector Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5
vxworks66	vxworks66 - Interface with VxWorks 6.6
vxworks7	vxworks7 - Interface with VxWorks 7
rtp	rtp - Real Time Process for VxWorks 6.x
rtp7	rtp7 - Real Time Process for VxWorks 7
other	other - Enter text string

2.16. RH850 Target Configuration Options

Renesas target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

RH850	V850	V850E
V850E2	V850E3	RH850G4MH
RH850G3K	RH850G3KH	RH850G3M
RH850G3MH		

Object Module Formats

Code	Description
E	ELF little endian, 16-bit SDA
F	ELF little endian, 32-bit far addressing
G	ELF little endian, 23-bit SDA
L	ELF little endian, 23-bit absolute addressing

Floating Point Support

Code	Description
F	Single Hardware, Double Software Floating Point
H	Hardware Floating Point inclusively Vector Mode
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
other	other - Enter text string

2.17. SH Target Configuration Options

SH target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

SH	SH1	SH2
SH2A	SH3	SH4
SH4A		

Object Module Formats

Code	Description
E	SH ELF ABI Object Format
L	SH ELF little-endian Object Format

Floating Point Support

Code	Description
N	No Floating Point
S	Software Floating Point
H	Hardware Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
psos	psos - Interface with pSOSystem.
vxworks55	vxworks55 - Interface with VxWorks 5.5
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3

Code	Description
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
other	other - Enter text string when requested

2.18. SPARC Target Configuration Options

SPARC target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

SPARC	SPARClite
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Object Module Formats

Code	Description
E	SPARC ELF Object Format
F	SPARC No Small-Data ELF Object Format

Floating Point Support

Code	Description
H	Hardware Floating Point
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
solaris	solaris - Run on Solaris (SPARC only, not SPARClite)
vxworks60	vxworks60 - Interface with VxWorks 6.0
vxworks61	vxworks61 - Interface with VxWorks 6.1
vxworks62	vxworks62 - Interface with VxWorks 6.2
vxworks63	vxworks63 - Interface with VxWorks 6.3
vxworks64	vxworks64 - Interface with VxWorks 6.4
vxworks65	vxworks65 - Interface with VxWorks 6.5
vxworks66	vxworks66 - Interface with VxWorks 6.6
rtp	rtp - Real Time Process for VxWorks 6.x
other	other - Enter text string
vxworks55	vxworks55 - Interface with VxWorks 5.5

2.19. TriCore Target Configuration Options

TriCore target configuration options for target processors, object module formats, floating point support, and execution environments.

Target Processors

TC	TC12	TC13
TC131	TC11IB	TC1100
TC1130	TC1167	TC1197
TC1337	TC1367	TC1387

TC1724	TC1728	TC1736
TC1765	TC1766	TC1767
TC1775	TC1782	TC1784
TC1796	TC1797	TC1798
TC1910	TC1912	TC1920
TC16	TC161	TC16P
TC16E	TC21xAstep16E	TC22xAstep16E
TC23xAstep16E	TC26x16P	TC26x16E
TC27xAstep16P	TC27xAstep16E	TC27xBstep16P
TC27xBstep16E	TC29x16P	TC29x16E
TC2Dx16P	TC2Dx16E	TC162P
TC39xAAstep162P		

Object Module Formats

Code	Description
L	TriCore Z-Data, No Small-Data ELF Object Format
M	TriCore No Small-Data, No Z-Data ELF Object Format
N	TriCore Small-Data, No Z-Data ELF Object Format

Floating Point Support

Code	Description
F	Single Hardware, Double Software Floating Point
G	All Single Hardware Floating Point
N	No Floating Point
S	Software Floating Point

Execution Environments

Code	Description
windiss	windiss - Instruction Set Simulator
tsim	iss - Infineon Instruction Set Simulator
cross	cross - Use Ram Disk for I/O
simple	simple - Only character I/O
other	other - Enter text string when requested