


|   |   |                          |                  |             |
|---|---|--------------------------|------------------|-------------|
| <div><div>灵星雨科技<br/>Linsn Technology</div></div> | 深圳灵星雨科技开发有限公司   |                          |                  |             |
|   | 地址：深圳市南山区深南大道西10168号佳嘉豪商务大厦15-16层                         |                          |                  |             |
|   | <a href="http://www.linsn.com/">http://www.linsn.com/</a> |                          |                  |             |
| POWER   |   |                          |                  |             |
|   | Size<br>A3  | CAGE Code<br><Cage Code> | DWG NO<br><Doc>  | Rev<br>V1.0 |
| Wednesday, March 21, 2018   | Scale   |                          | Sheet<br>3 of 27 |             |



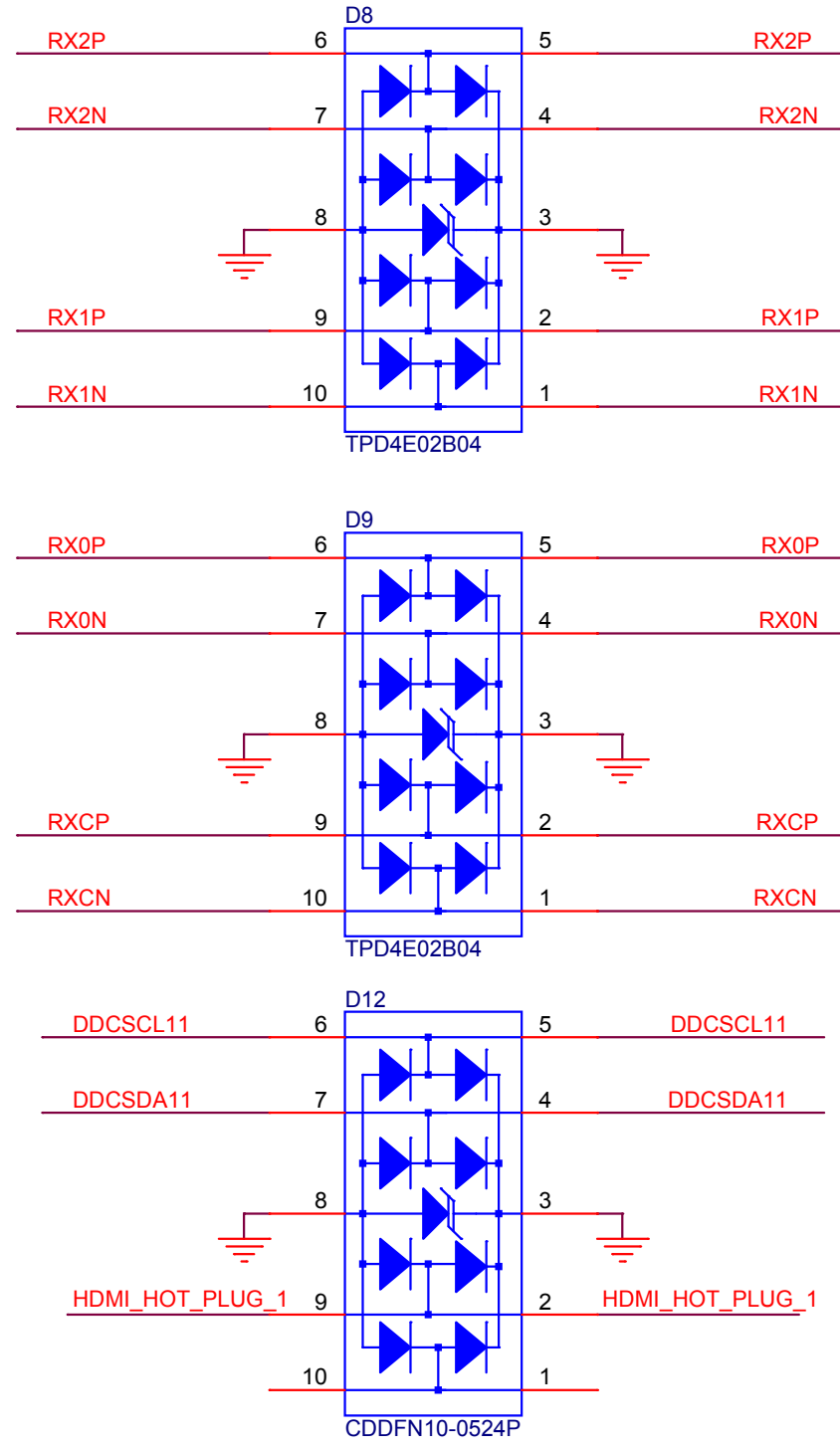
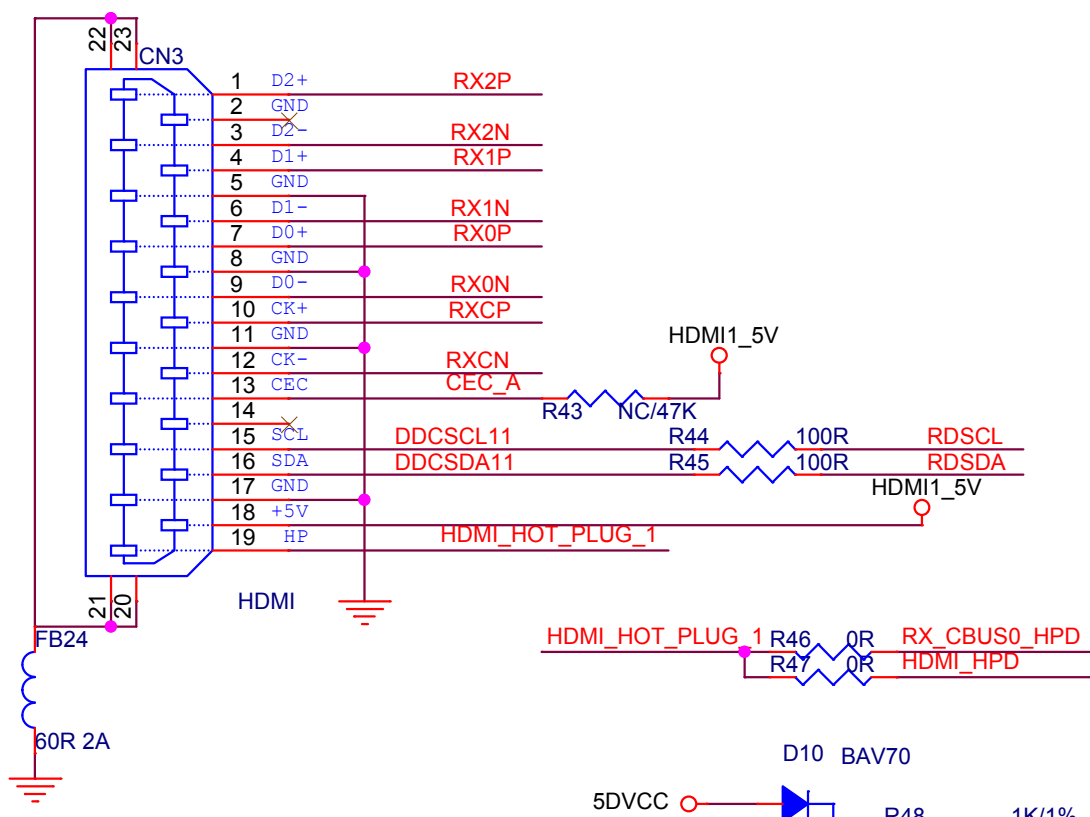
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## INPUT\_DP1.2

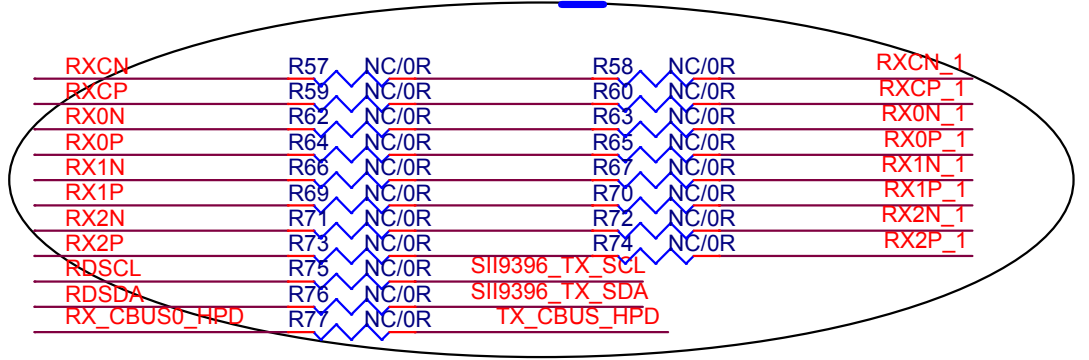
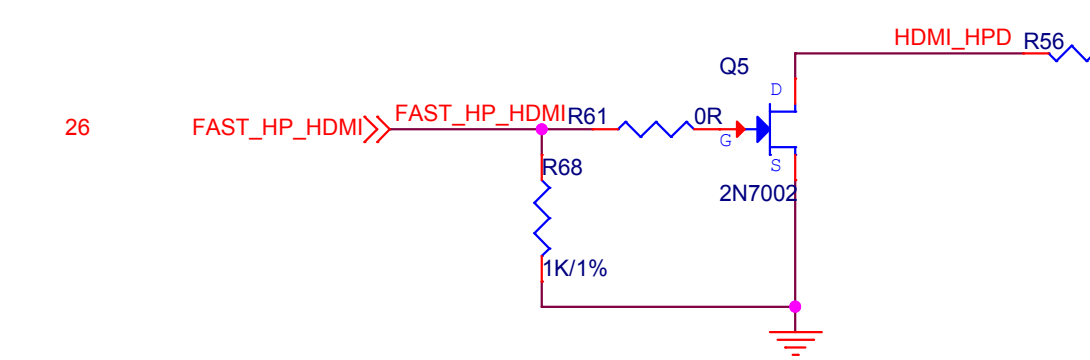
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| Wednesday, March 21, 2018 | Scale          |                          | Sheet<br>4 of 27 |             |



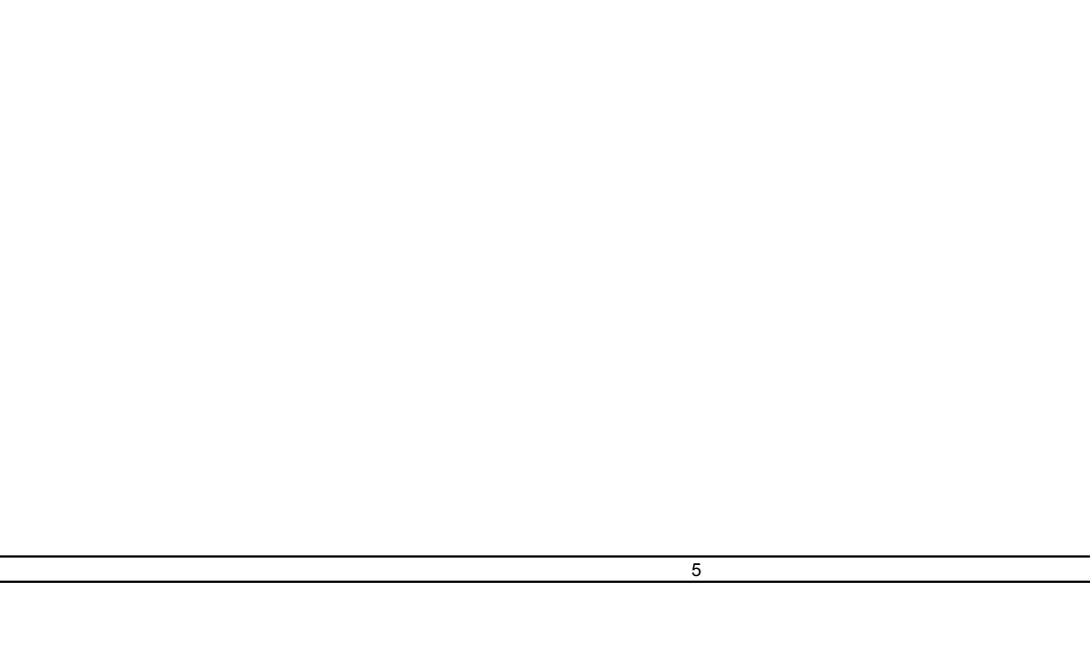
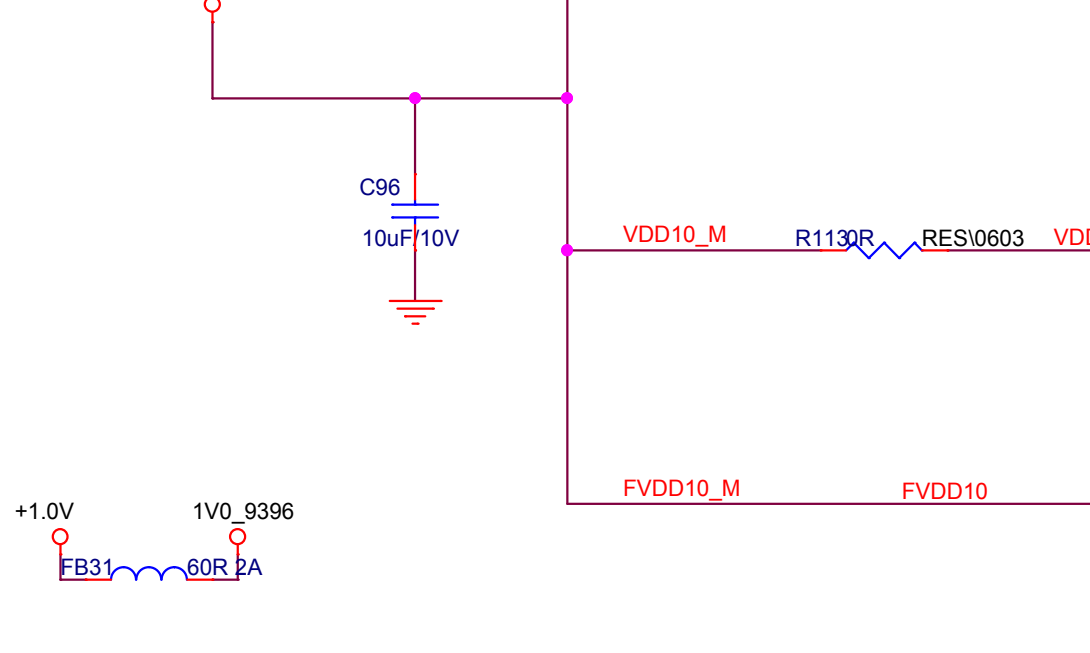
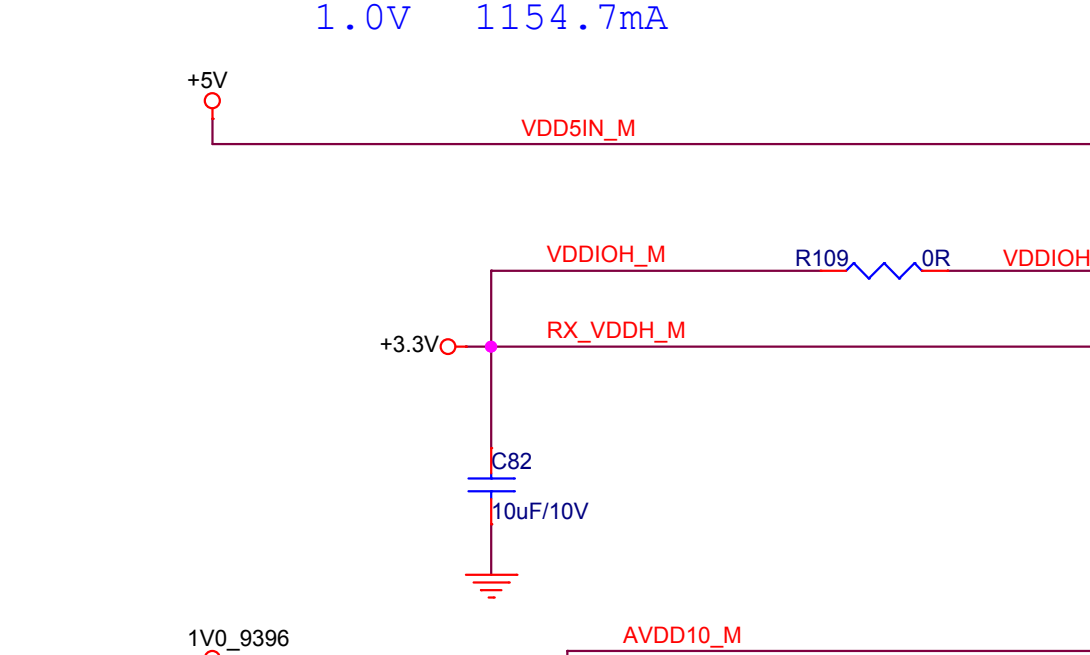
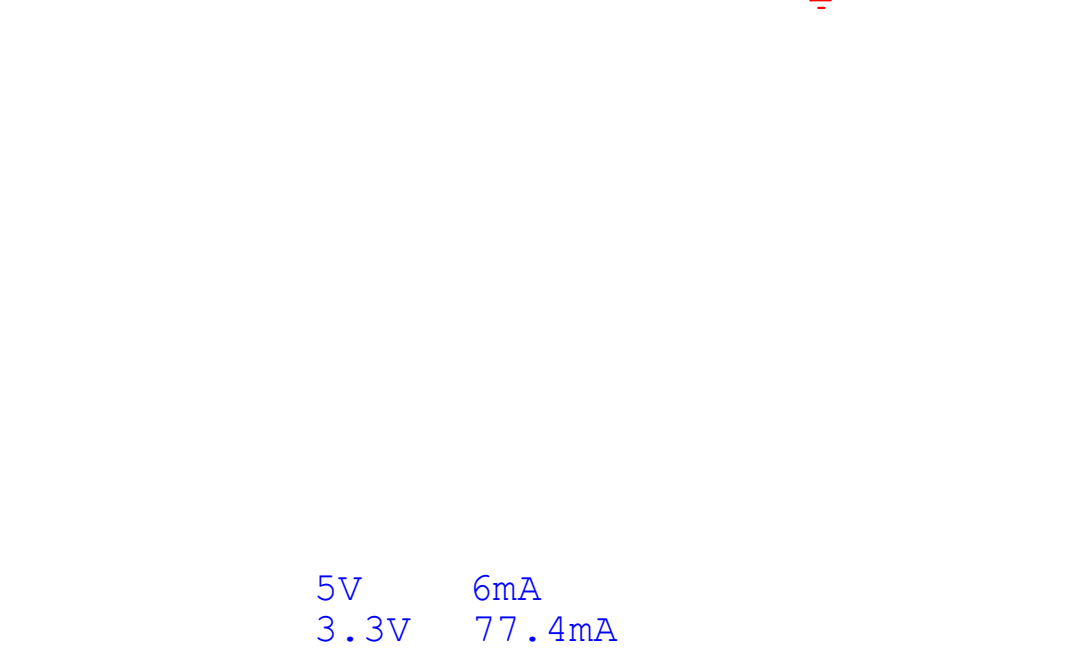
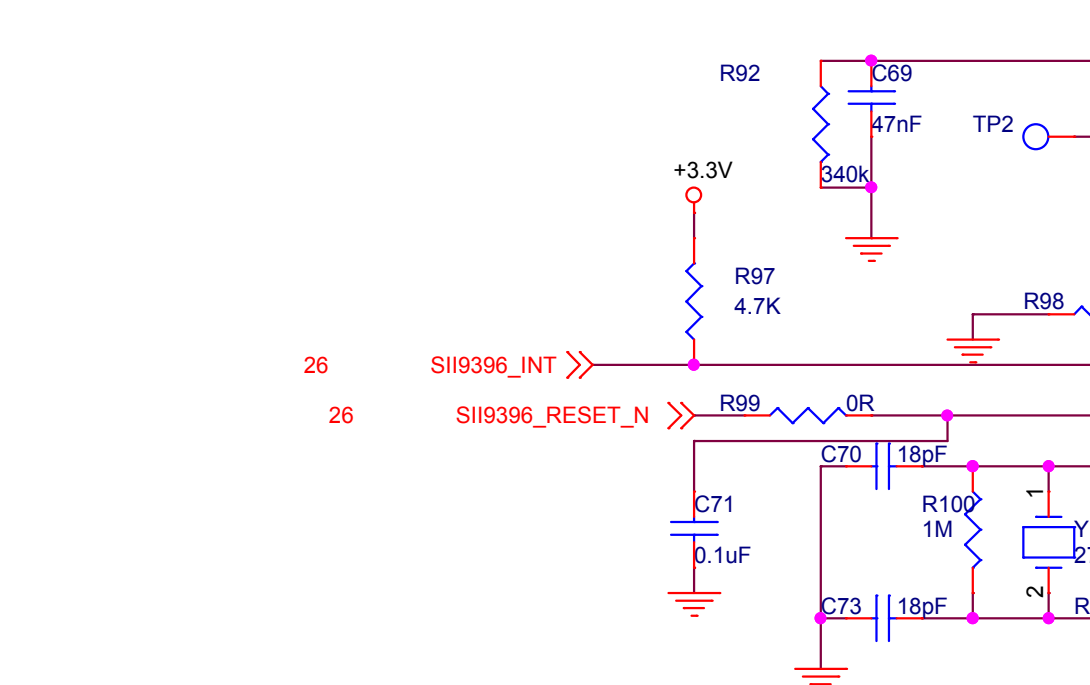
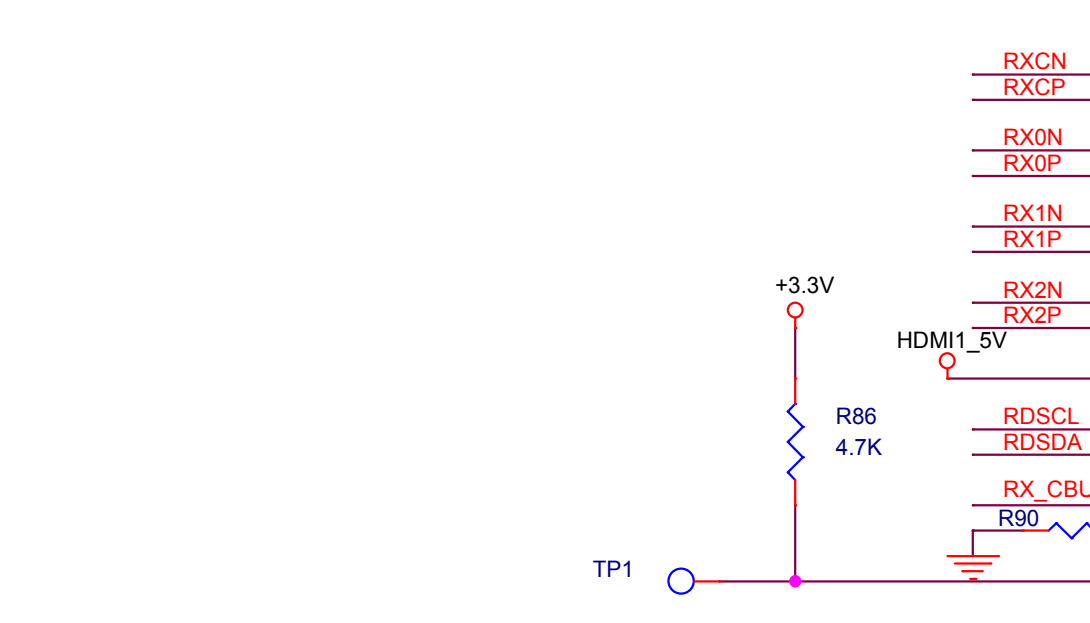
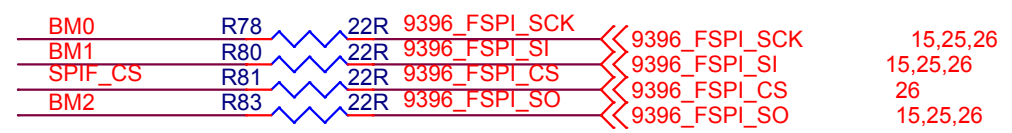
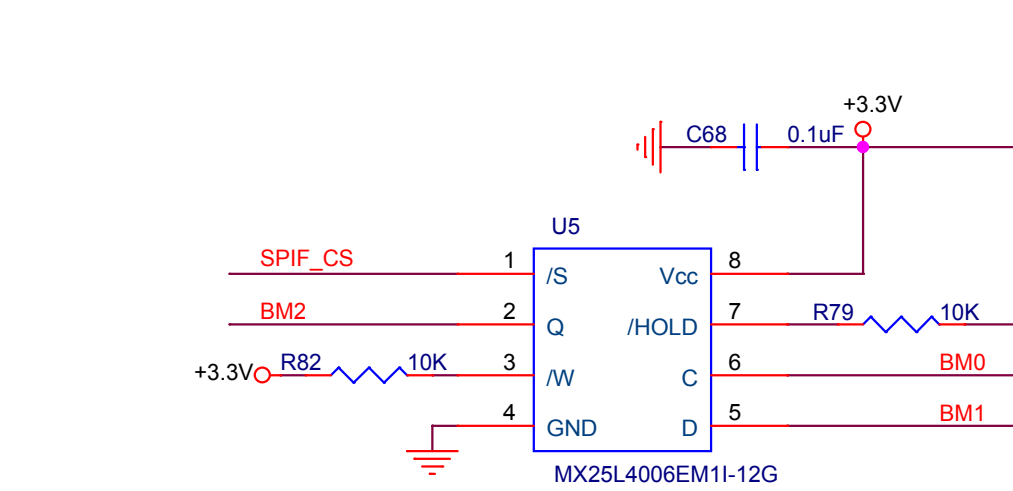
MHL\_DETECT = 0 (HDMI)  
MHL\_DETECT = 1 (MHL)



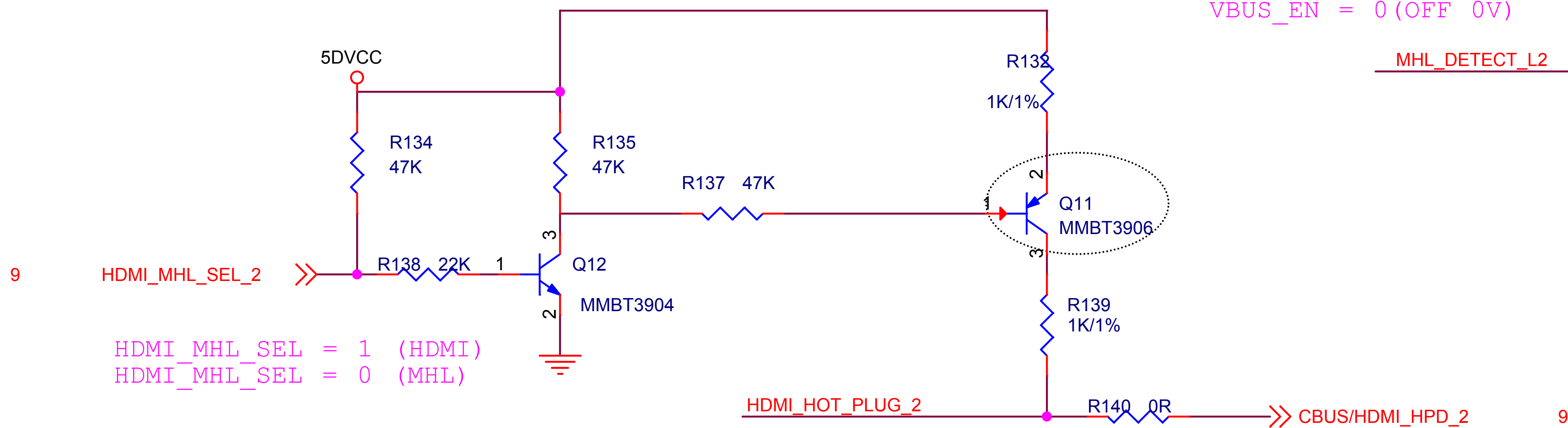
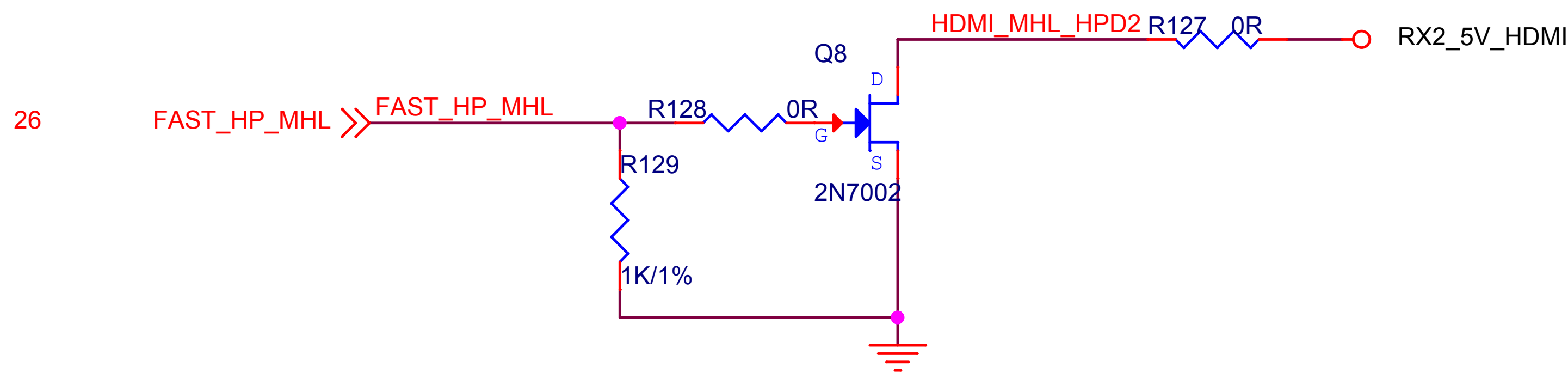
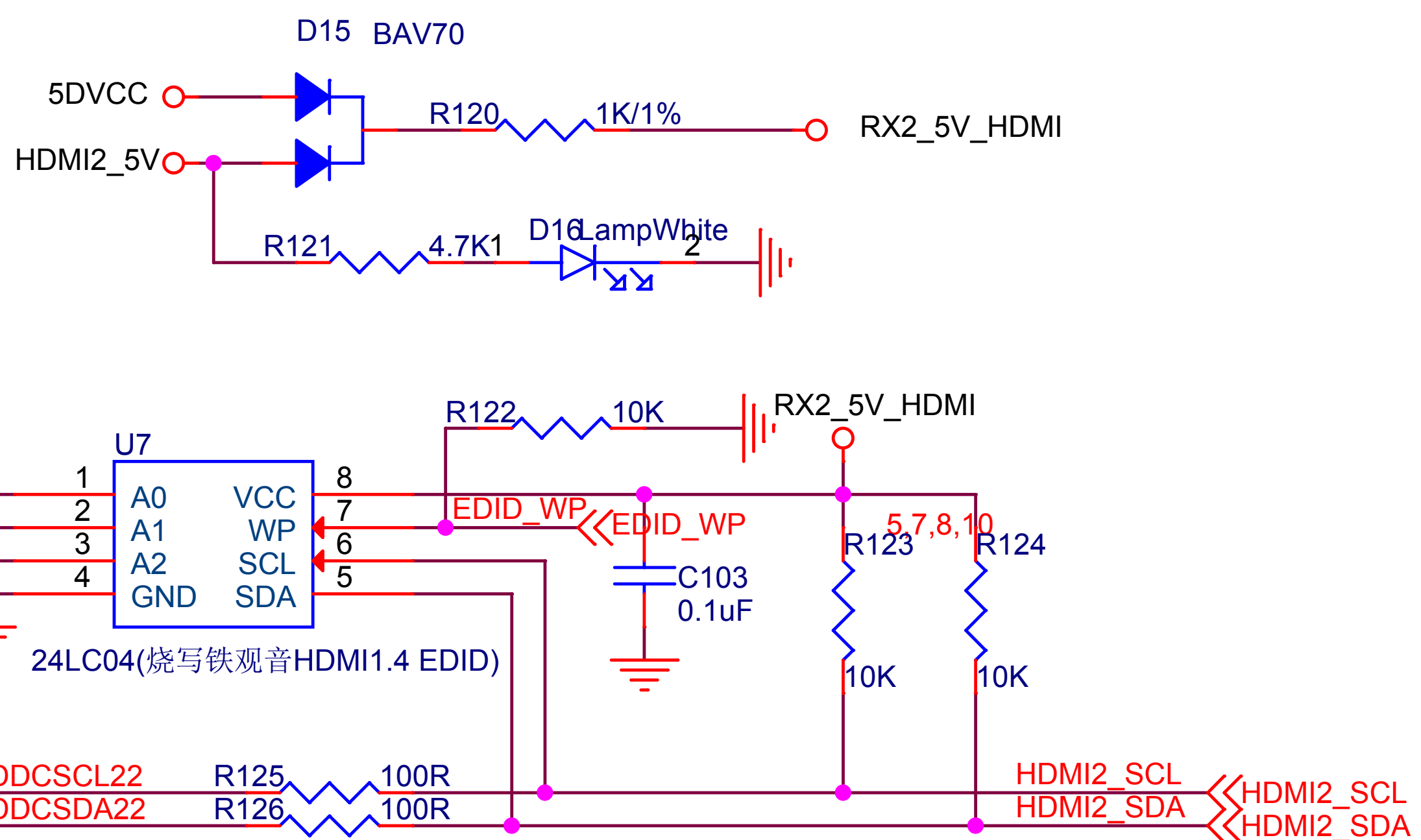
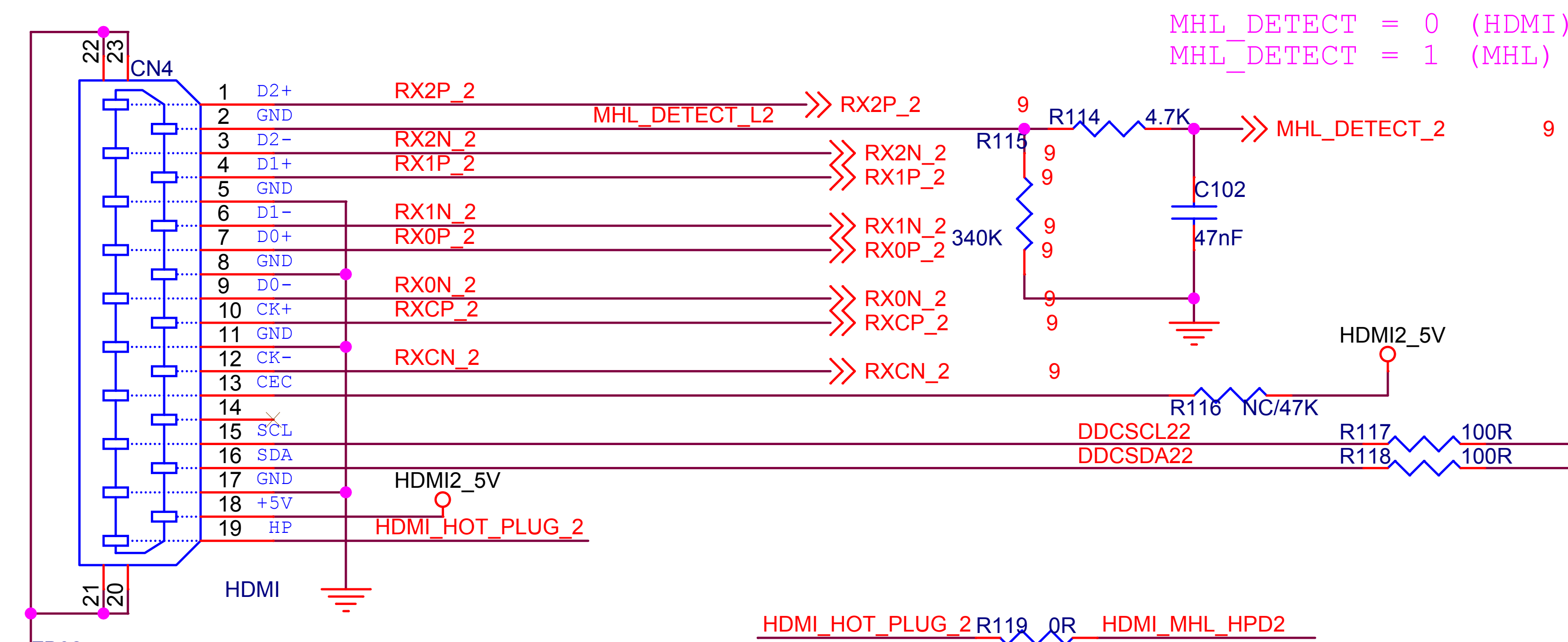
### HDMI MHL switch circuit



图例: 1. 黄色: 数据线, 2. 蓝色: 电源线, 3. 红色: 信号线, 4. 绿色: 地线

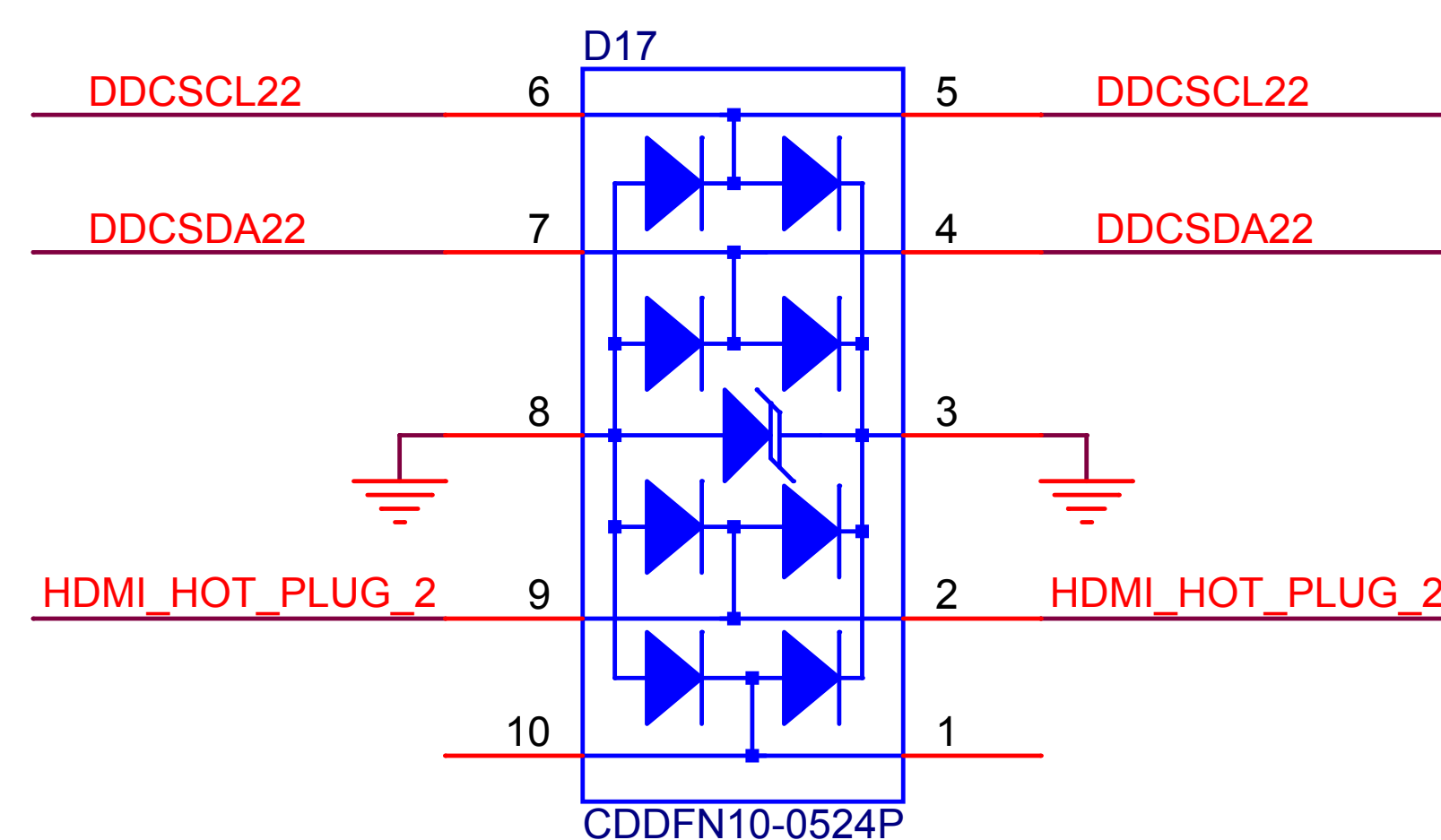
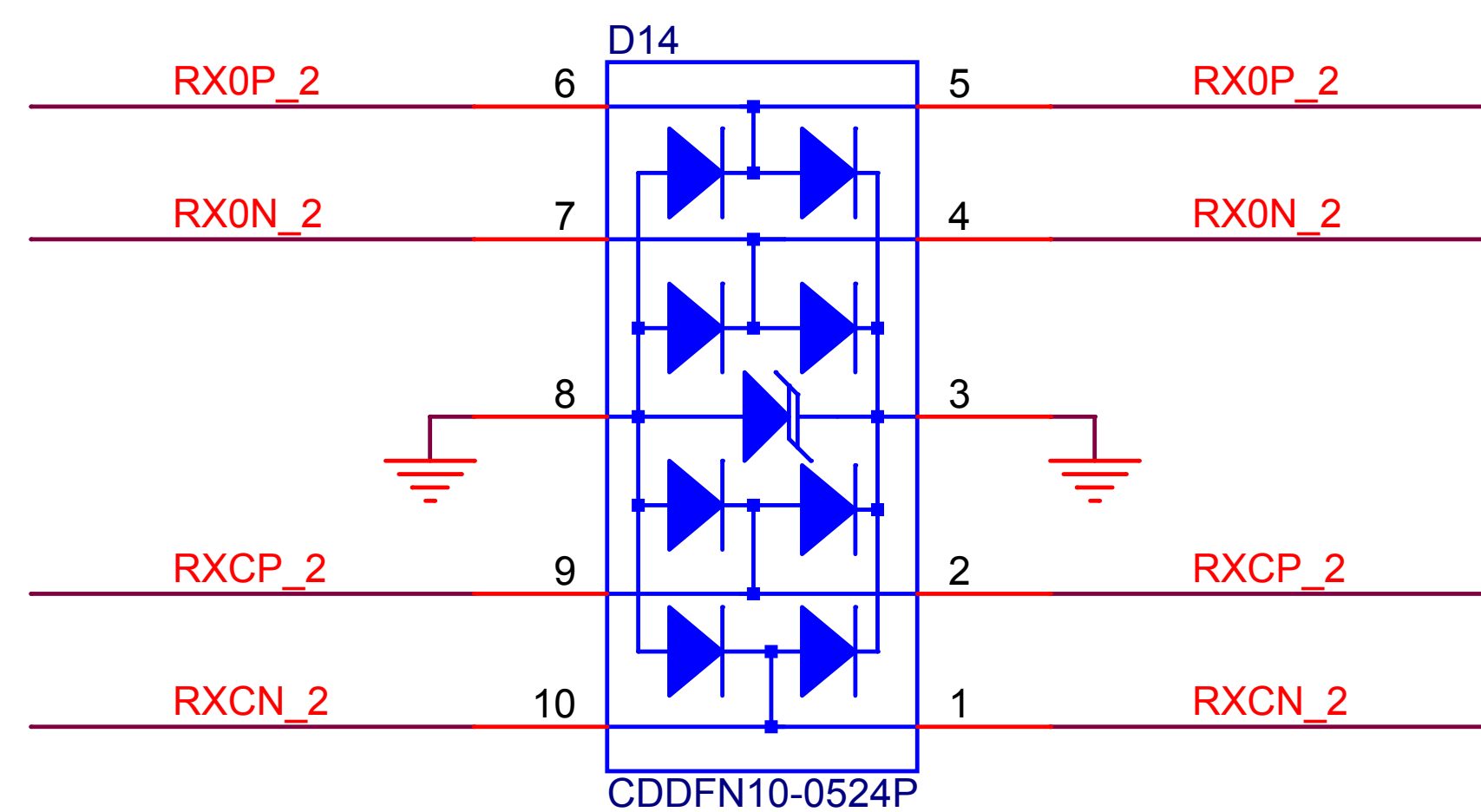
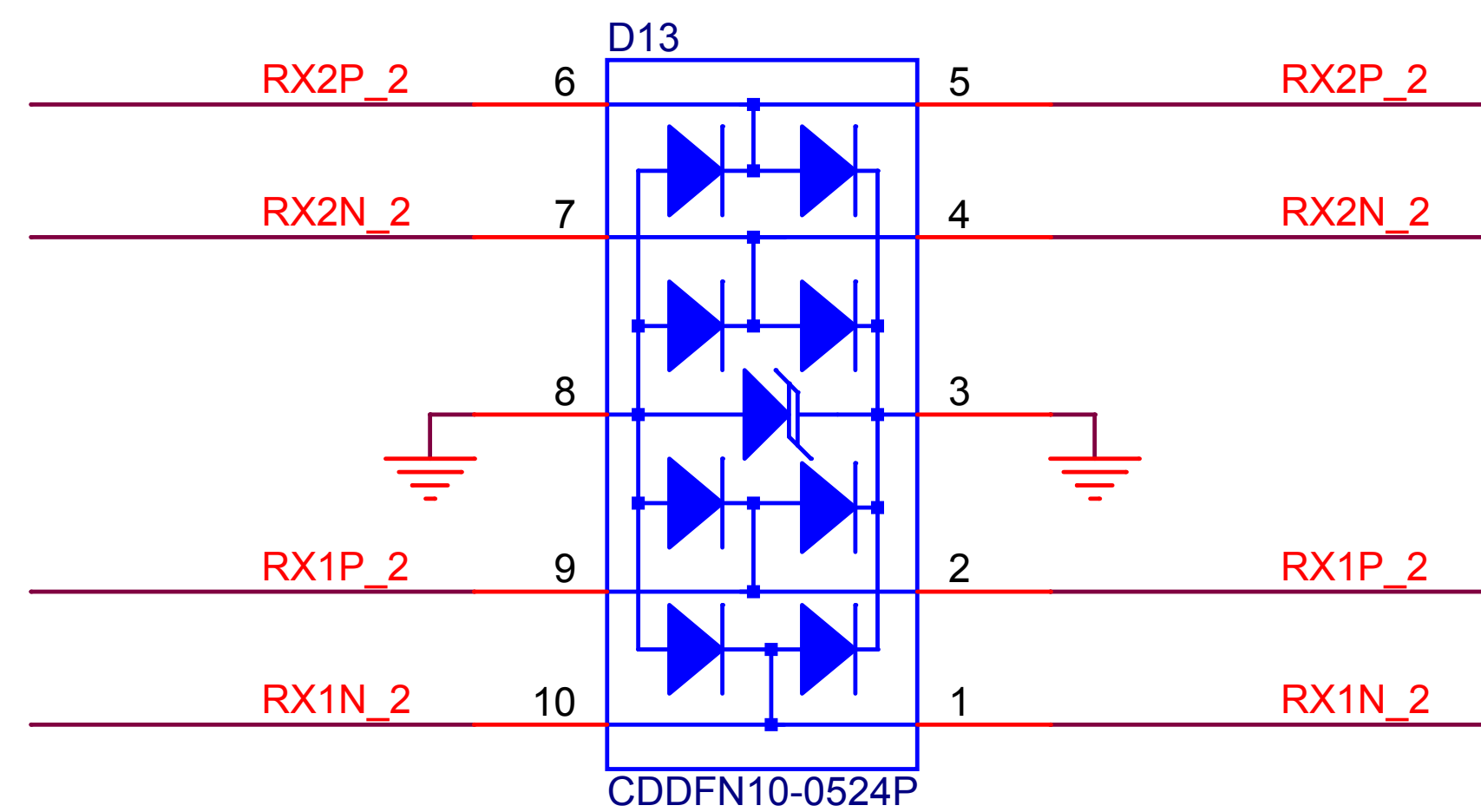
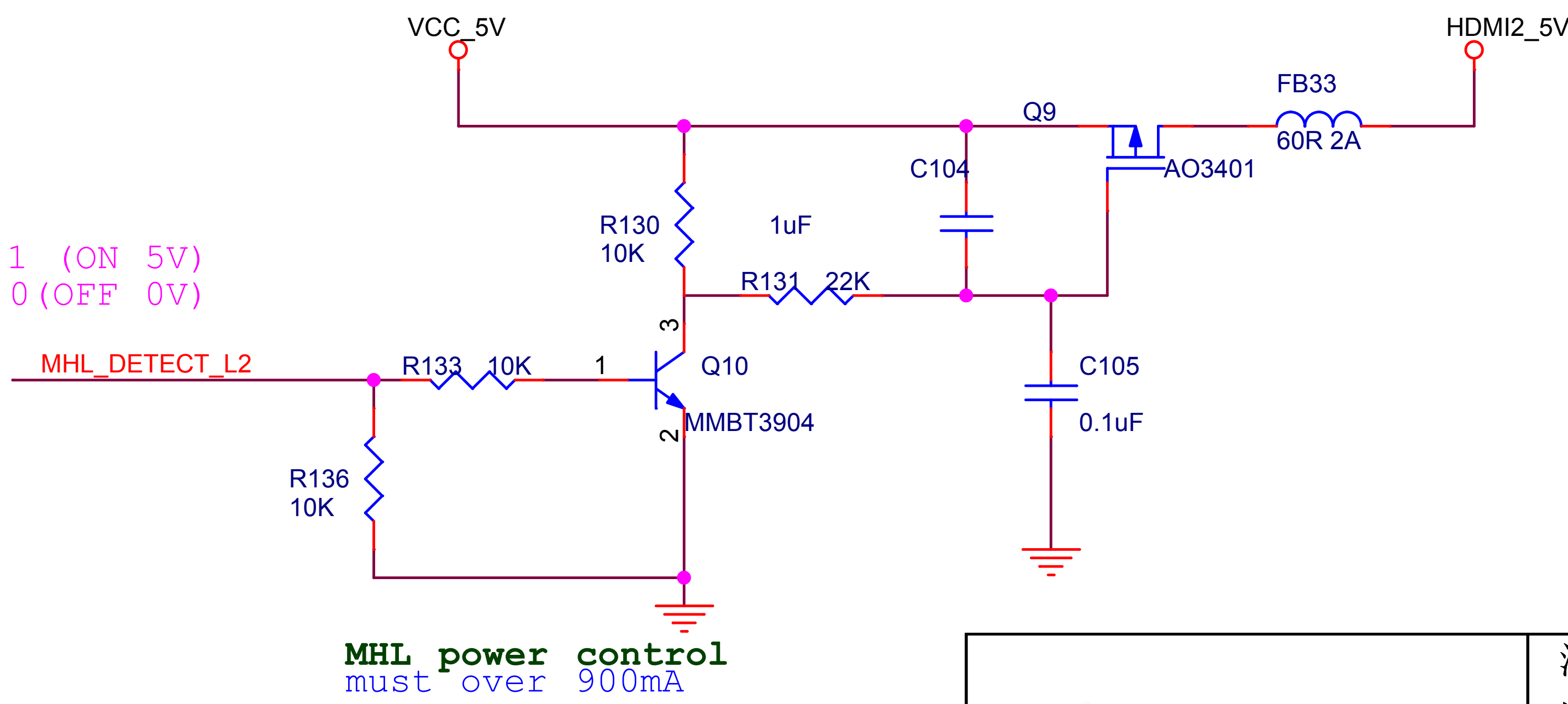


D1



HDMI\_MHL\_SEL = 1 (HDMI) PinShare成HDMI HPD  
HDMI\_MHL\_SEL = 0 (MHL) PinShare成CBUS

## HDMI\_MHL switch circuit



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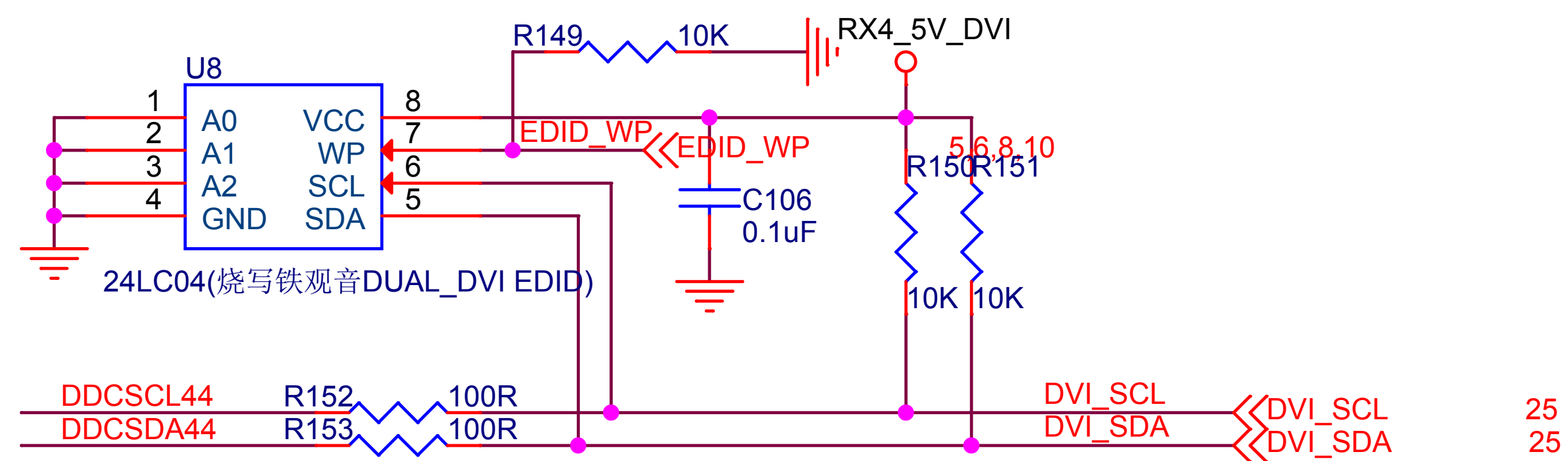
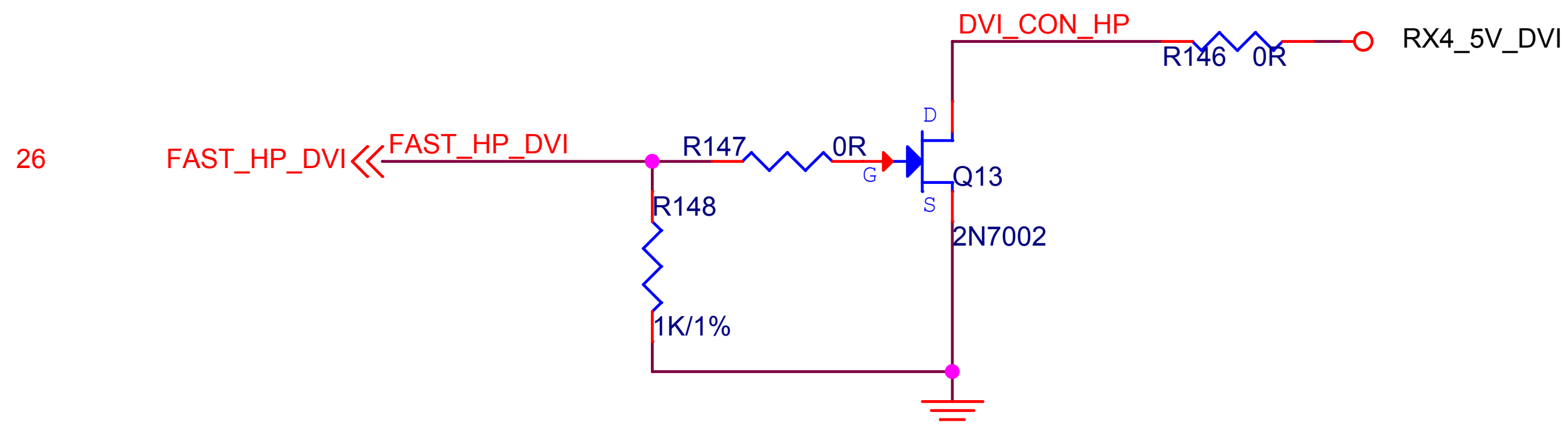
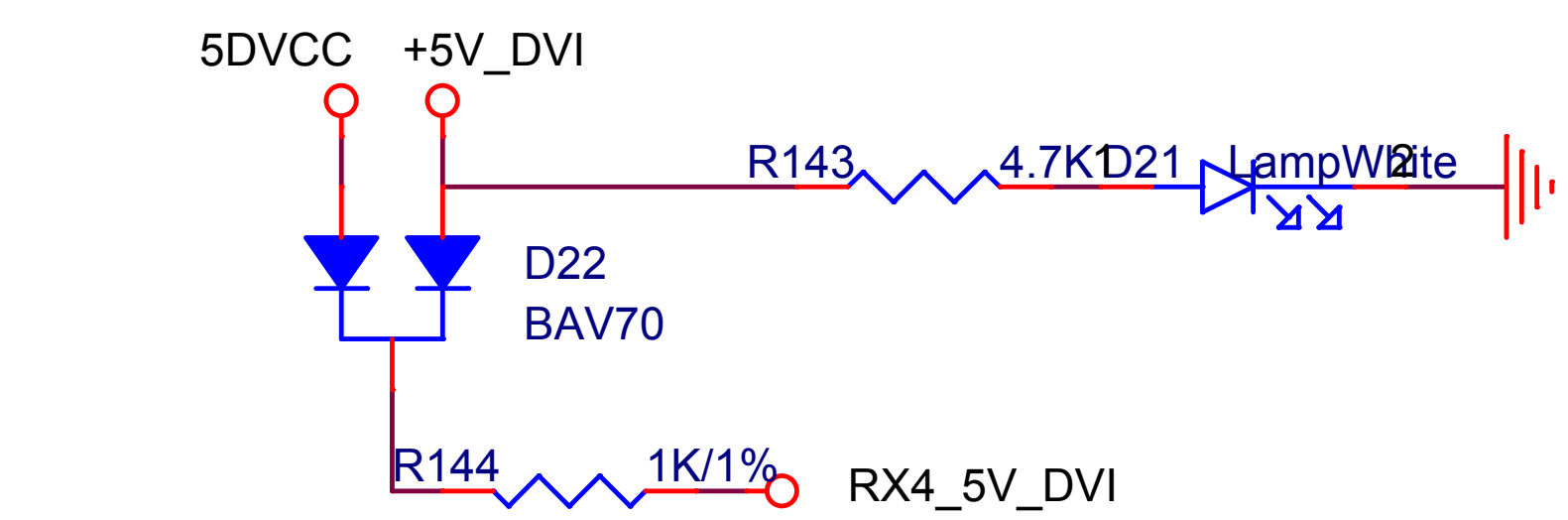
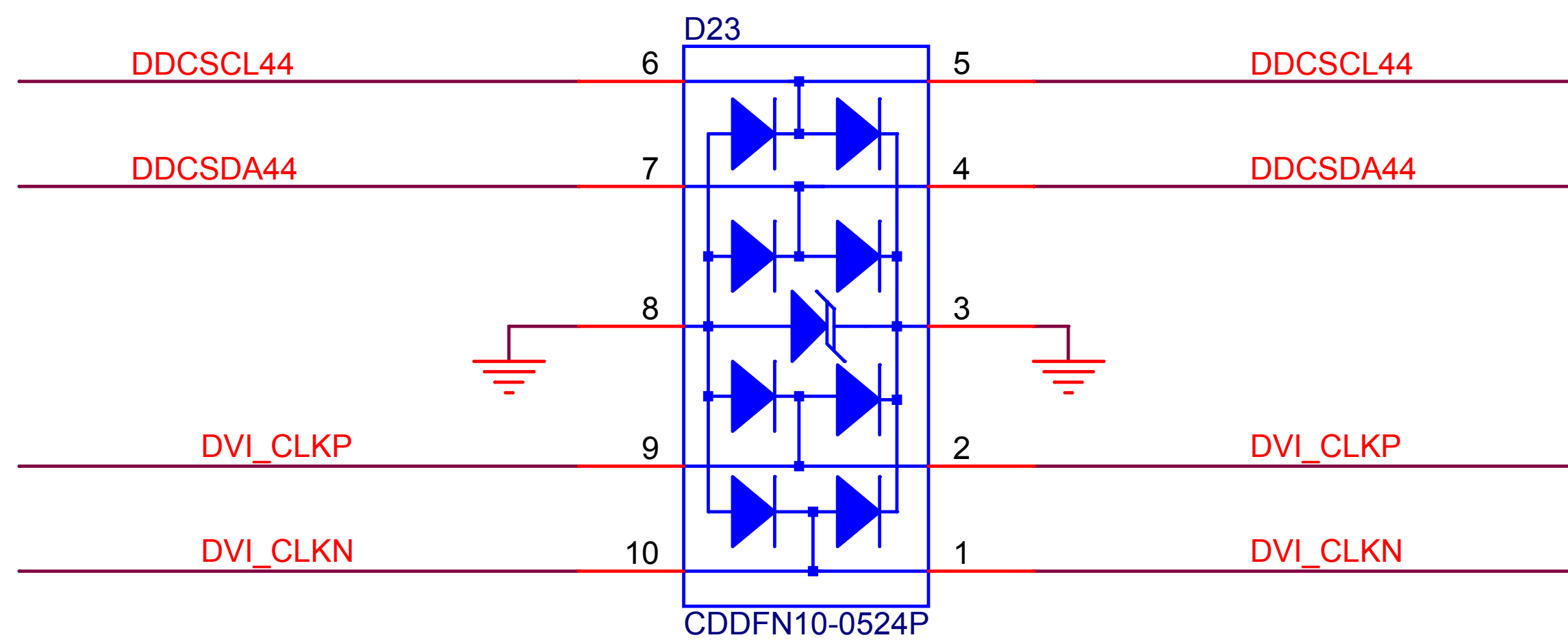
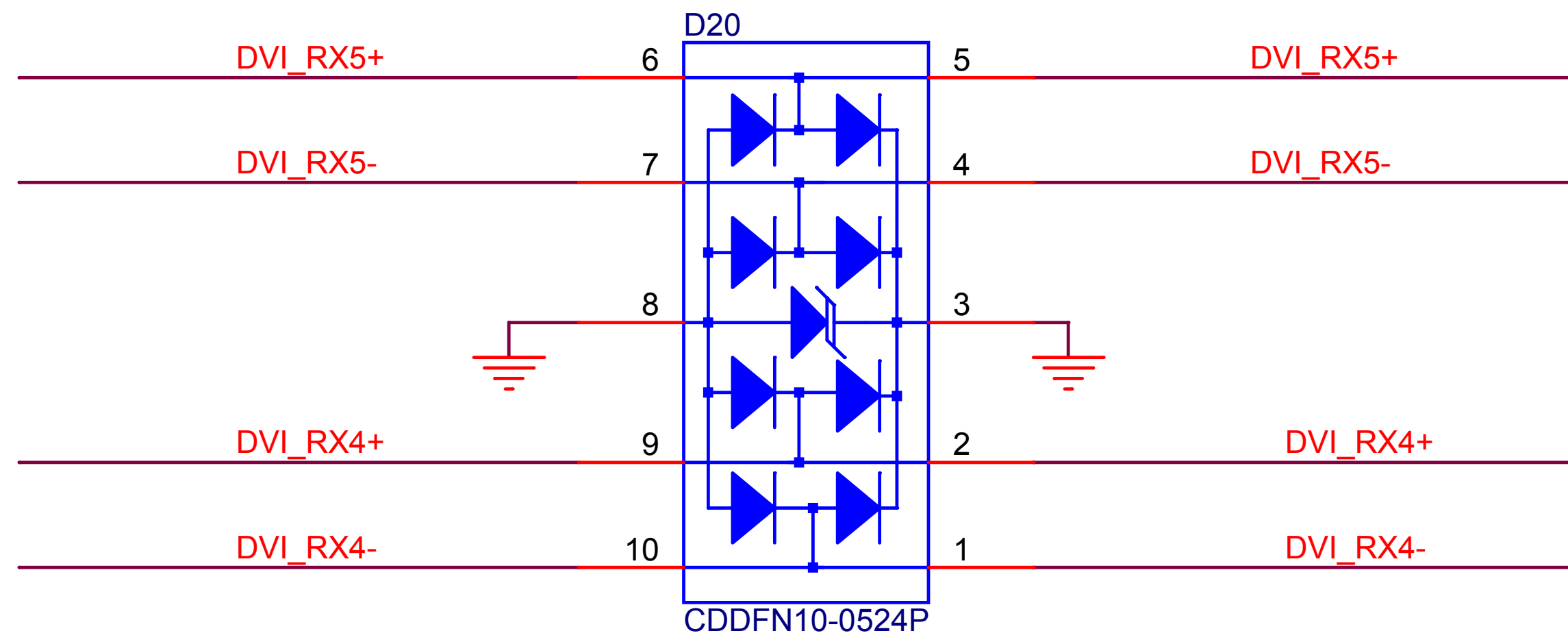
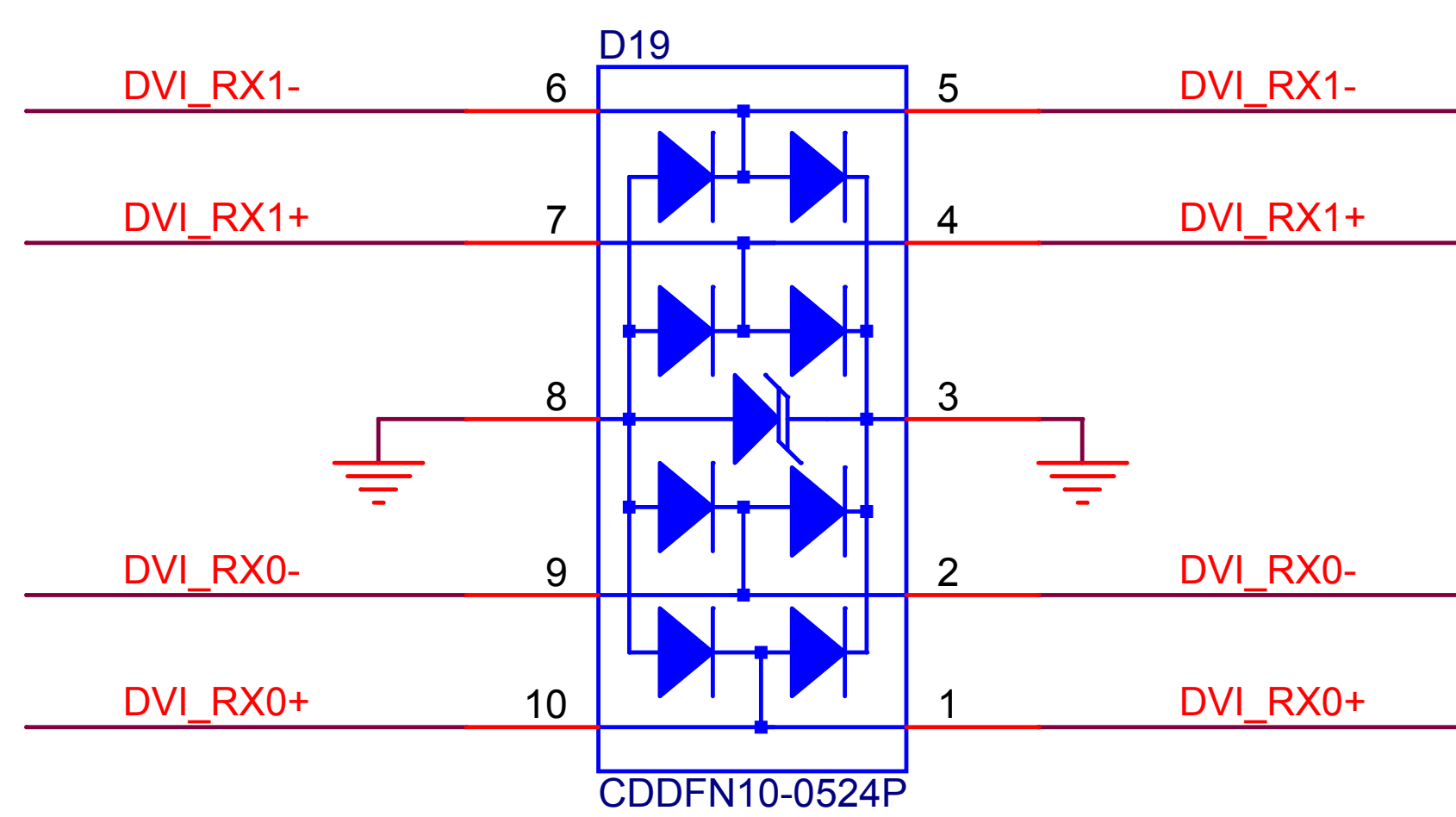
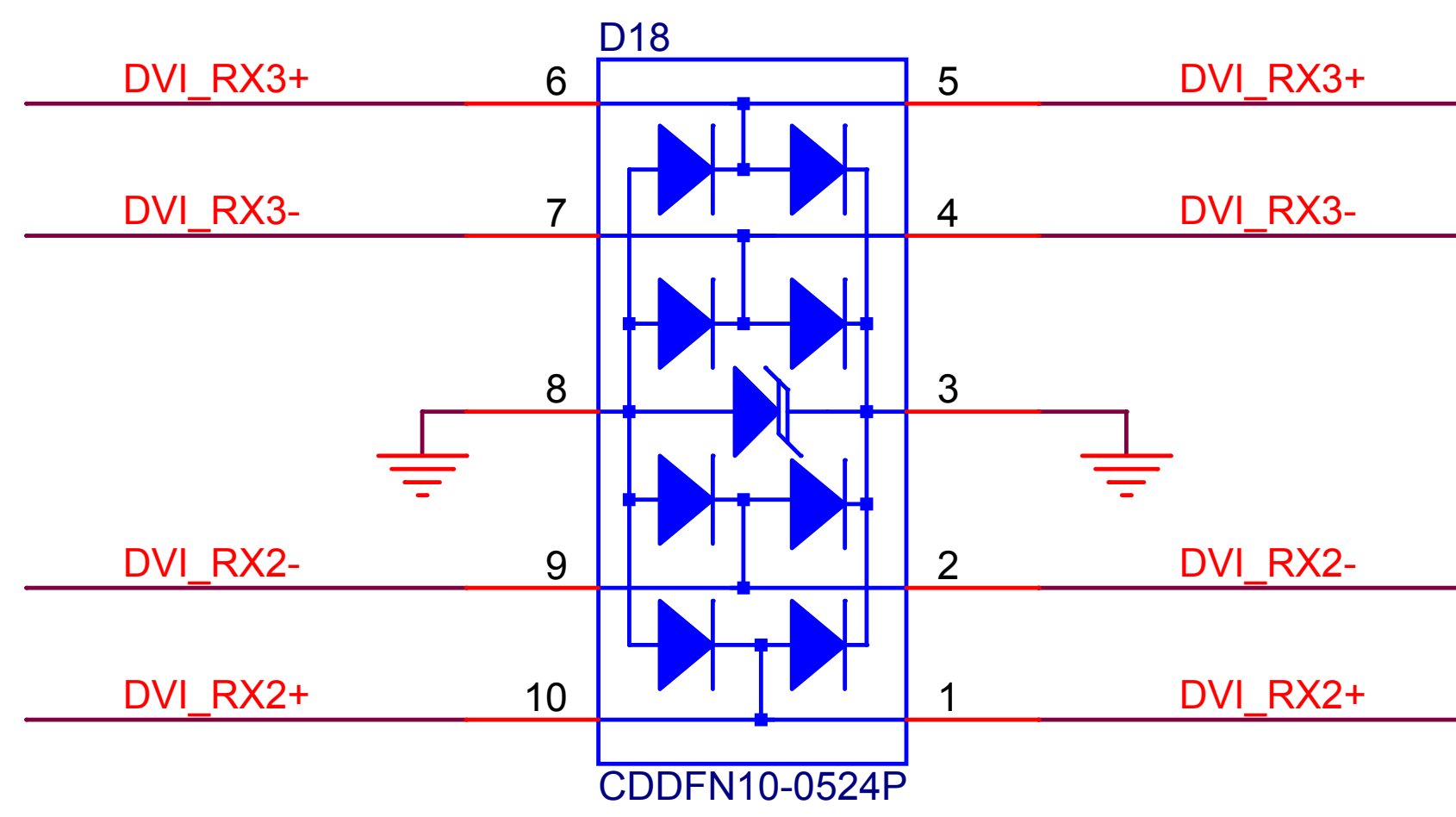
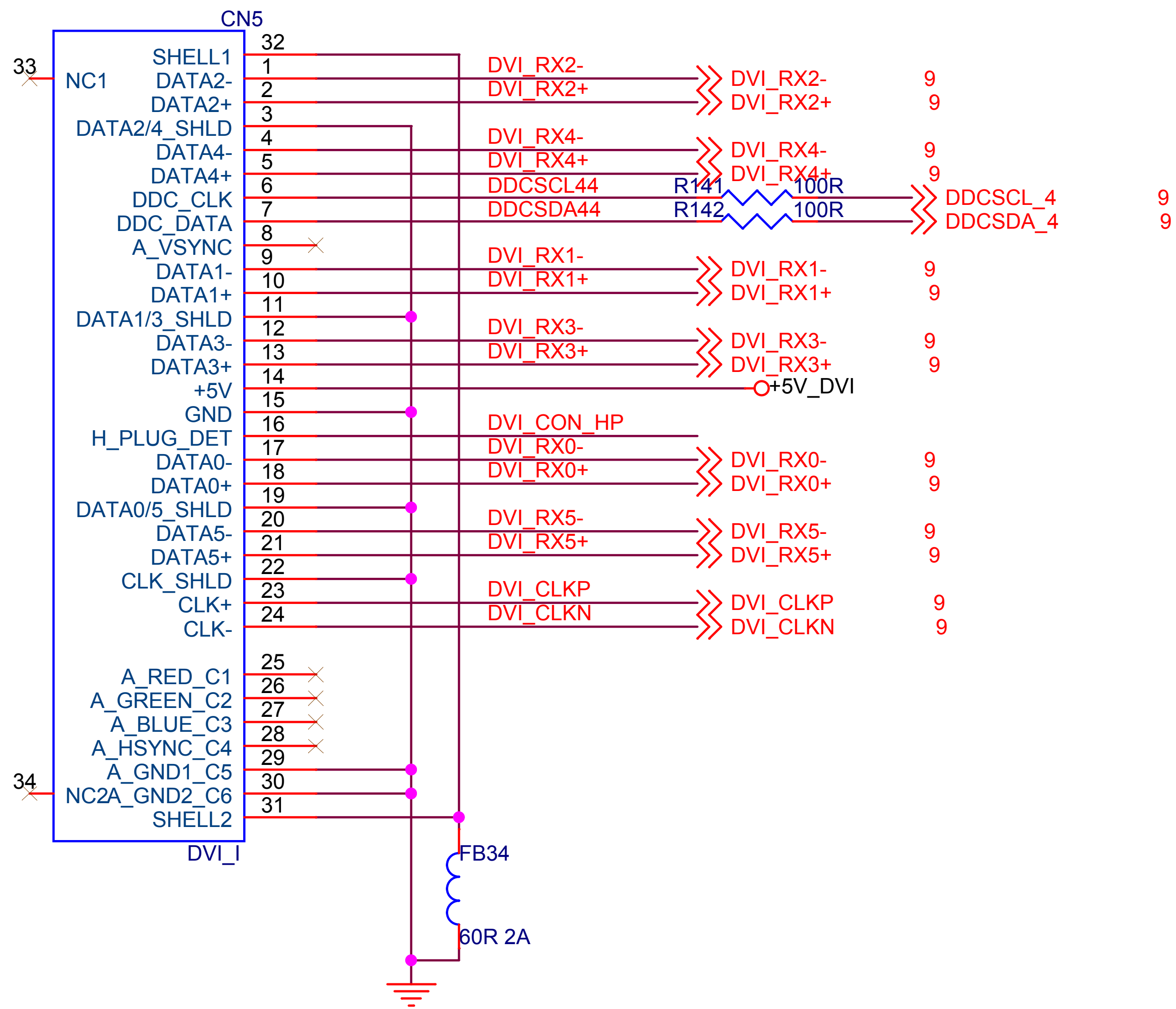
### INPUT\_HDMI1.4/MHL

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|-------|-------------|--------|---------|
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## Dual\_DVI Input



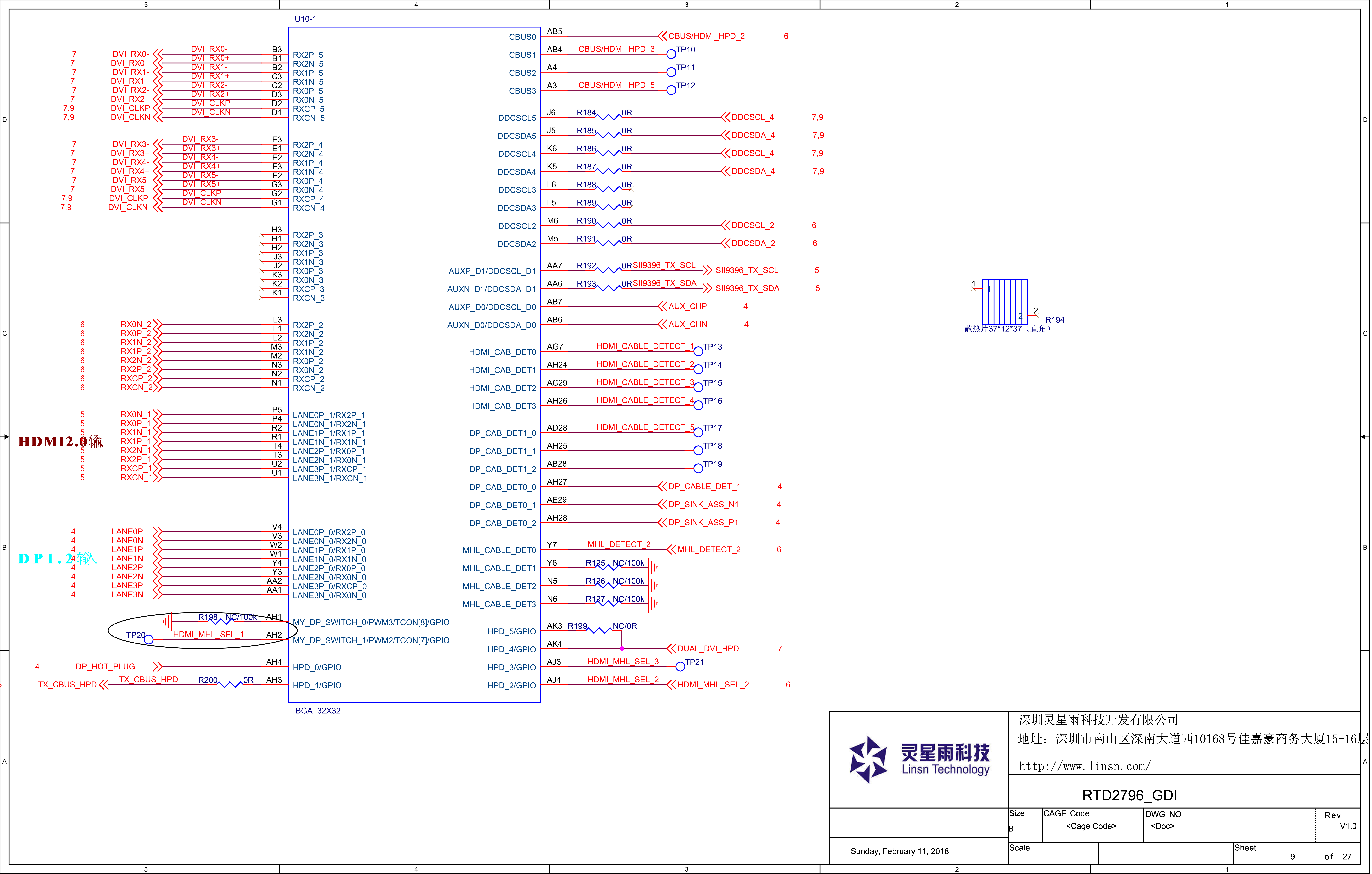
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INPUT\_DUAL DVI

|                           |            |                          |                  |             |
|---------------------------|------------|--------------------------|------------------|-------------|
|                           | Size<br>A3 | CAGE Code<br><Cage Code> | DWG NO<br><Doc>  | Rev<br>V1.0 |
| Sunday, February 11, 2018 | Scale      |                          | Sheet<br>7 of 27 |             |





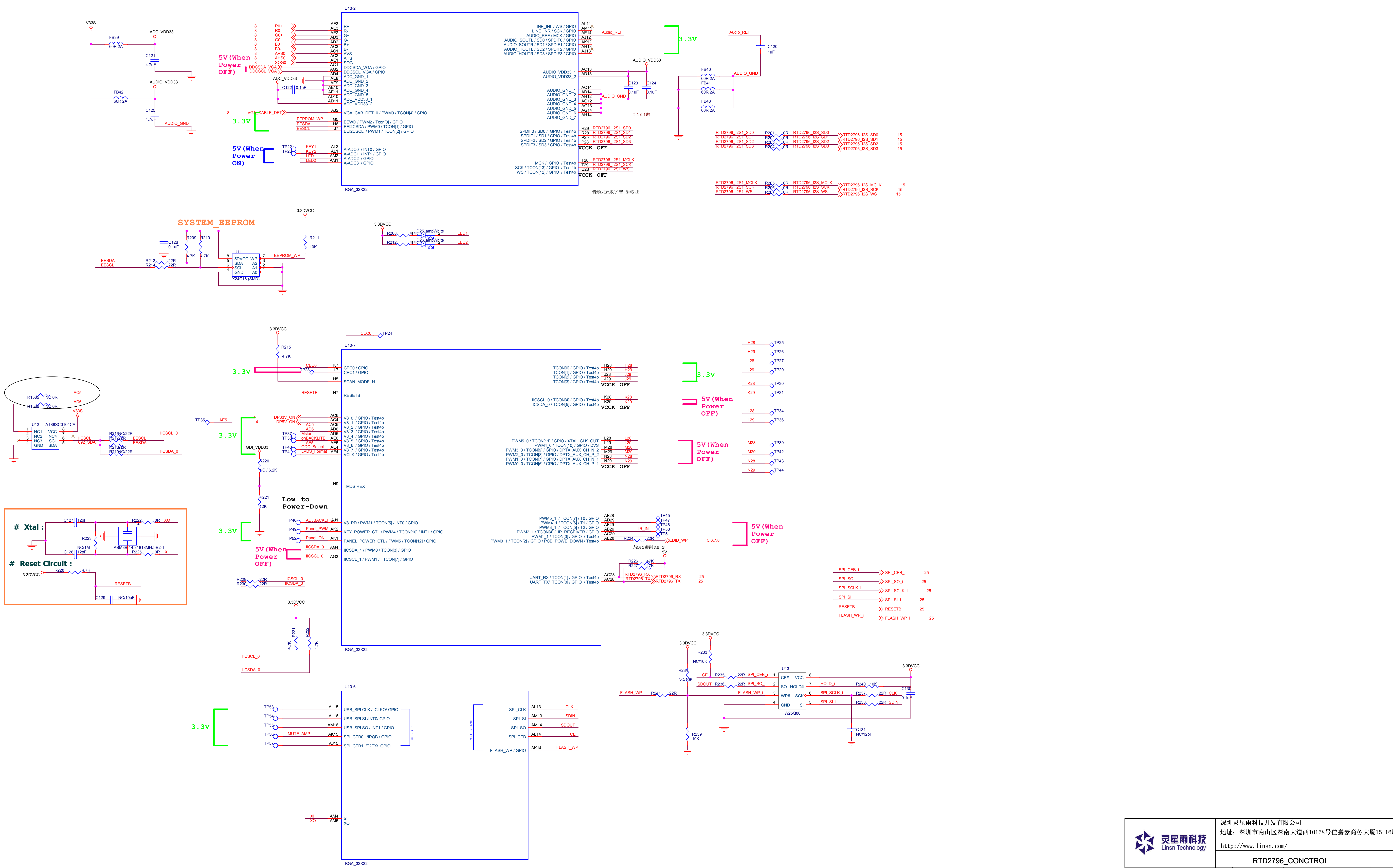


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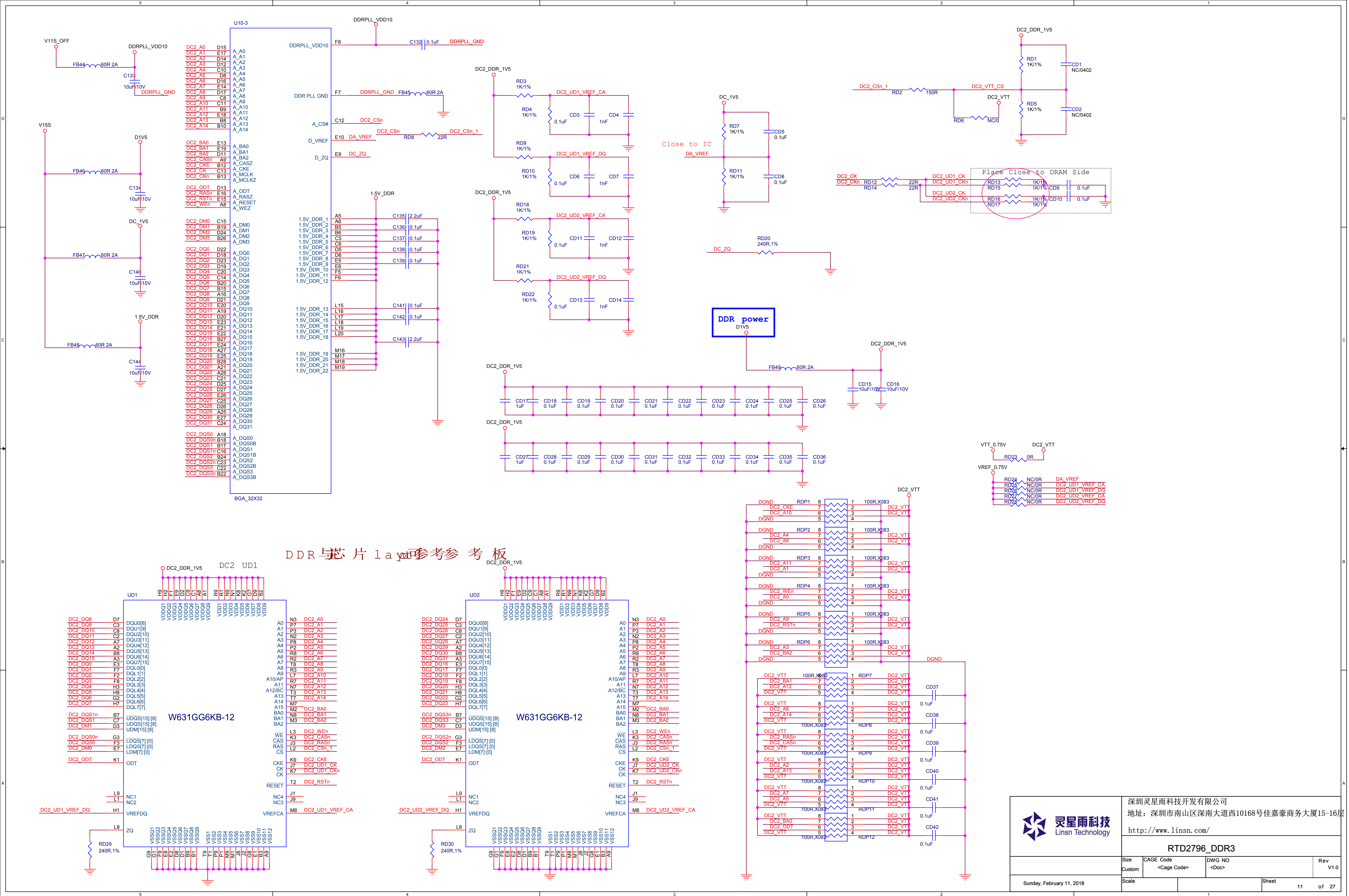
RTD2796\_GDI

| Size  | CAGE Code   | DWG NO | Rev     |
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| Scale |             | Sheet  | 9 of 27 |

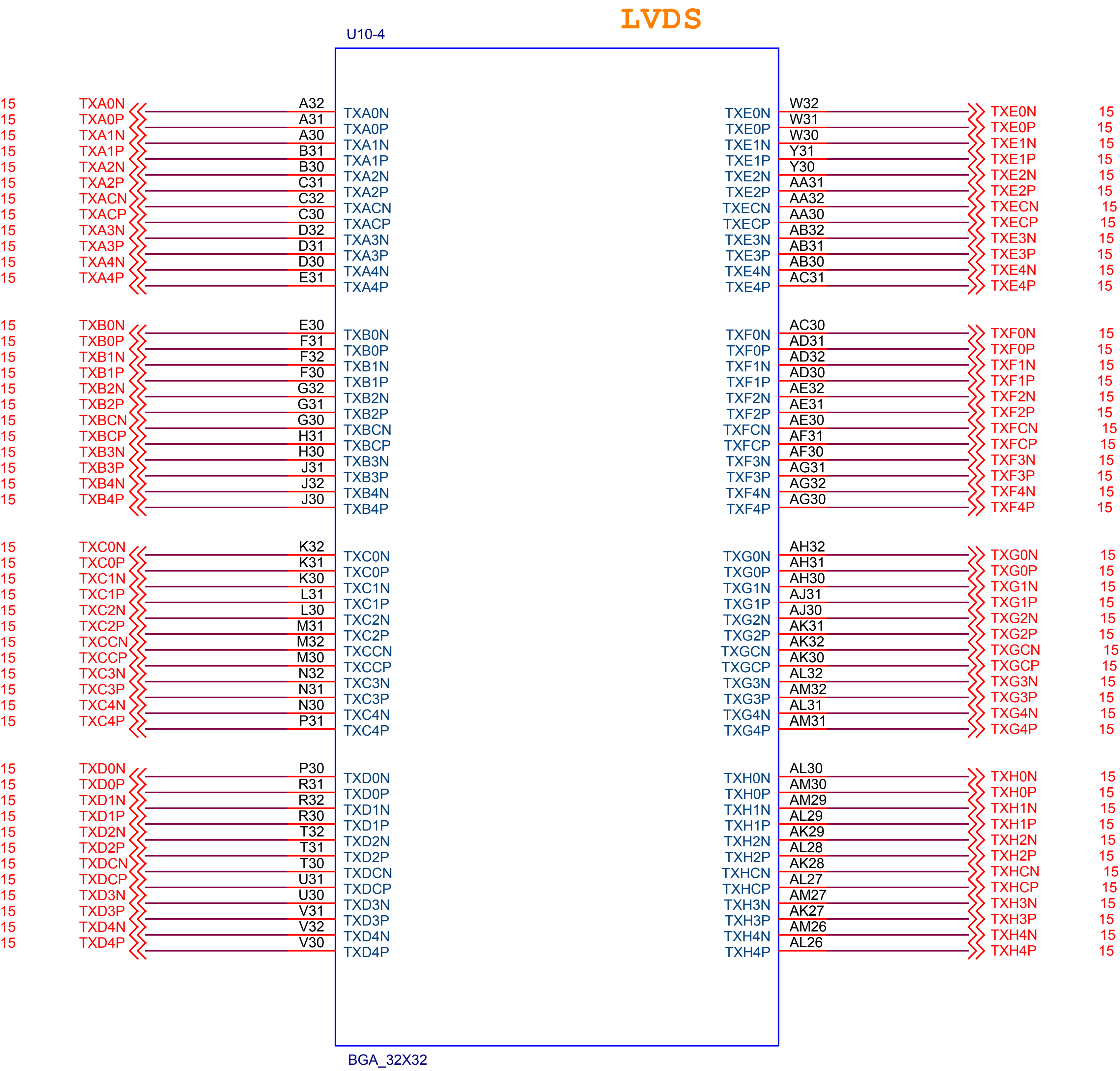
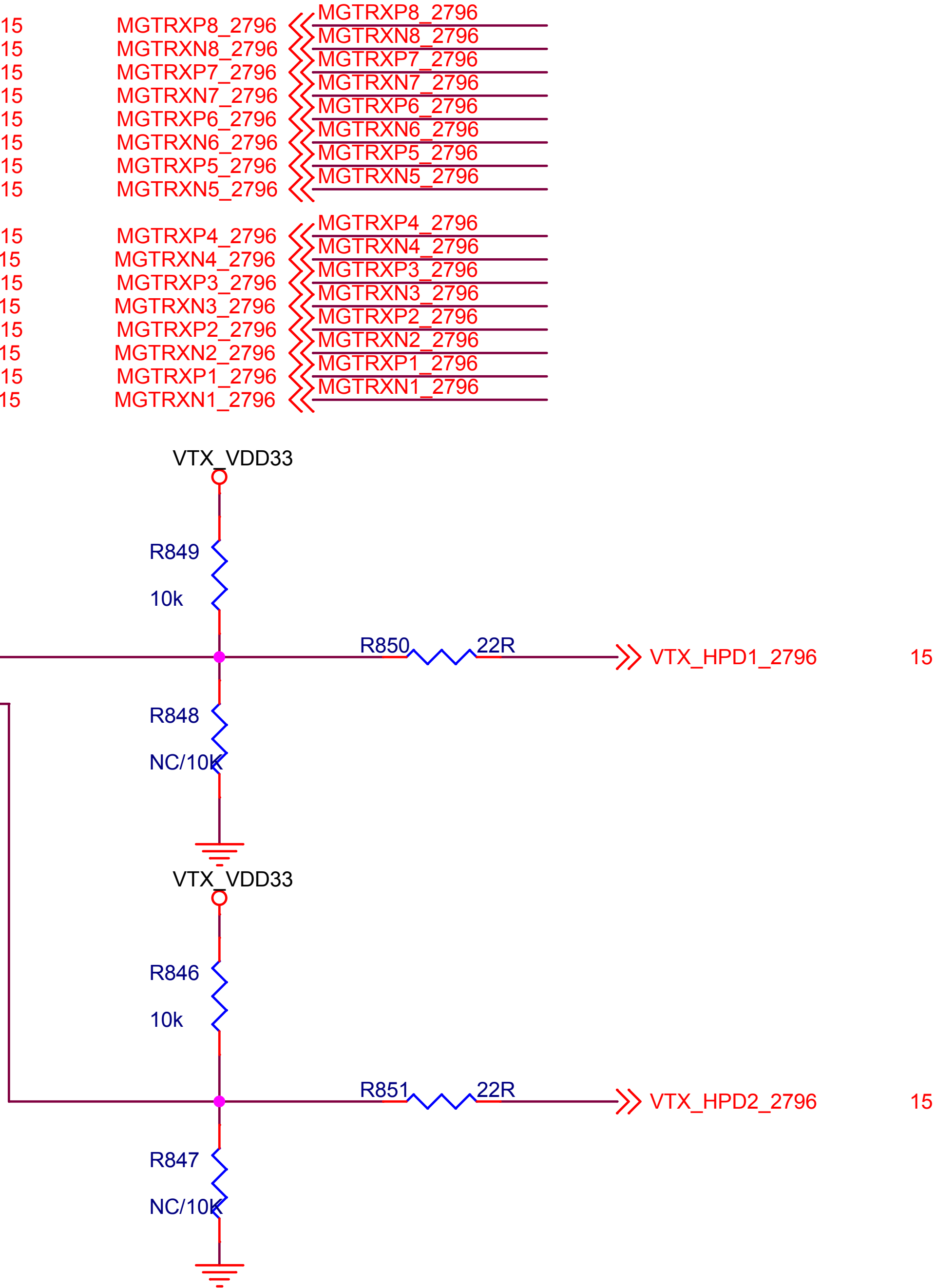
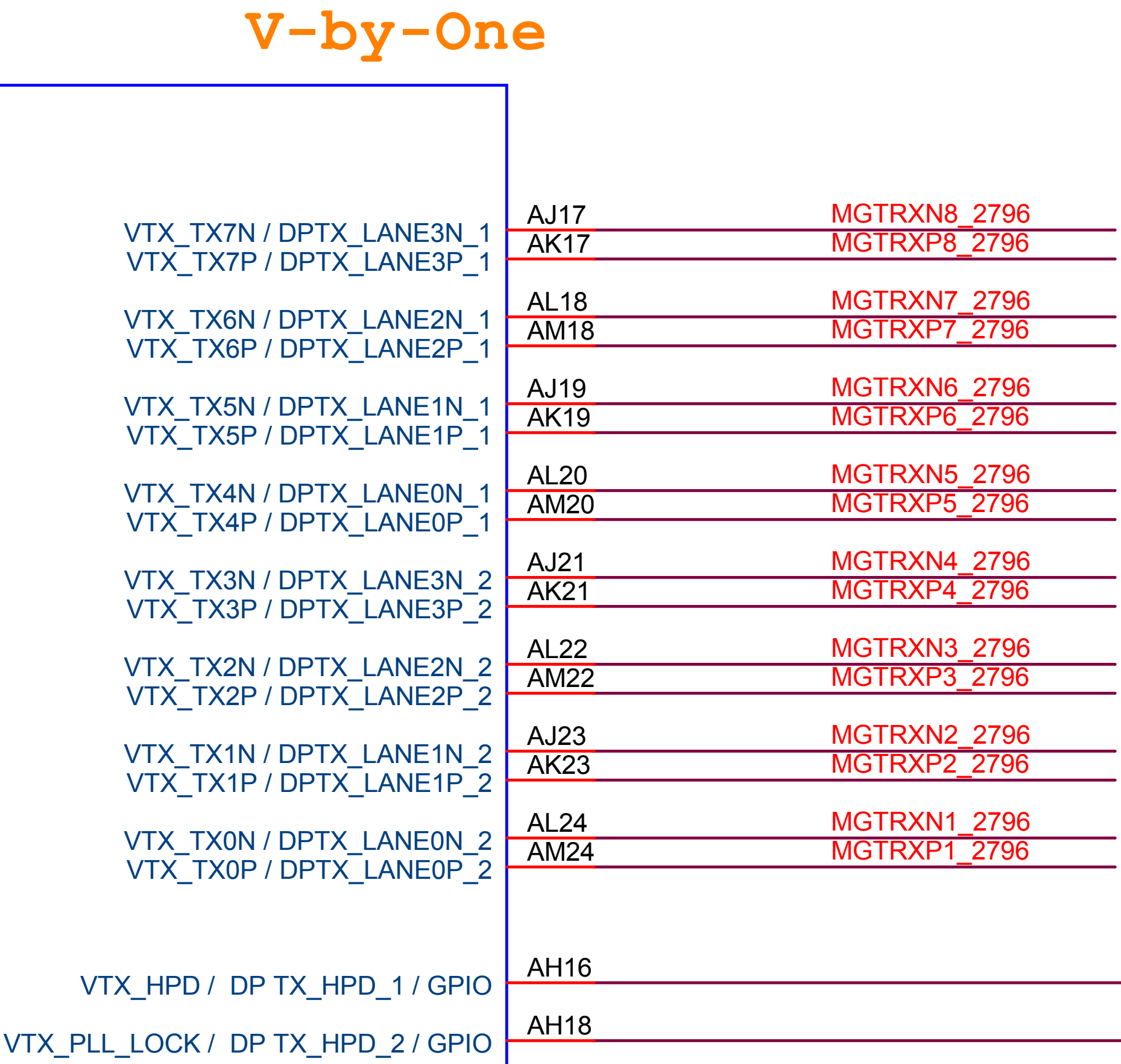
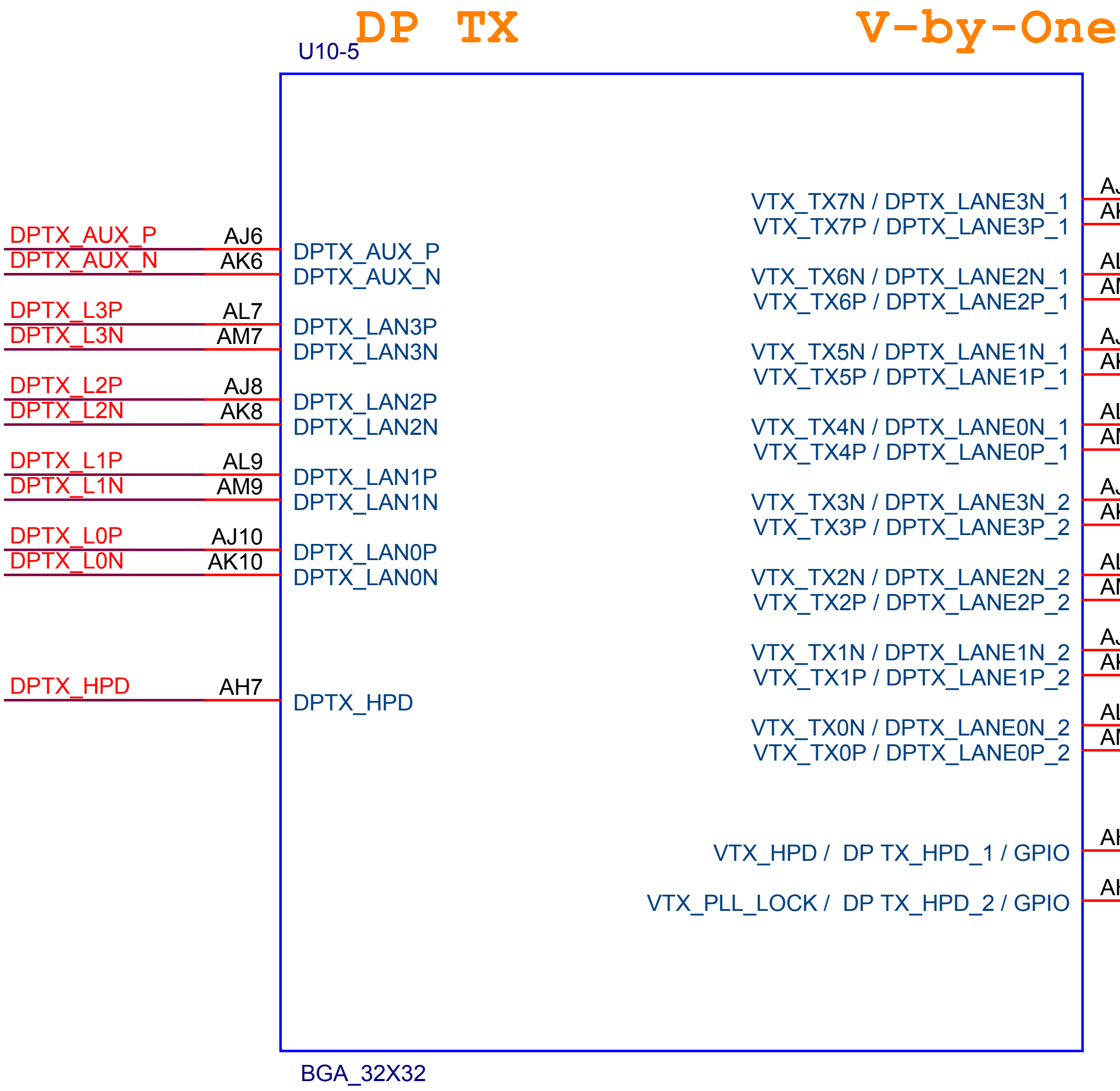
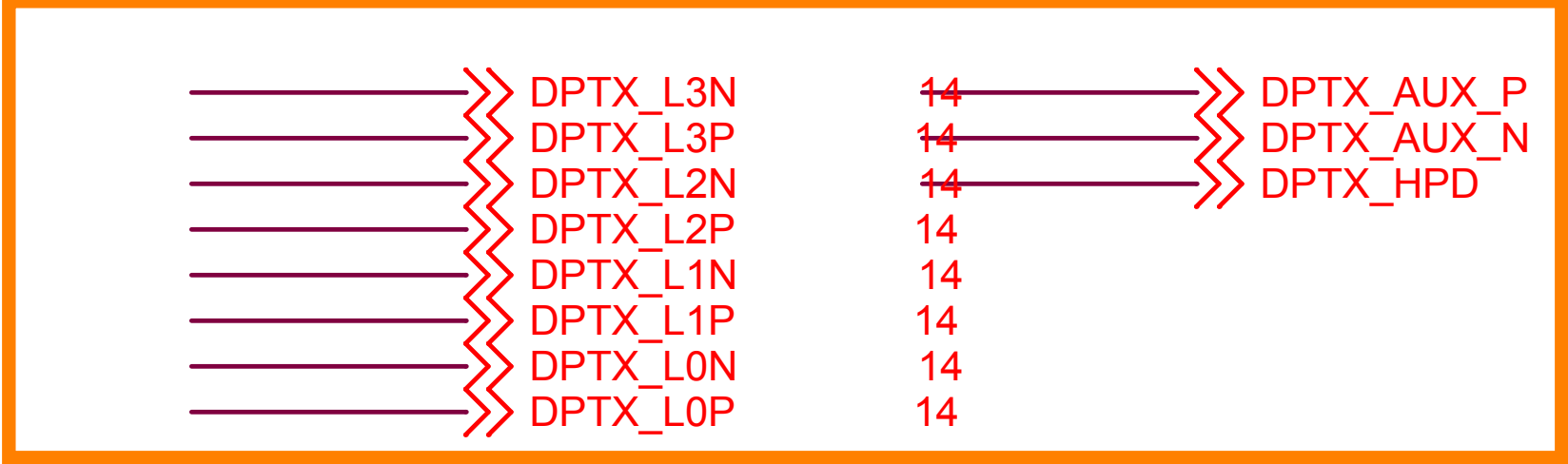















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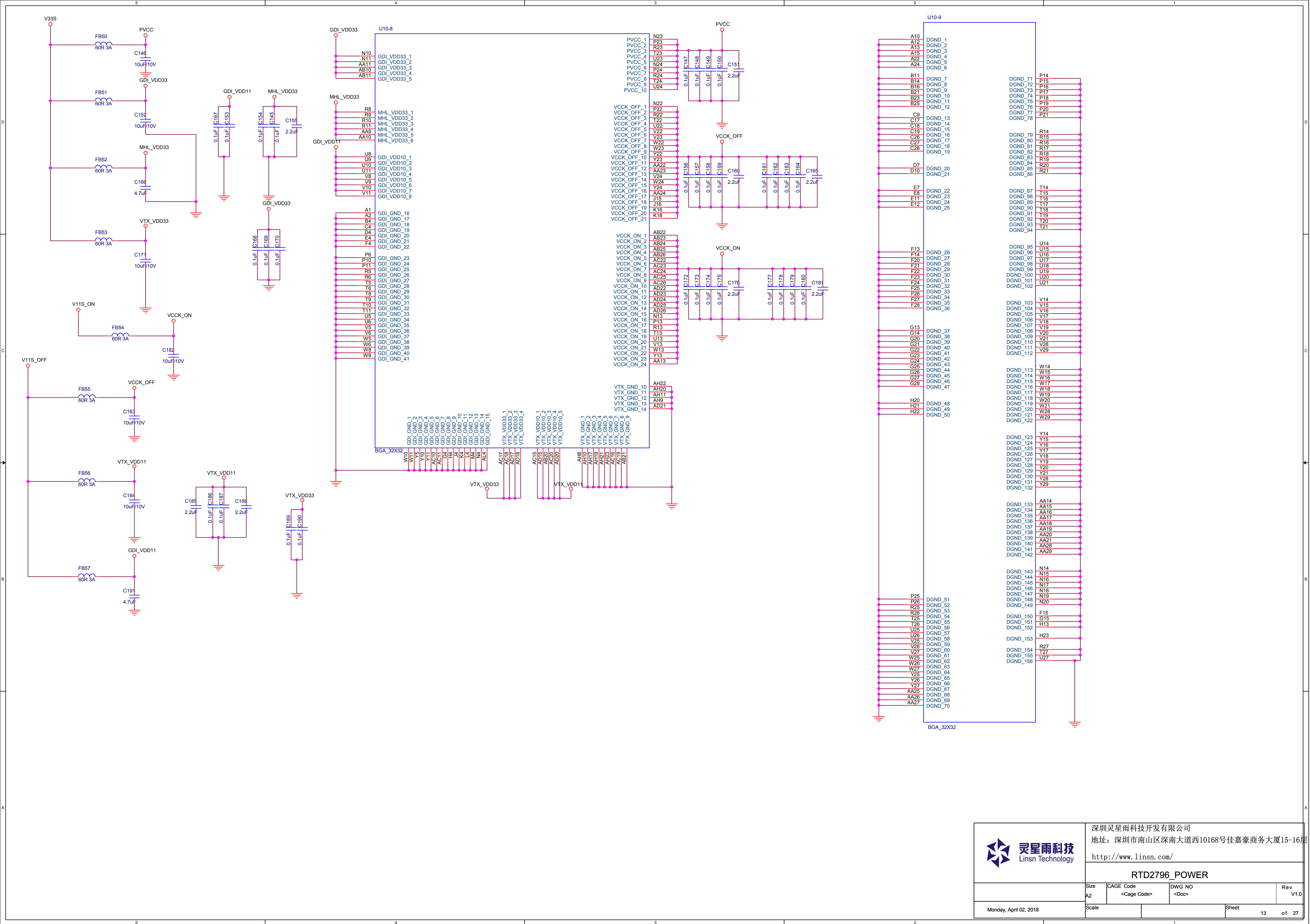
<http://www.linsn.com/>

**RTD2796\_OUTPUT**

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| Size   | CAGE Code   | DWG NO | Rev      |
| Custom | <Cage Code> | <Doc>  | V1.0     |
| Scale  |             | Sheet  | 12 of 27 |

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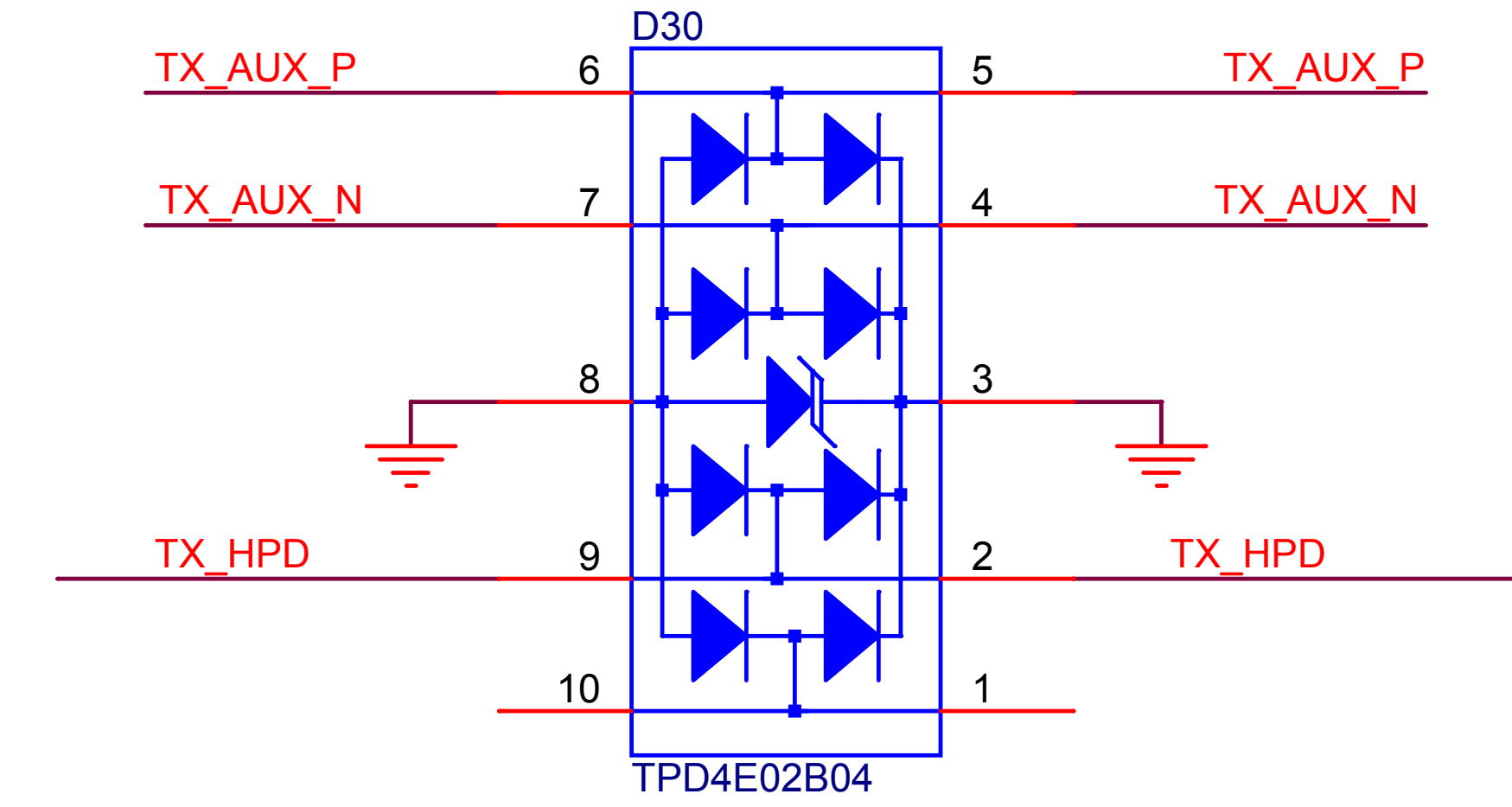
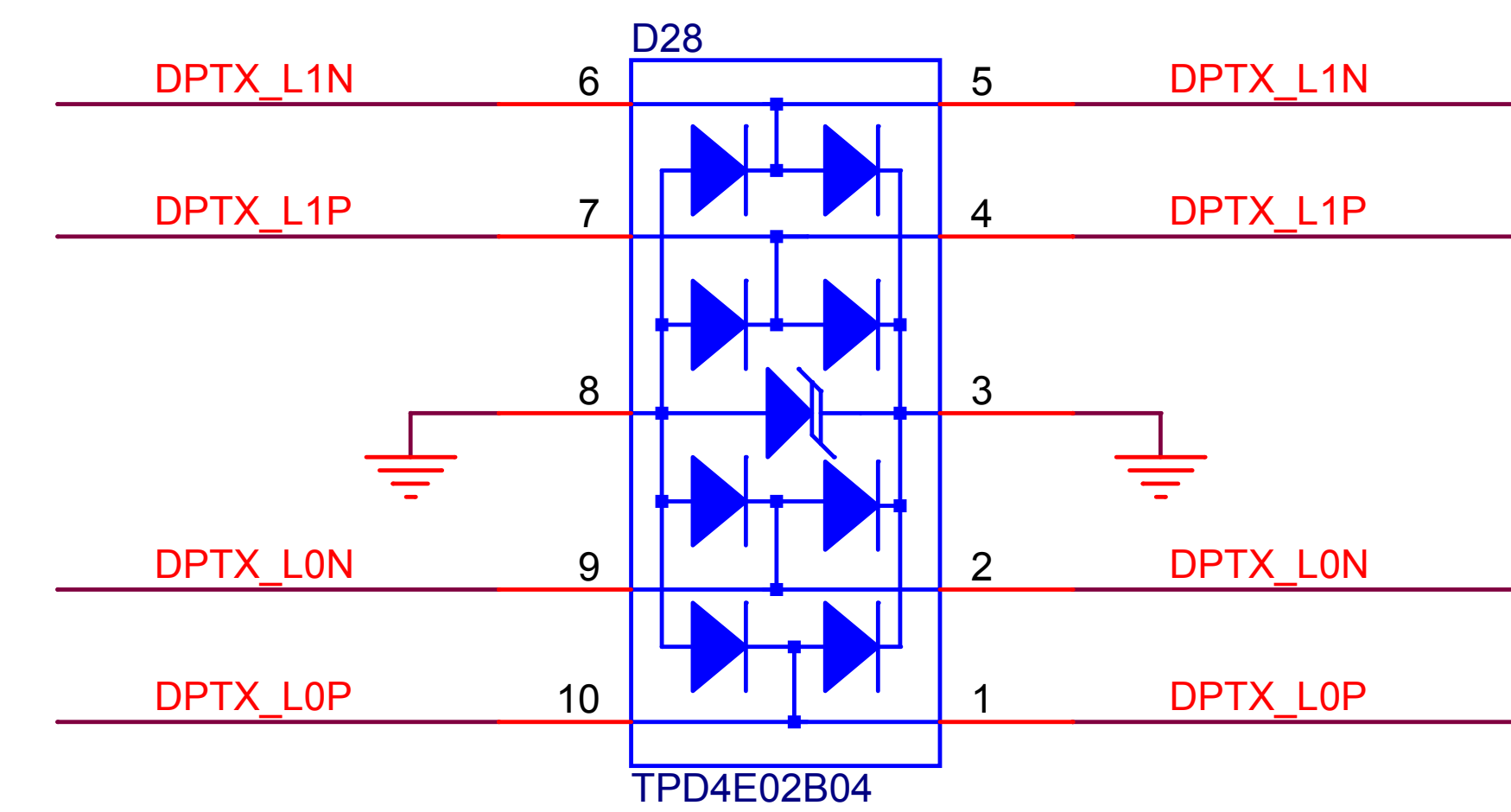
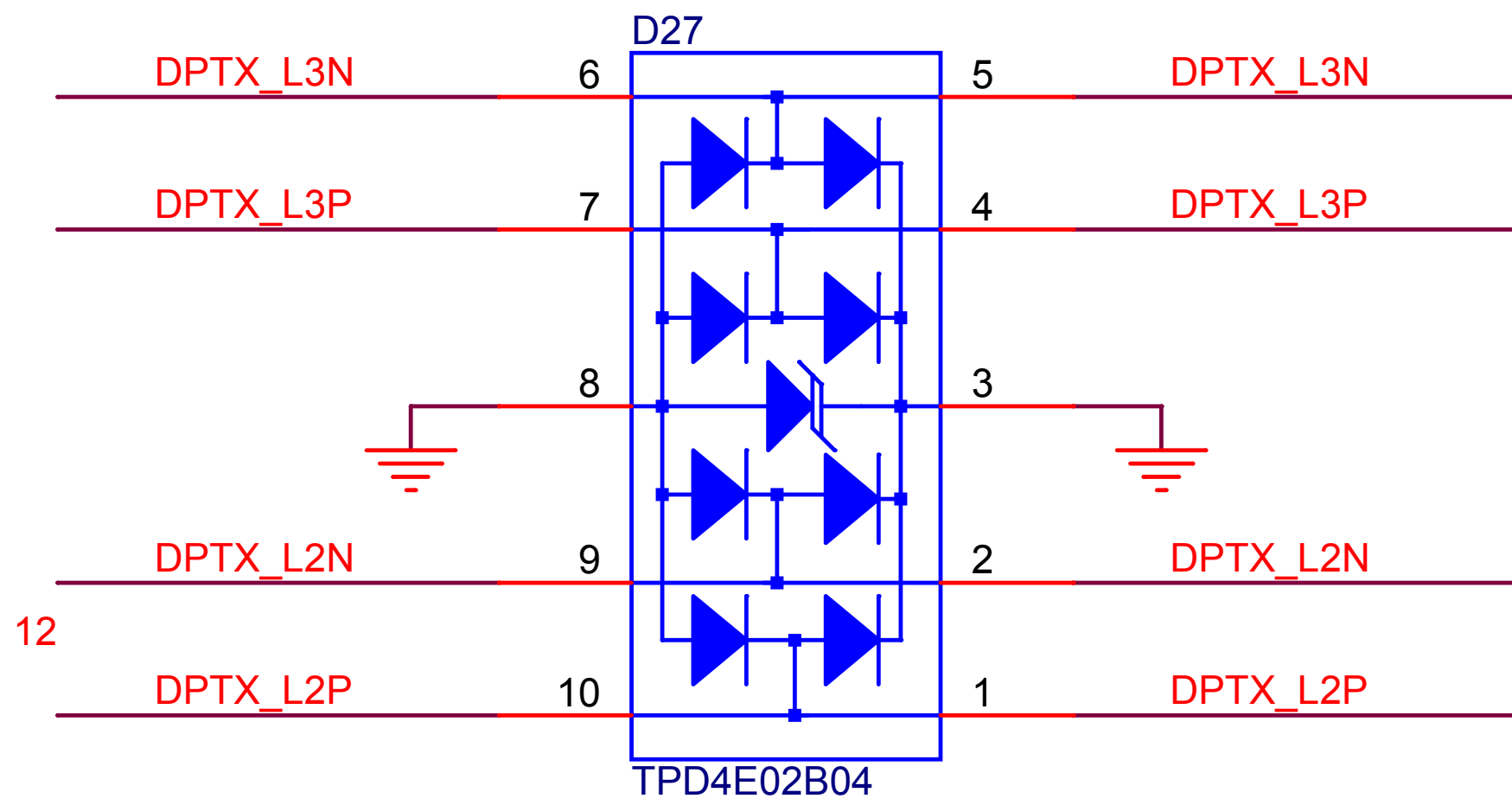
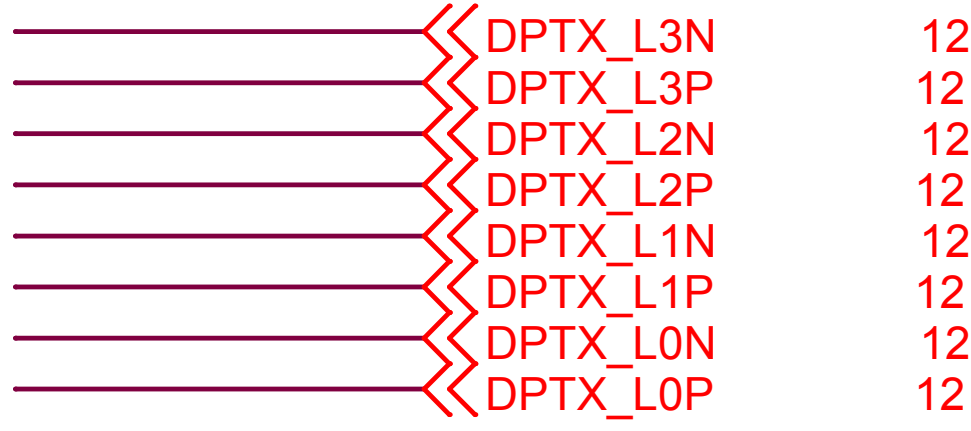
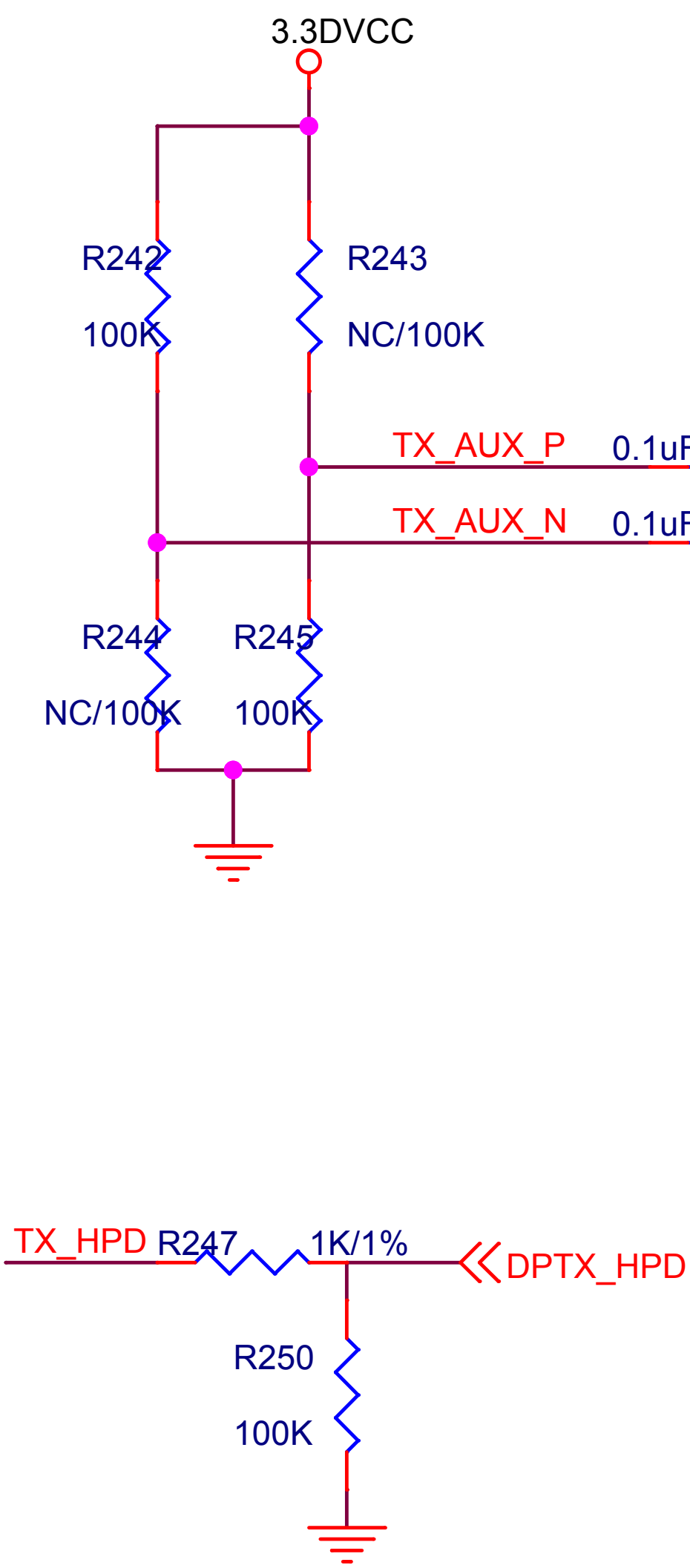
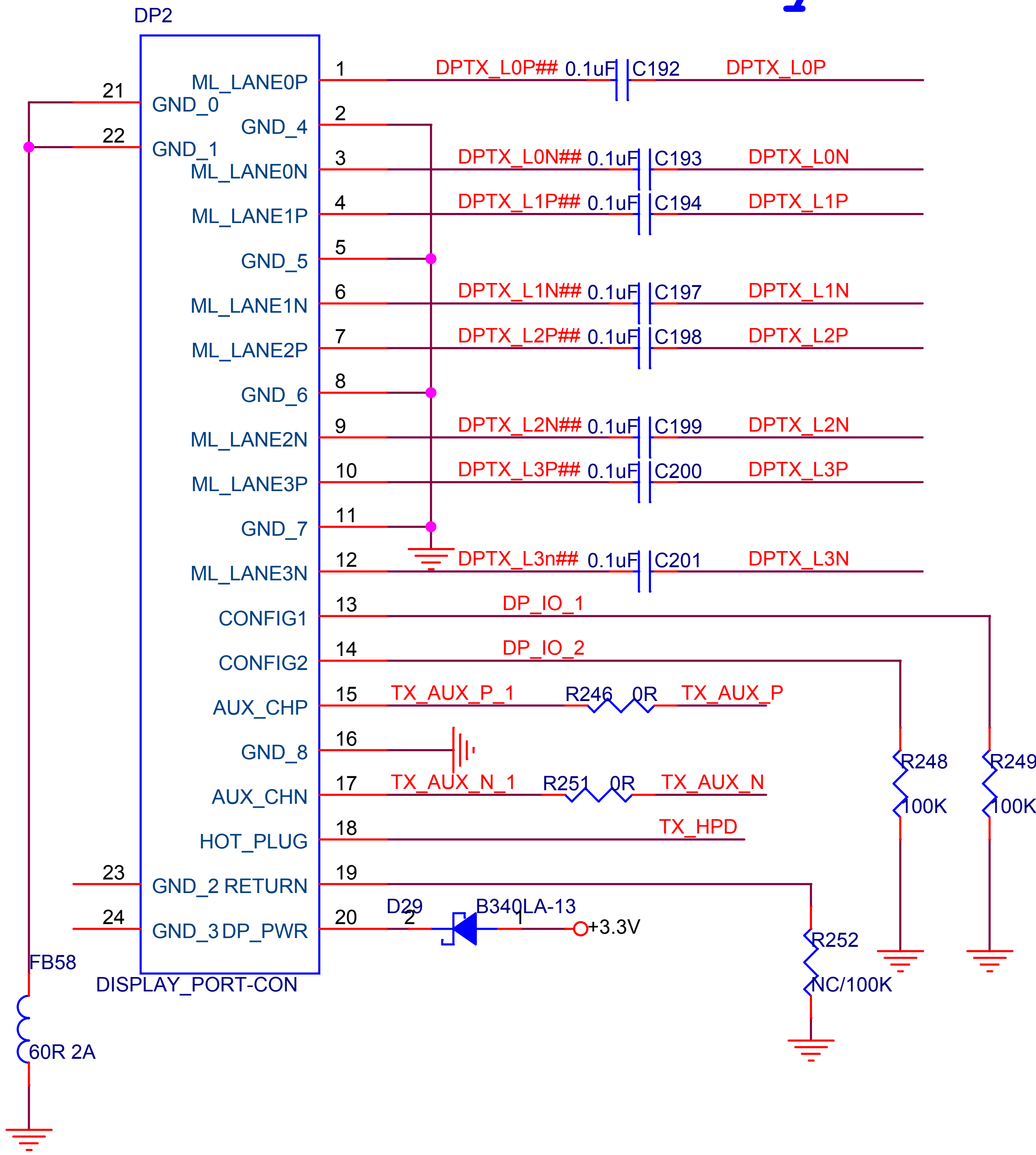



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RTD2796\_POWER

|                        |            |                          |                 |             |
|------------------------|------------|--------------------------|-----------------|-------------|
|                        | Size<br>A2 | CAGE Code<br><Cage Code> | DWG NO<br><Doc> | Rev<br>V1.0 |
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DP MST Daisy-chain out





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OUTPUT\_DP

Size  
A3

CAGE Code  
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DWG NO  
<Doc>

Rev  
V1.0

Sunday, February 11, 2018

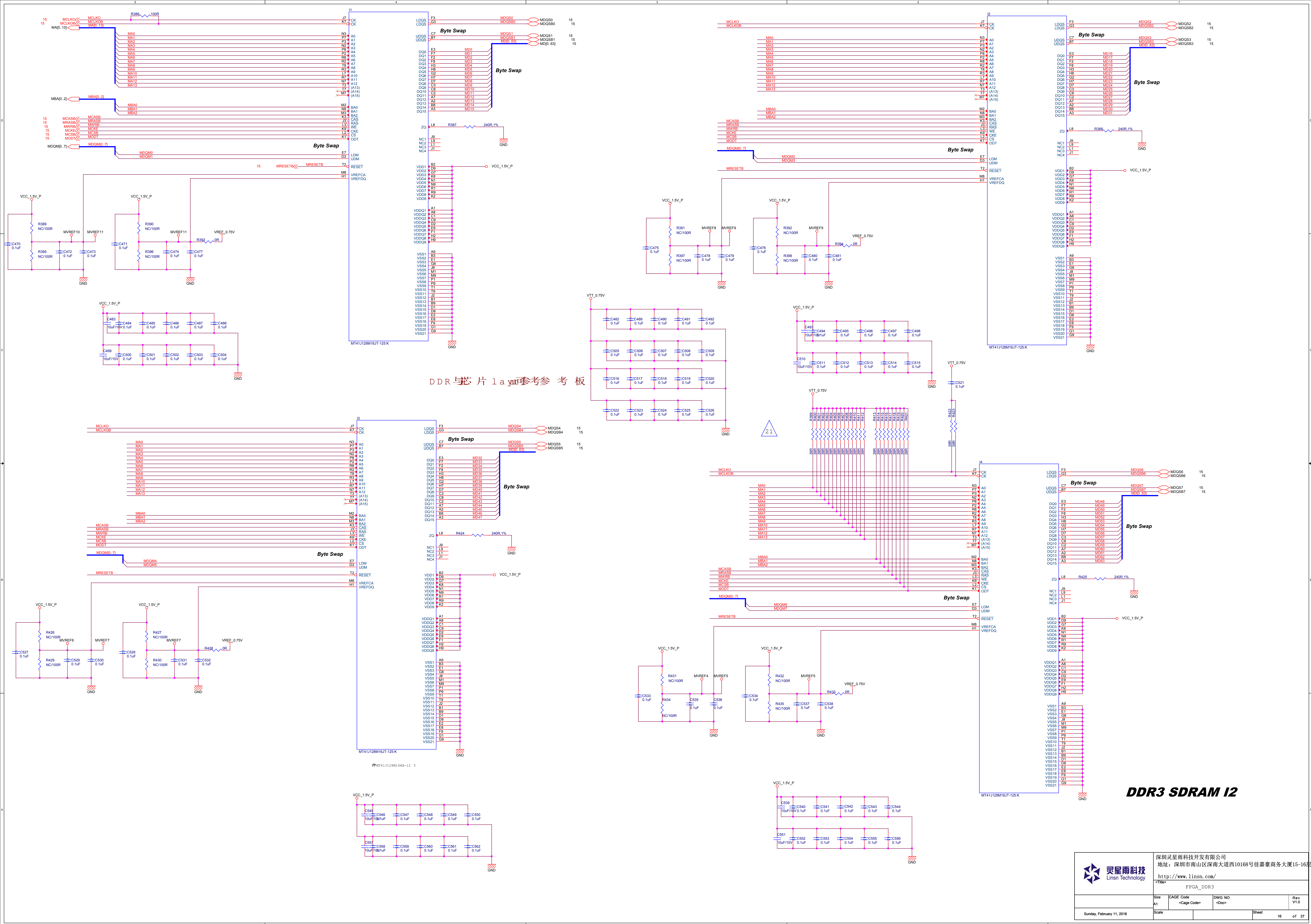
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Sheet  
14 of 27

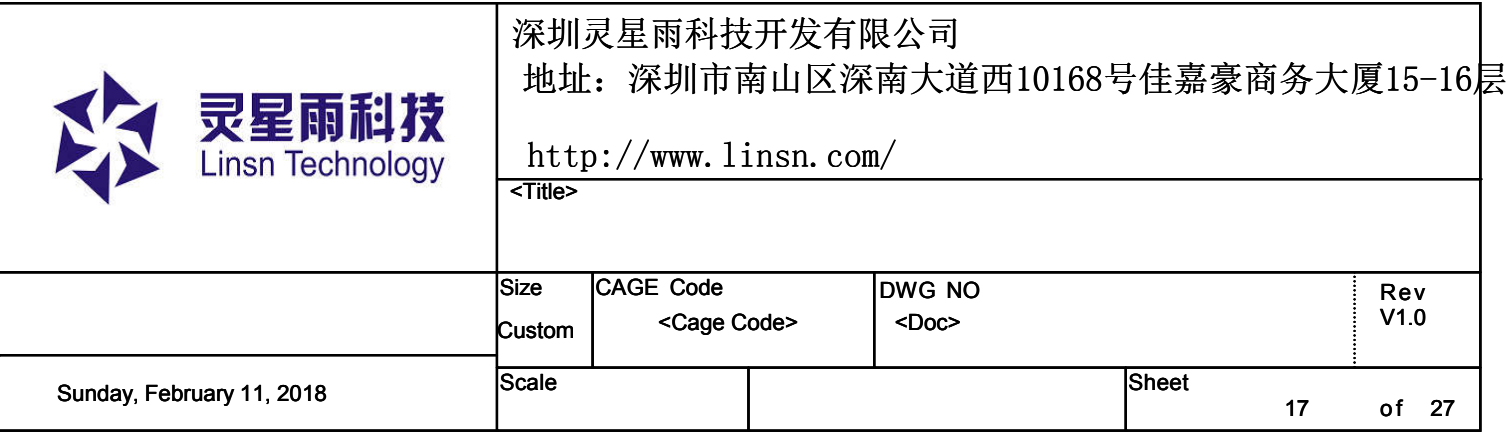




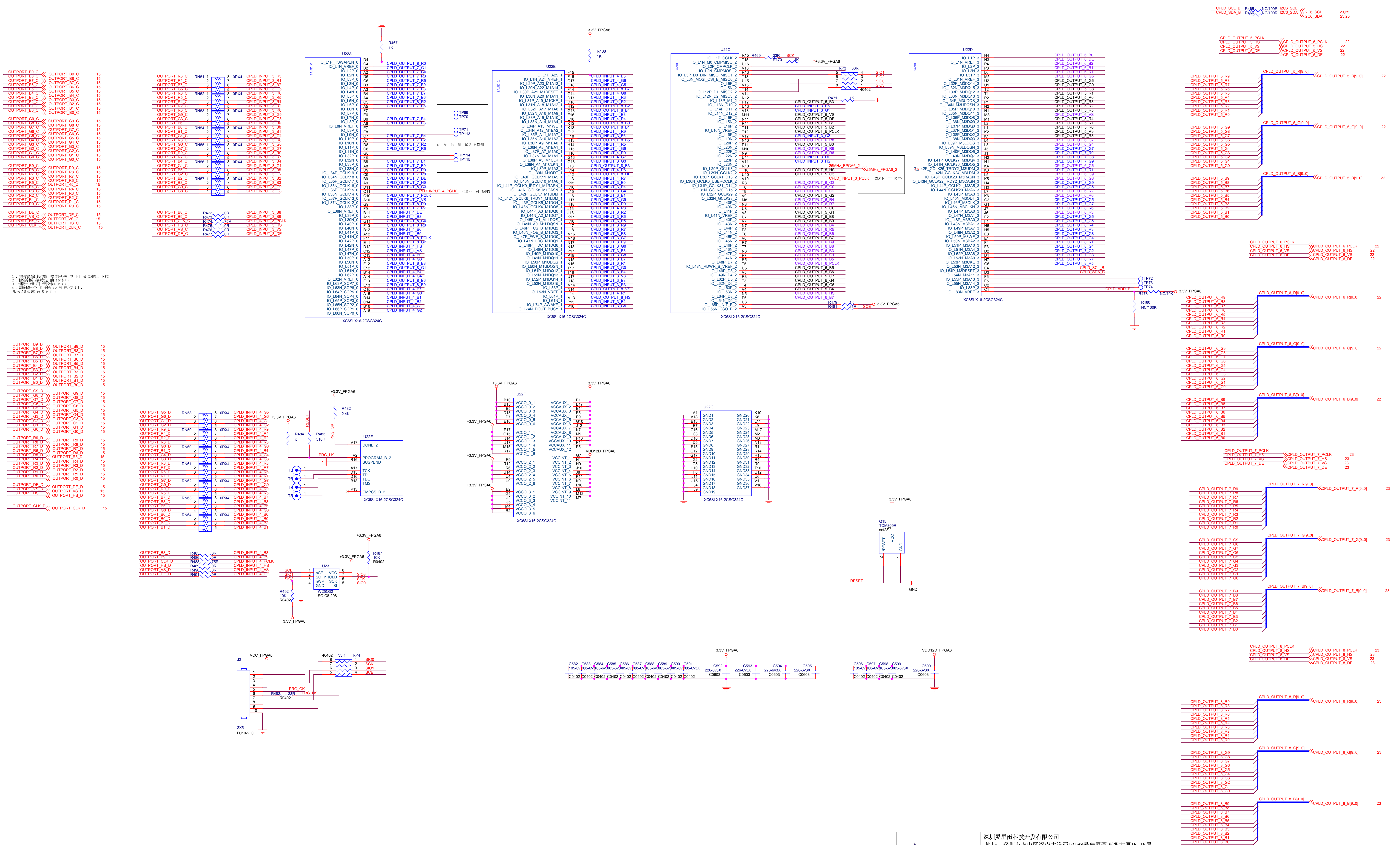




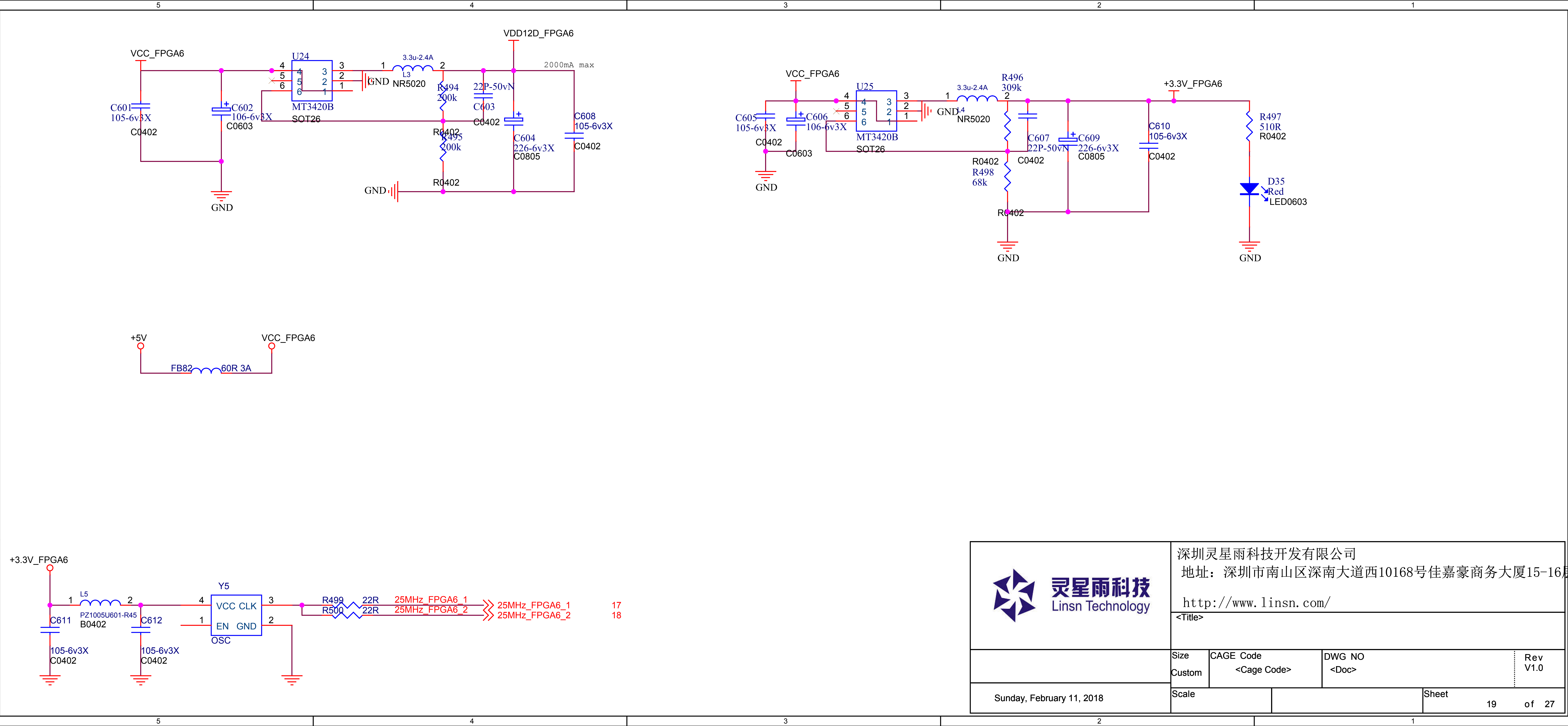




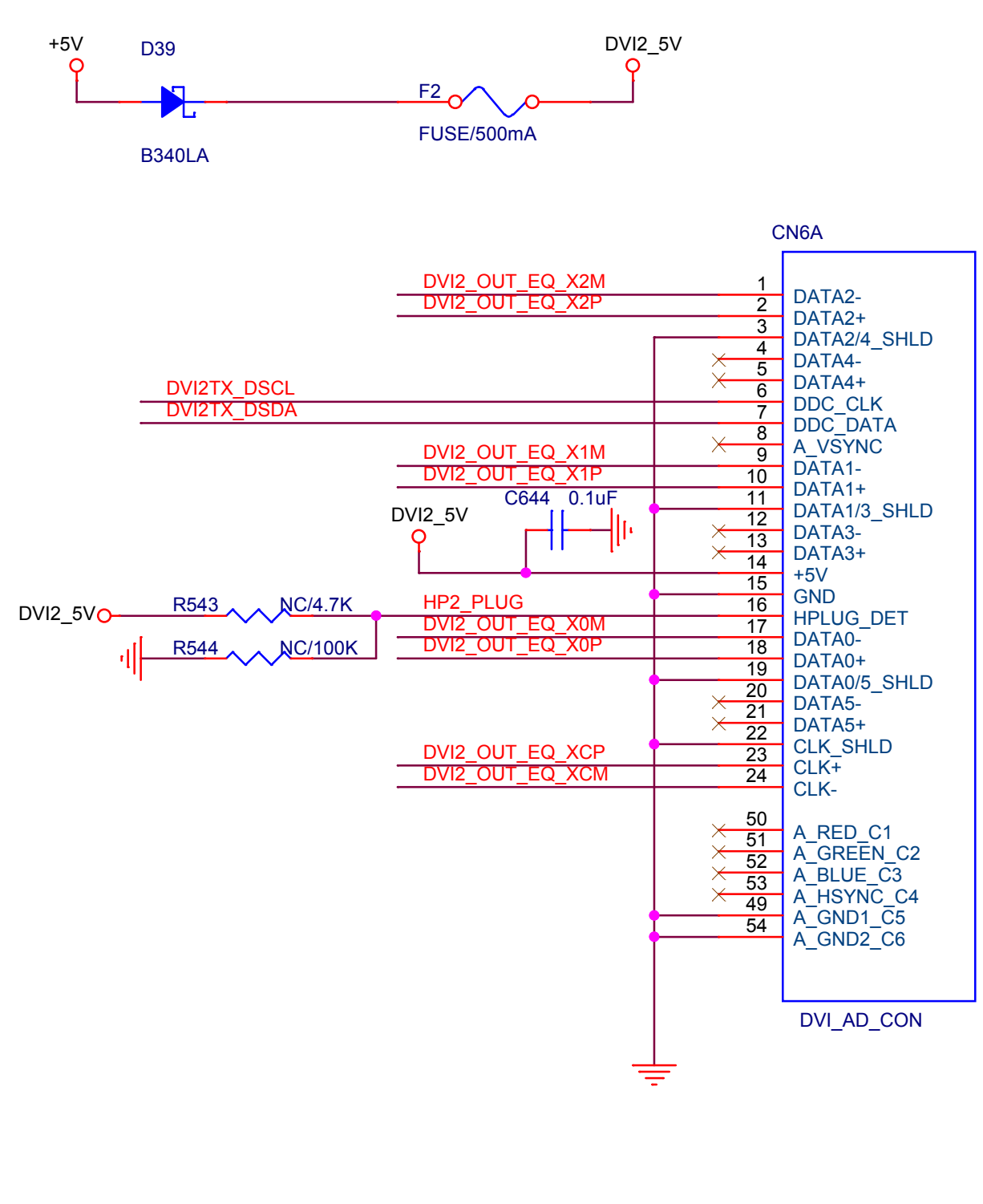
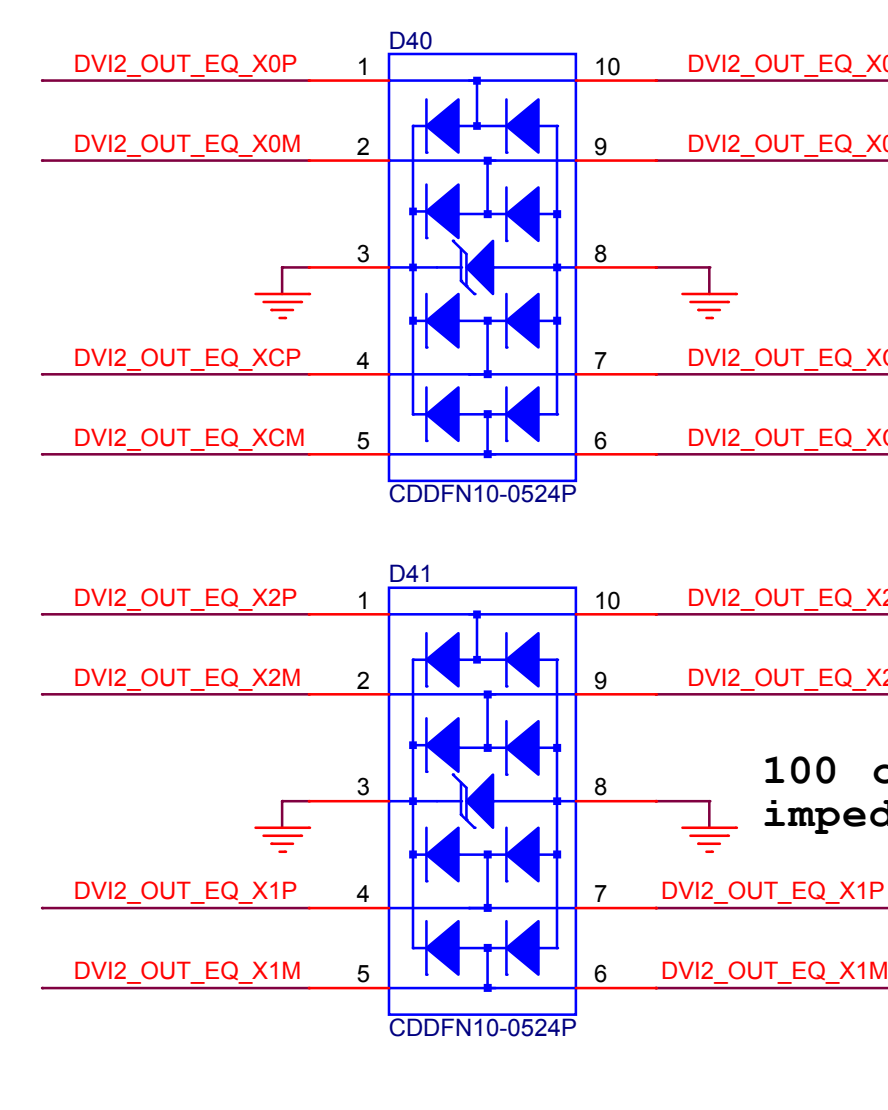
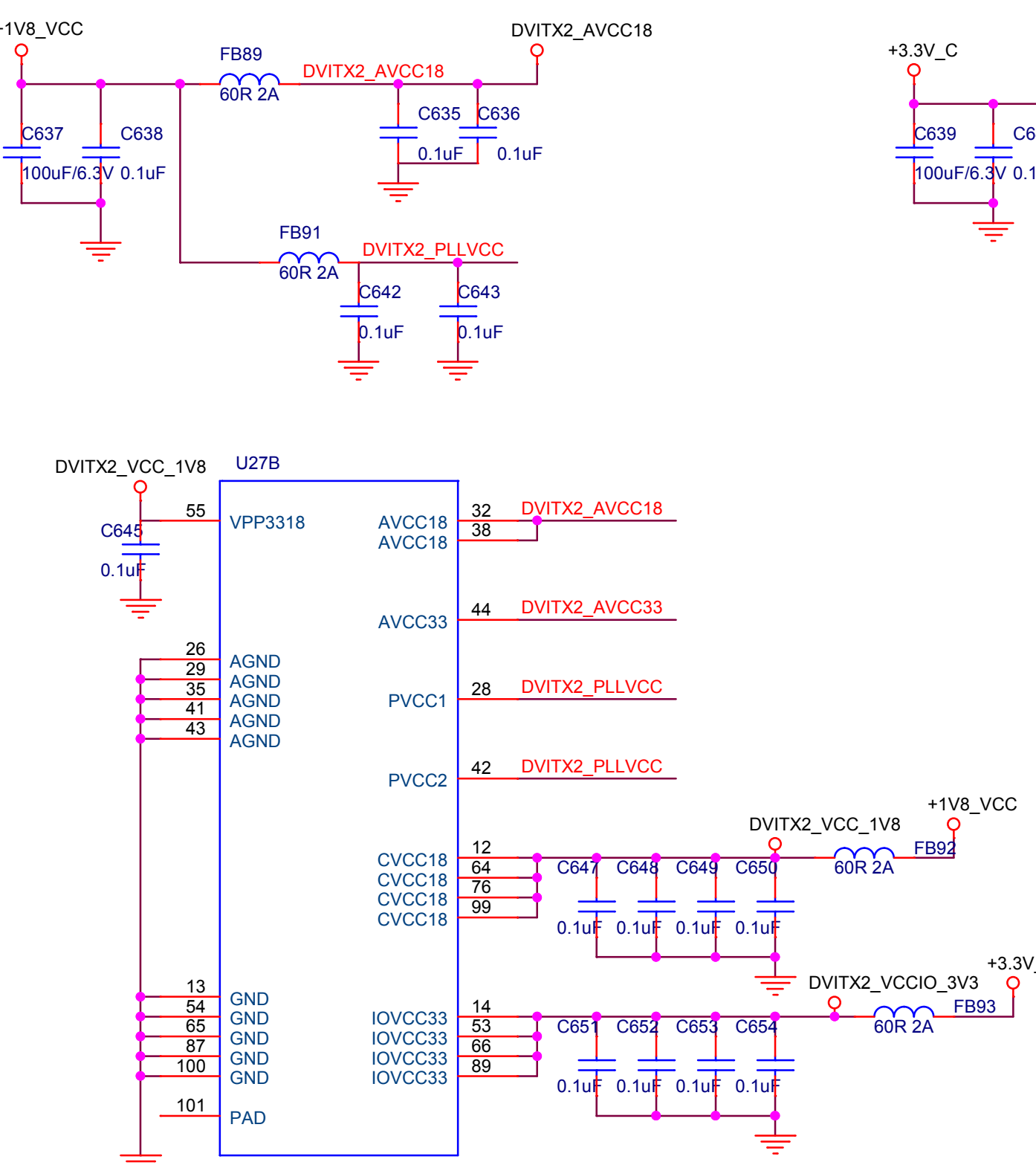
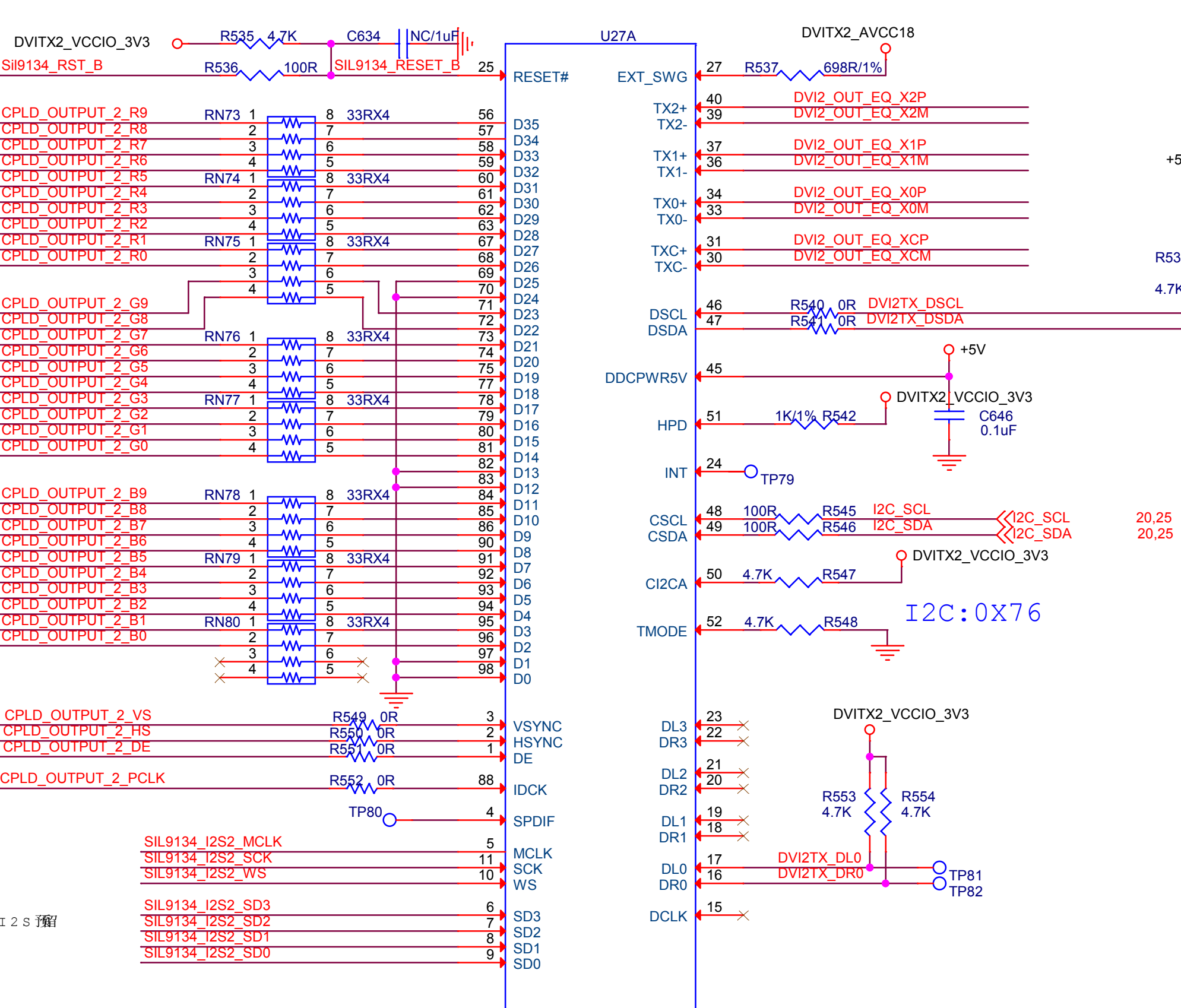
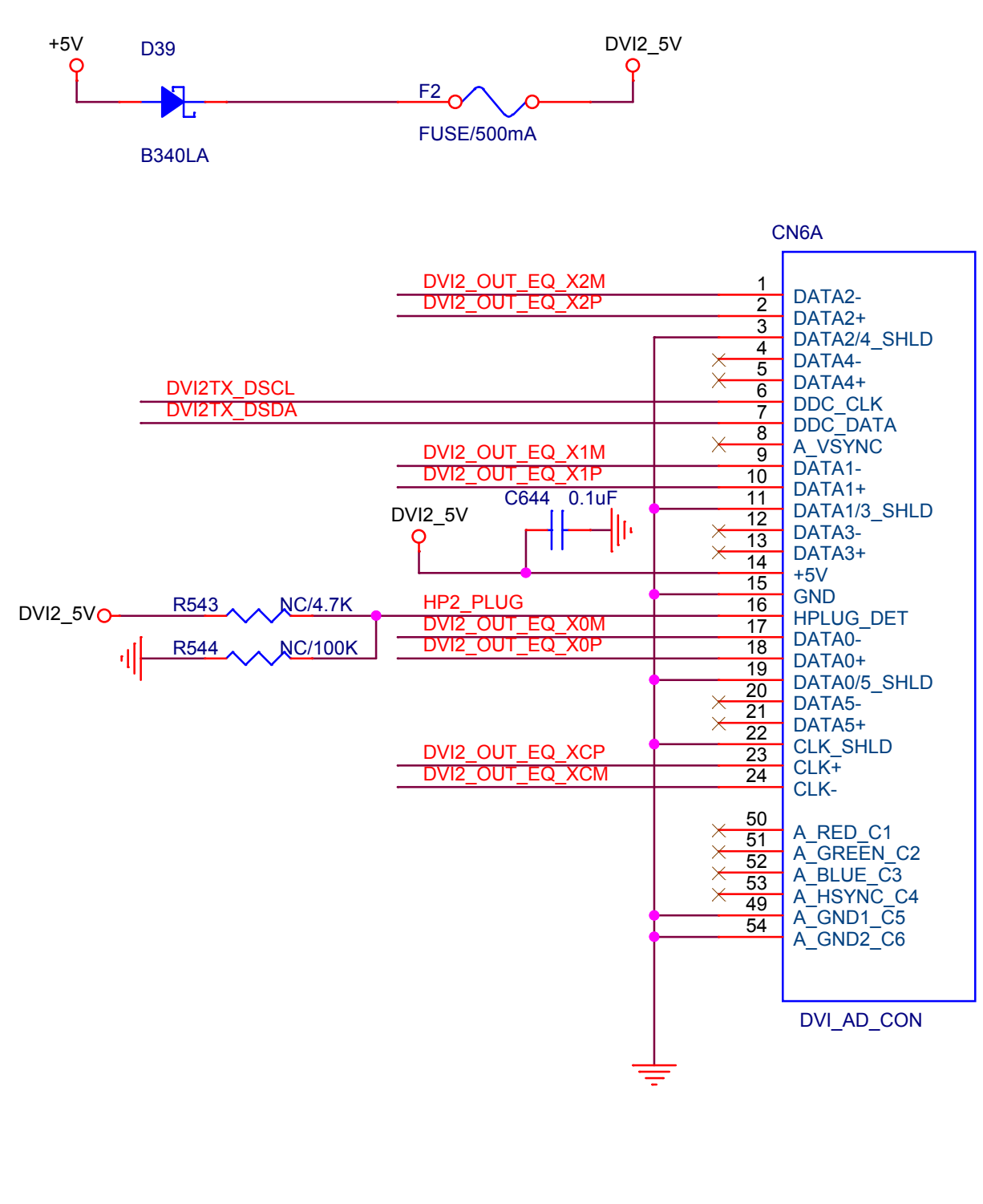
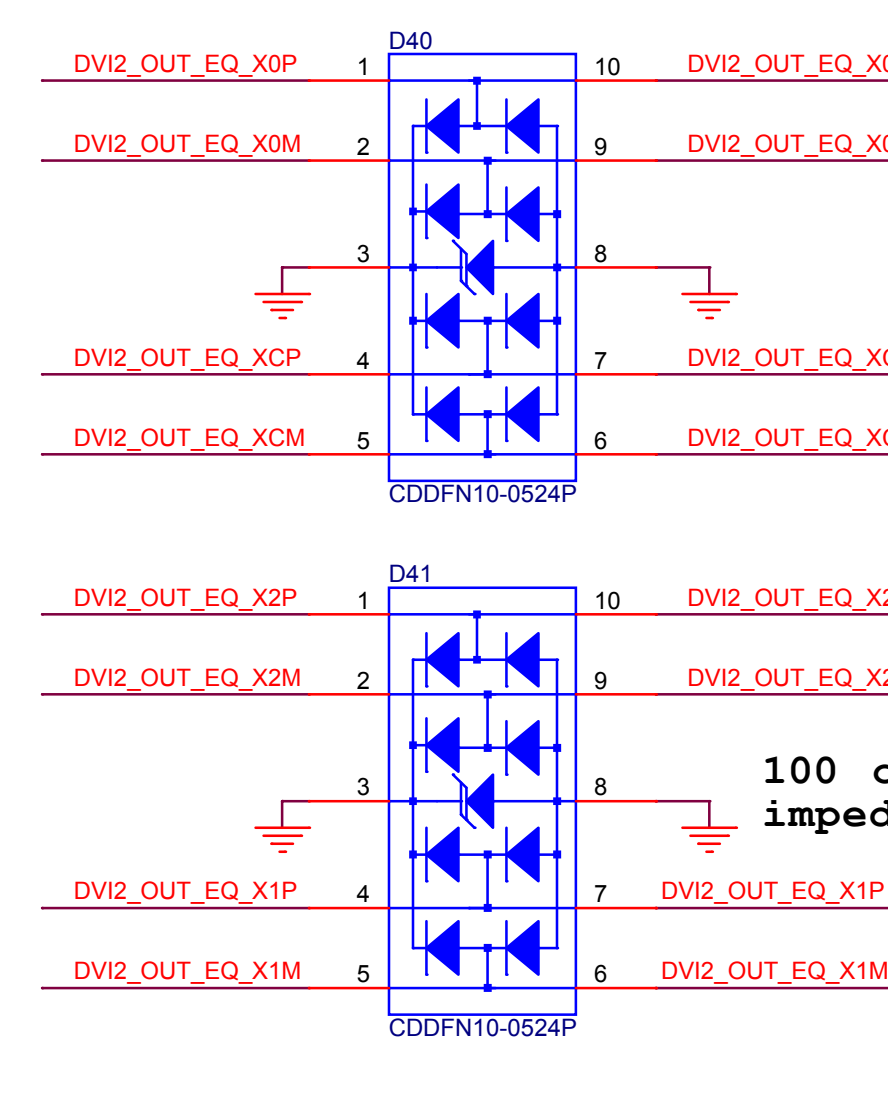
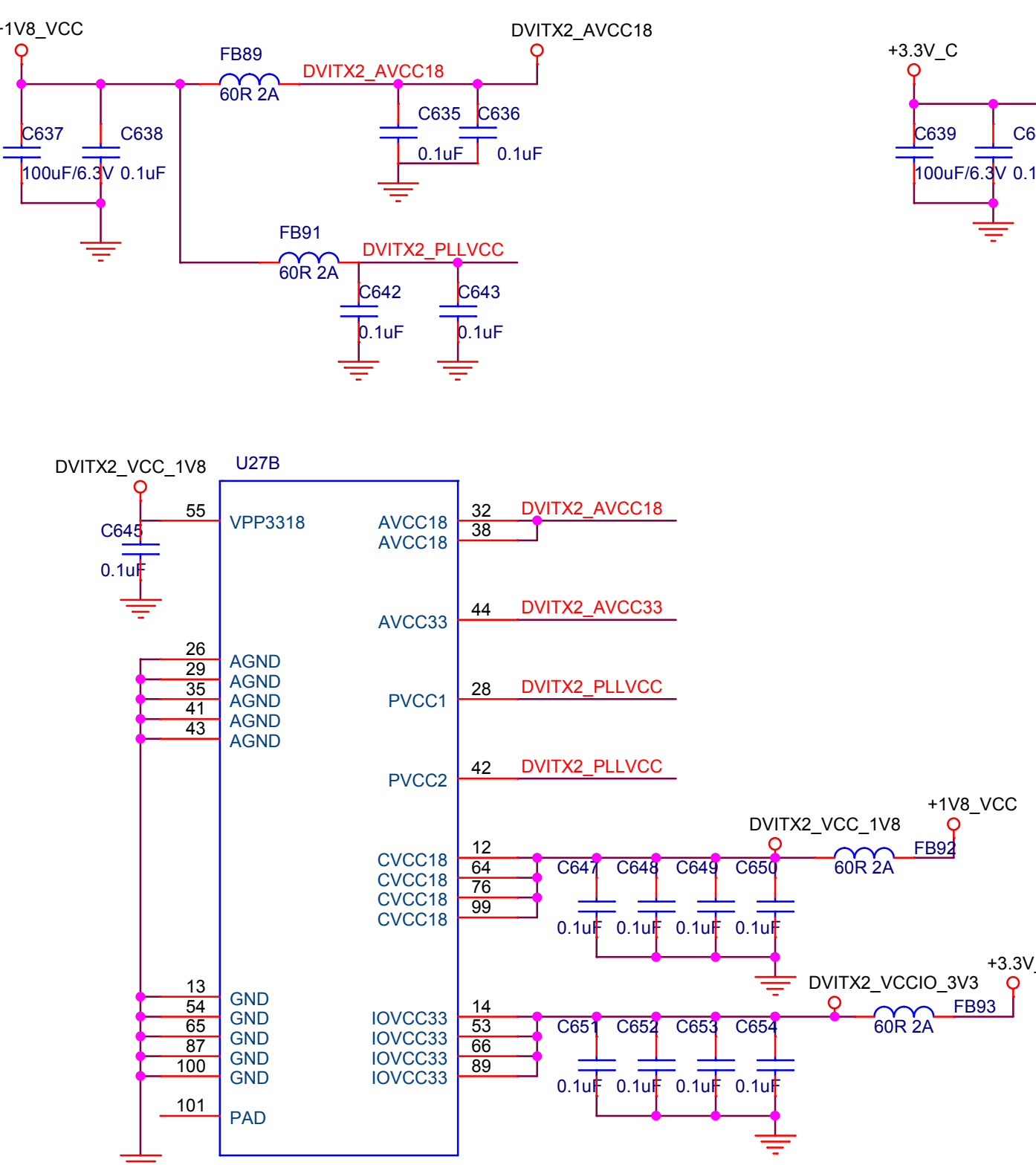
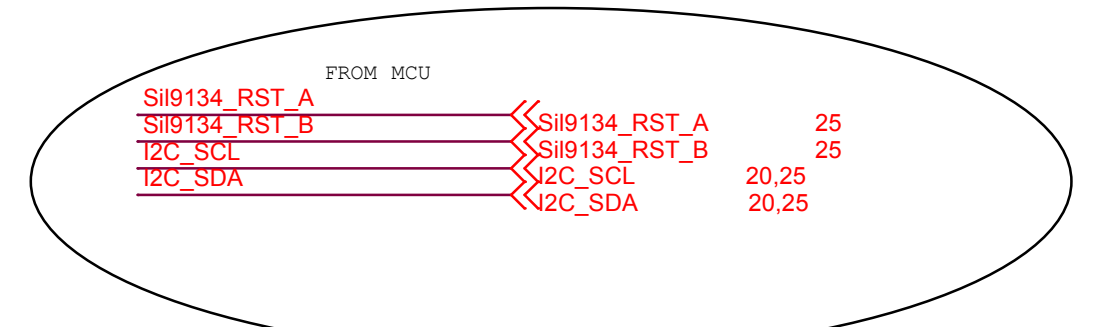
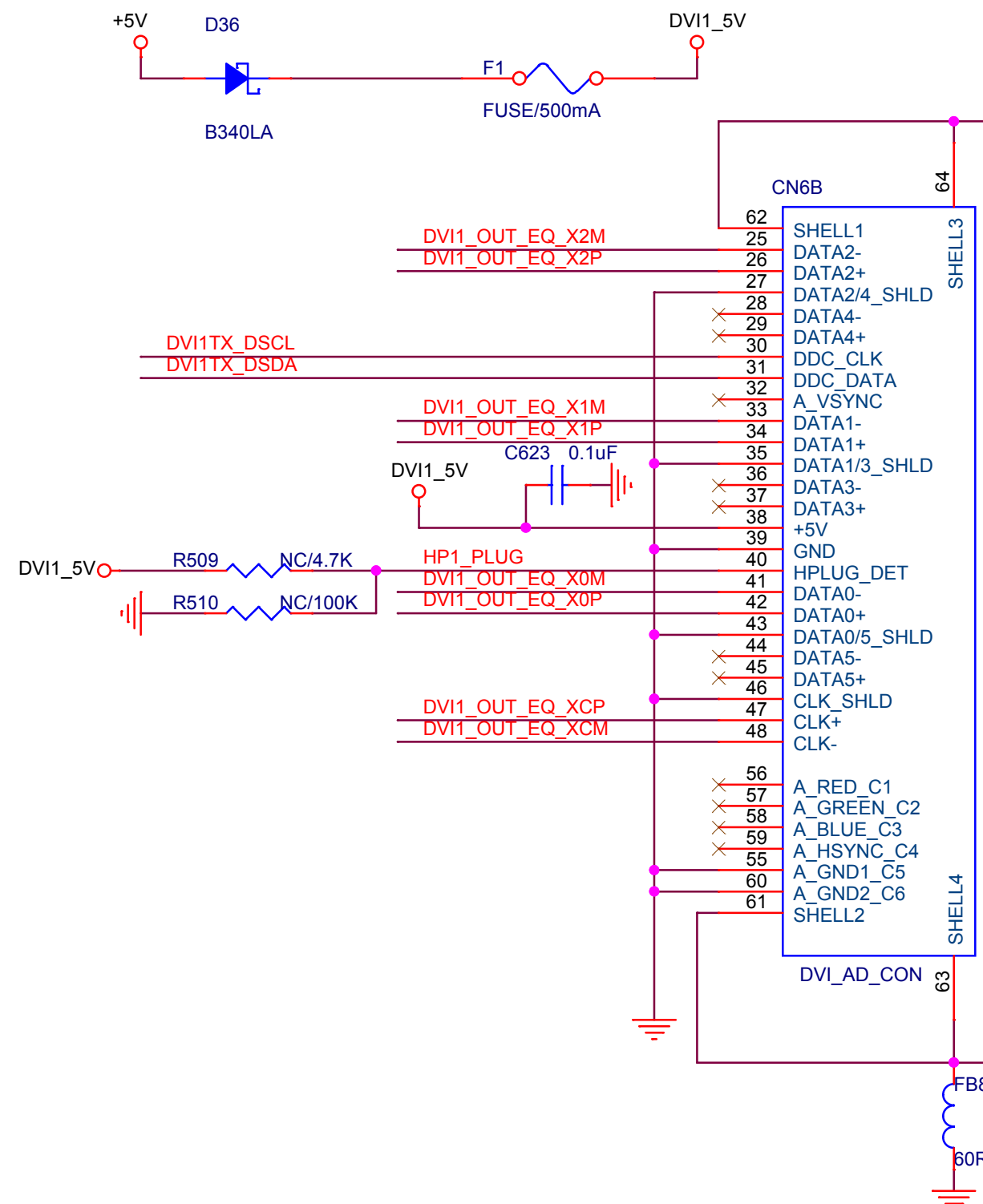
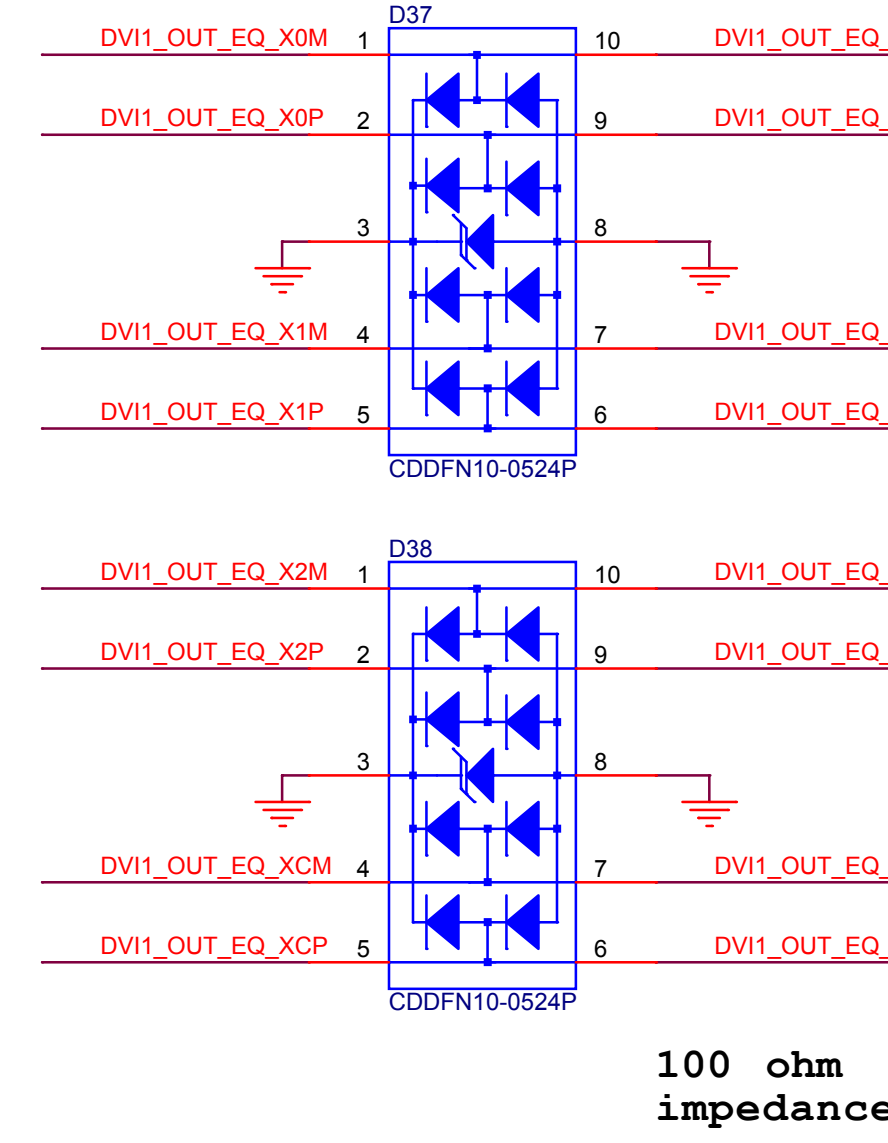
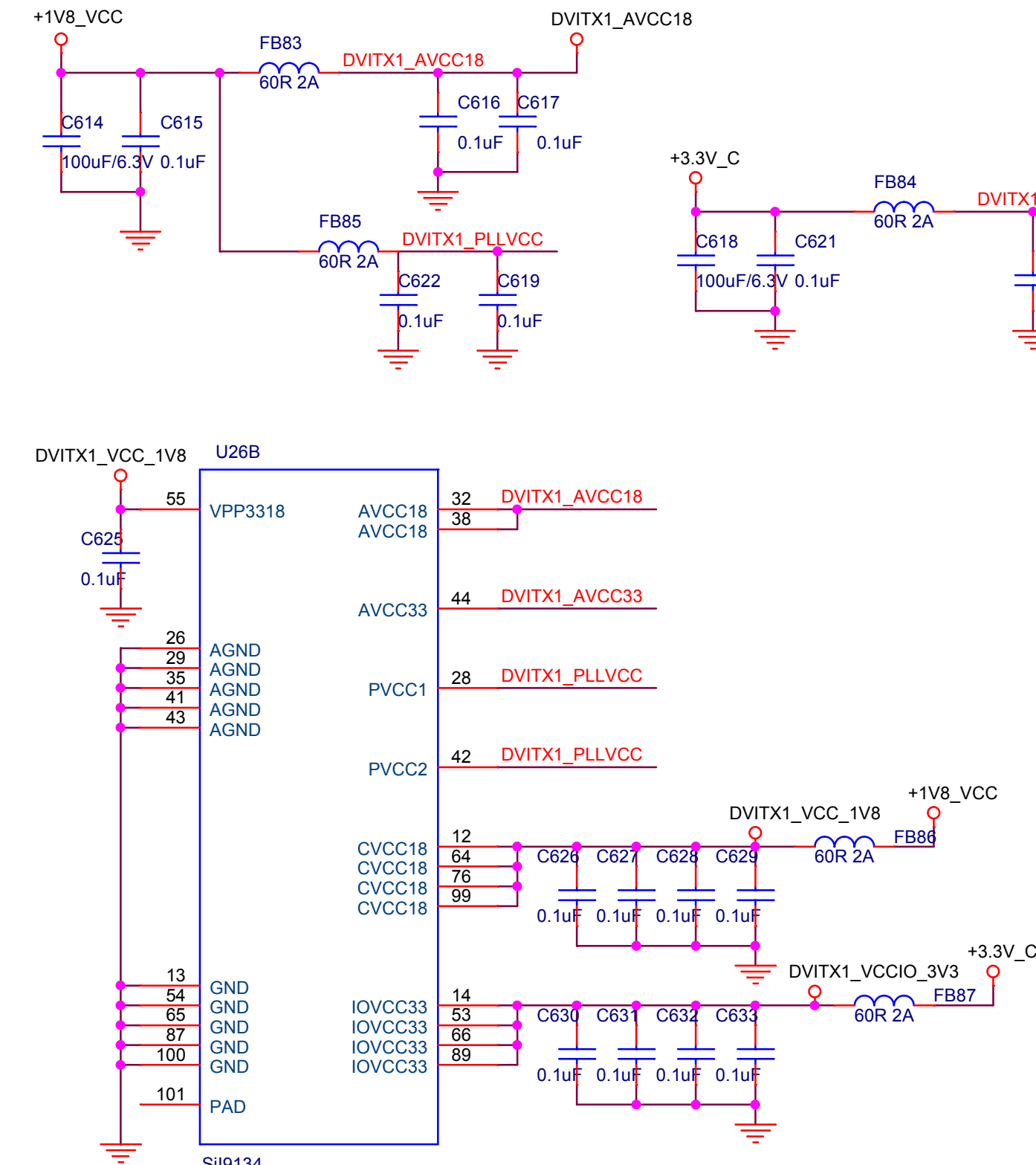
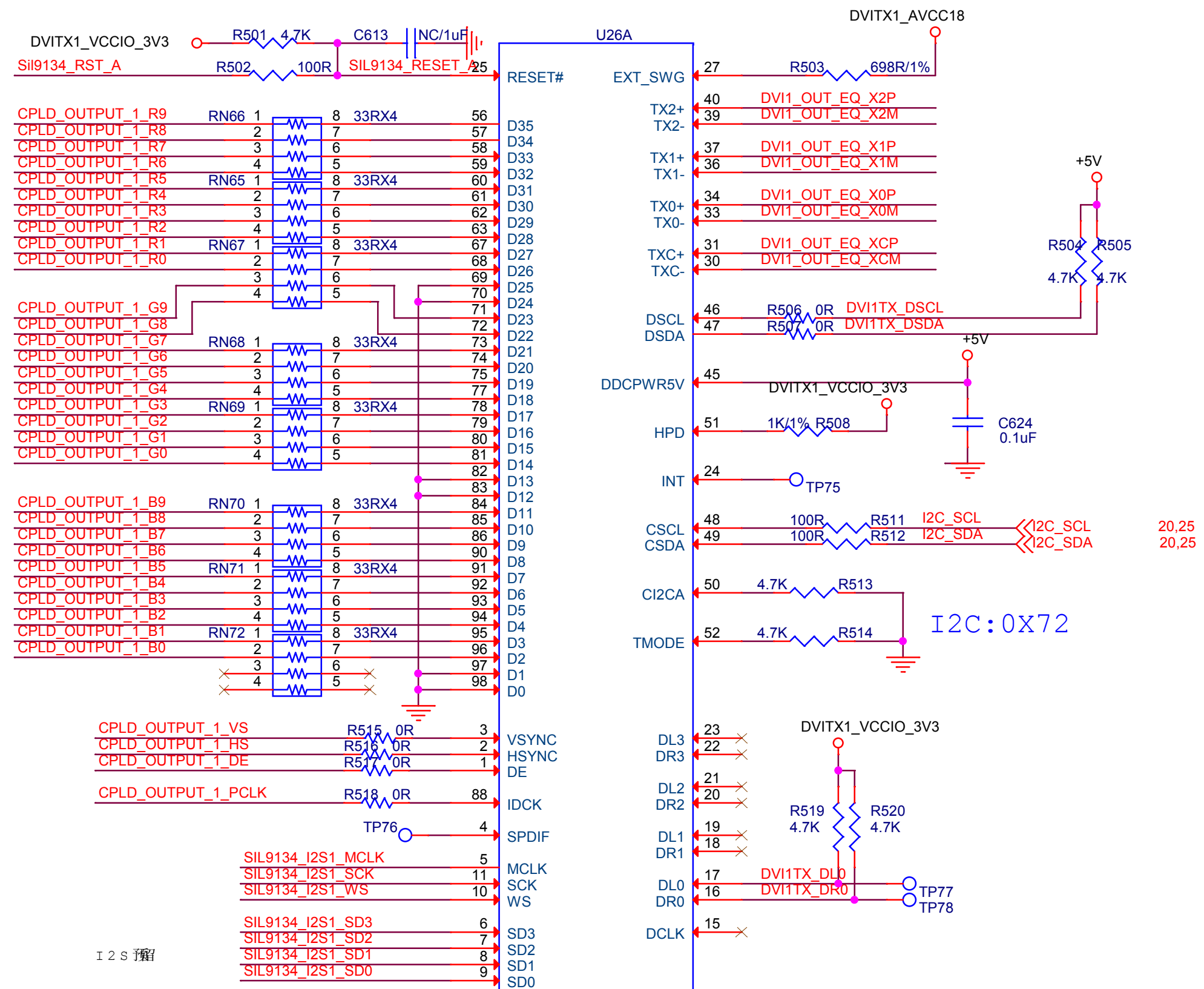
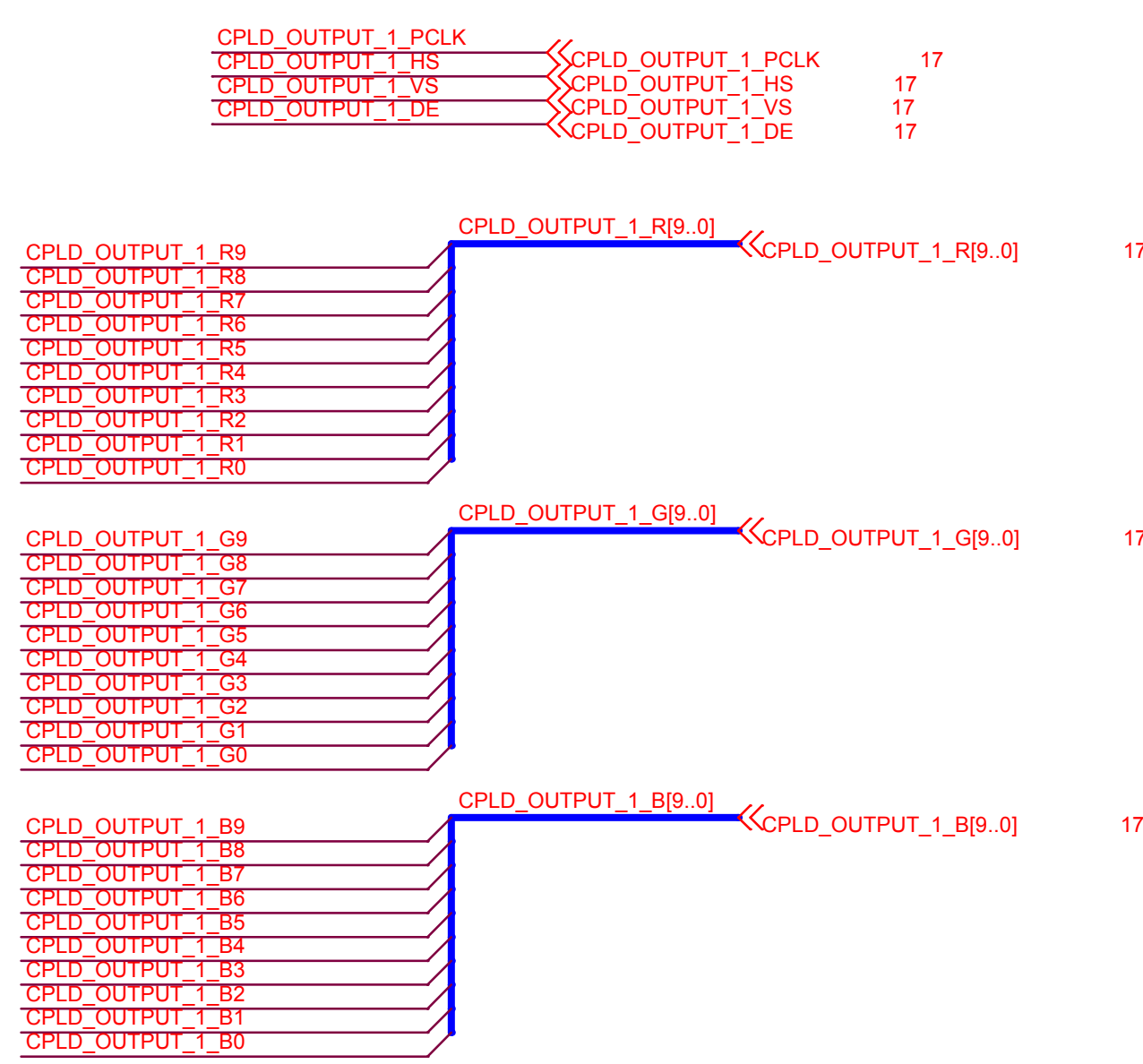




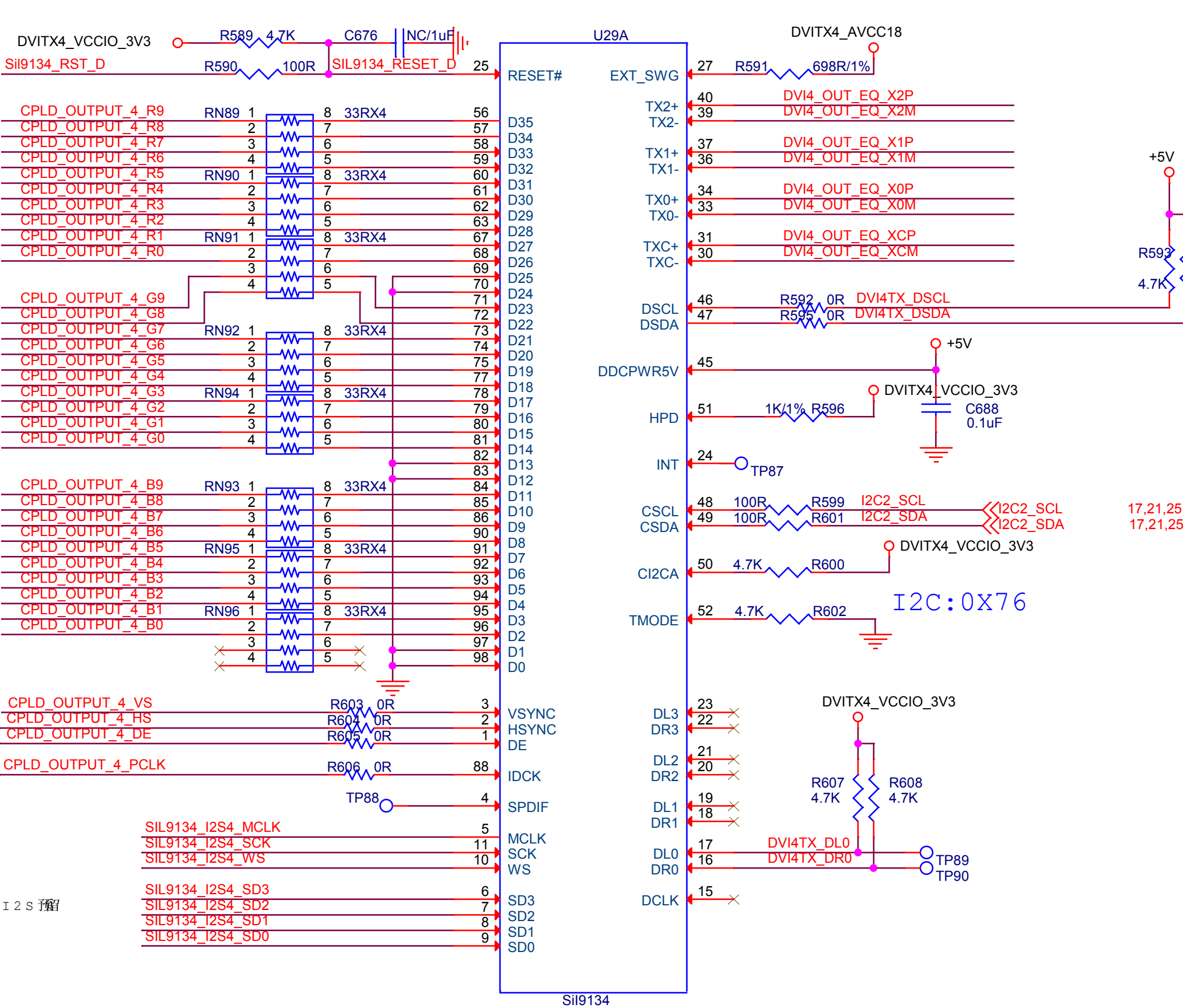
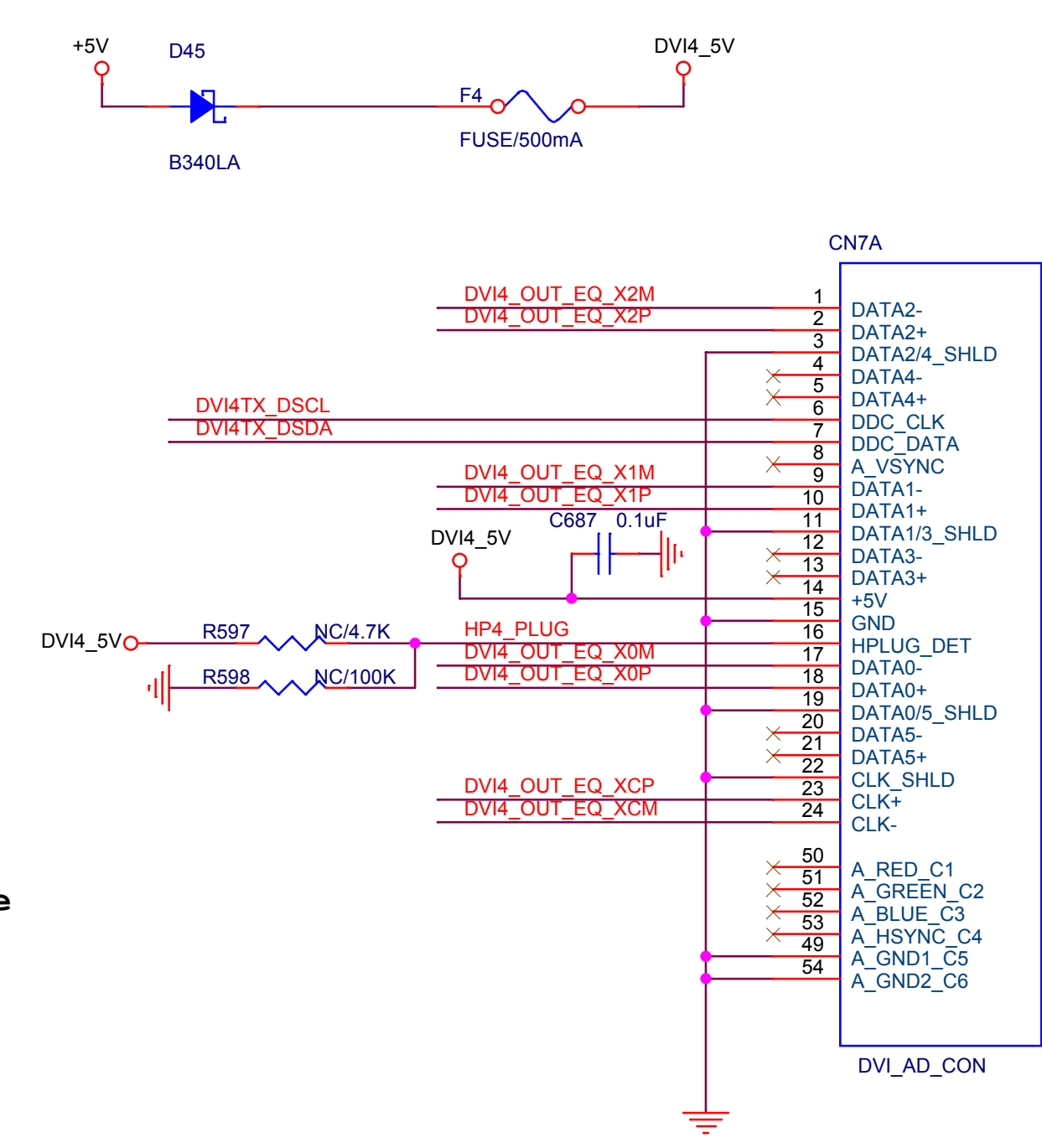
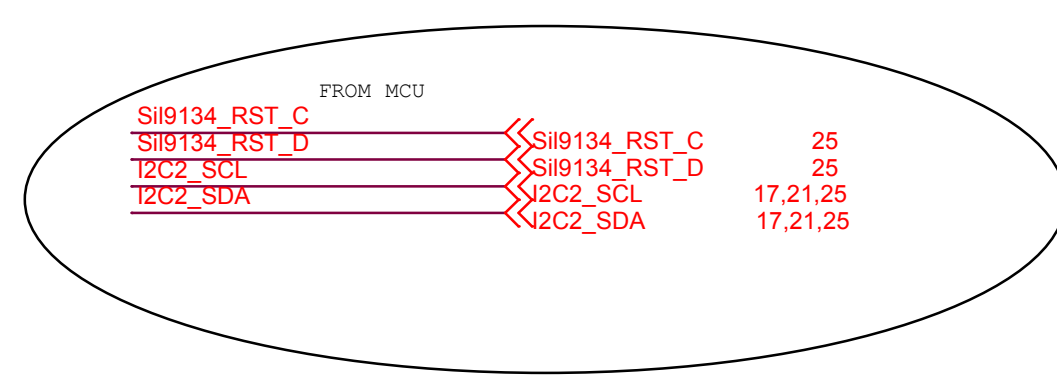
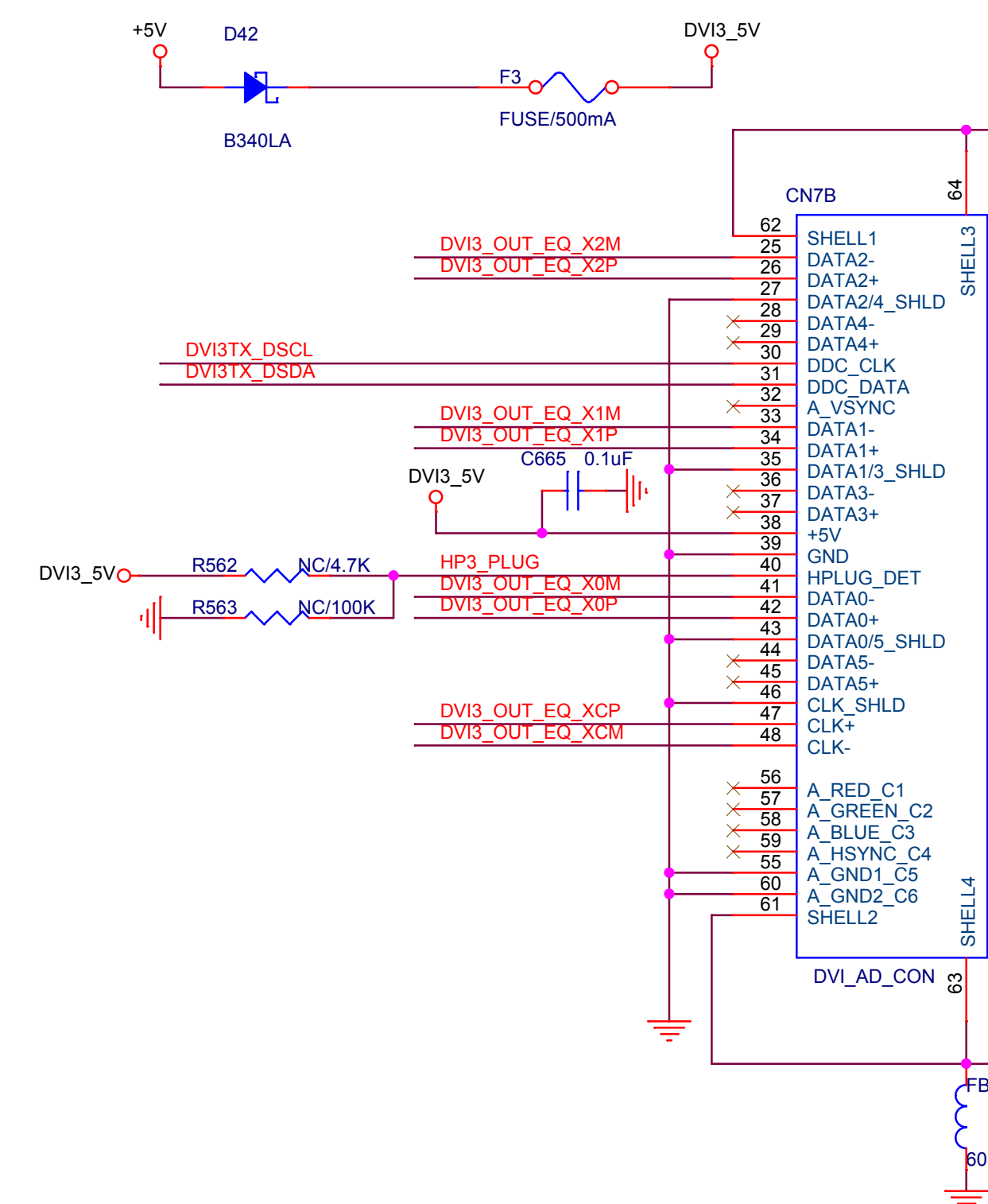




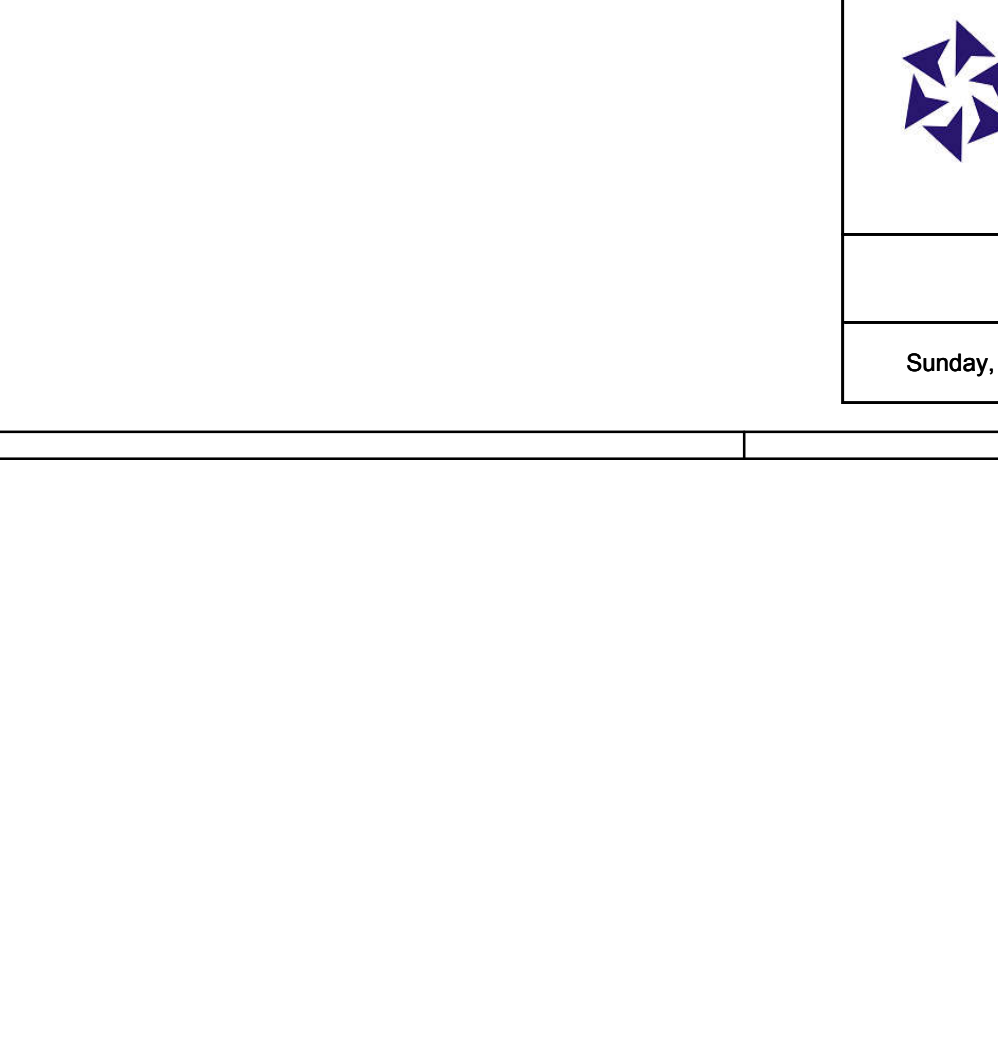
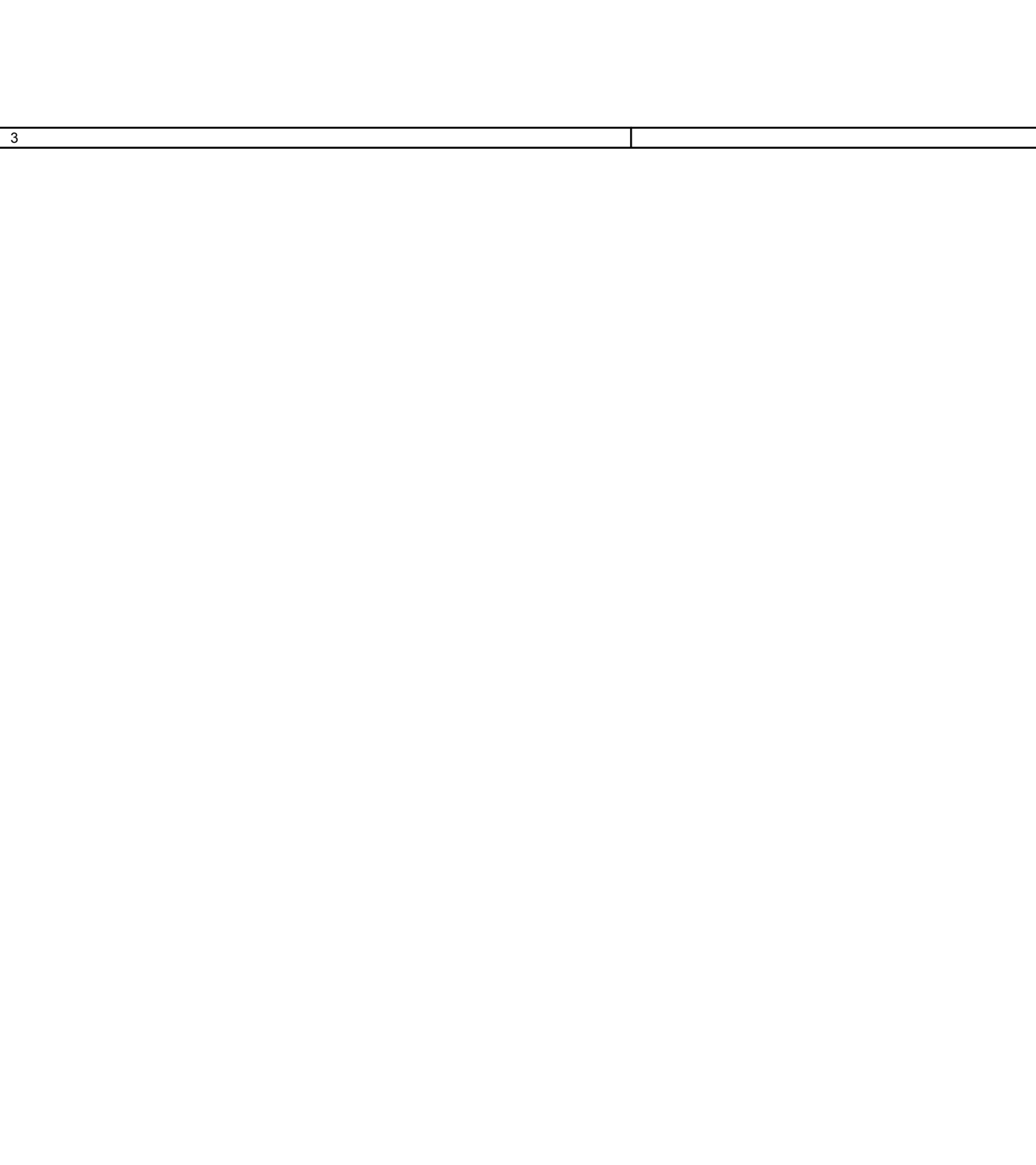
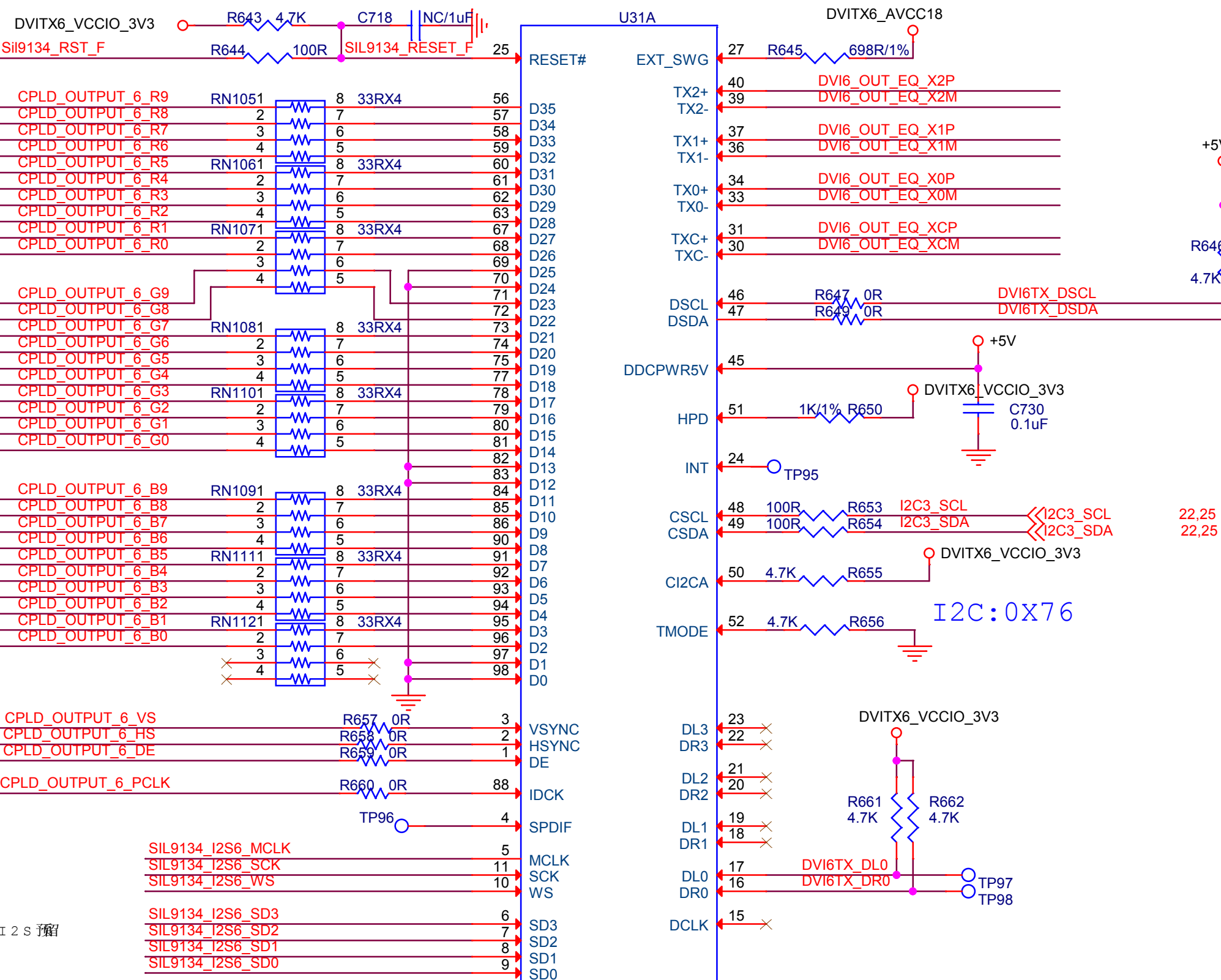
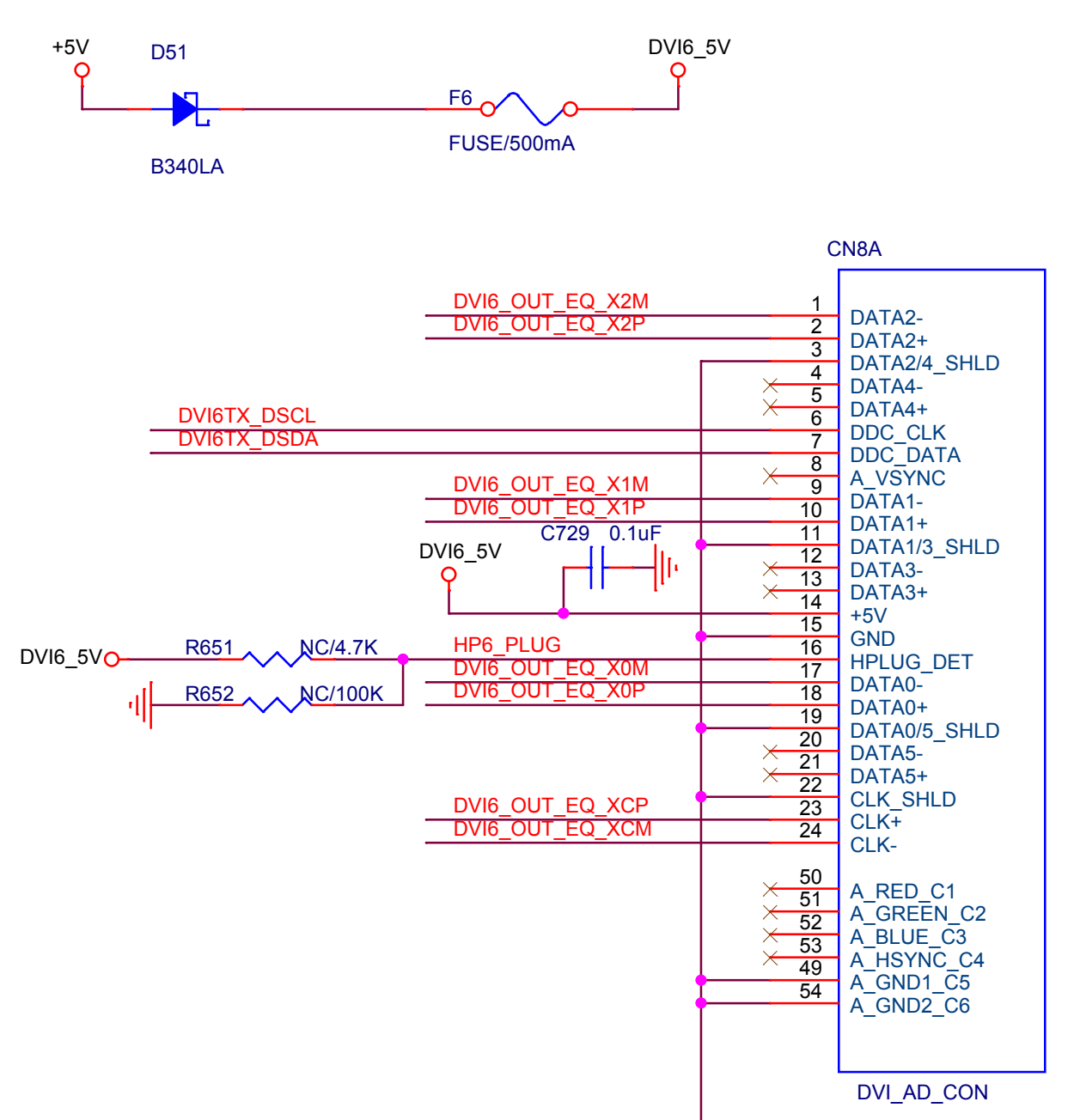
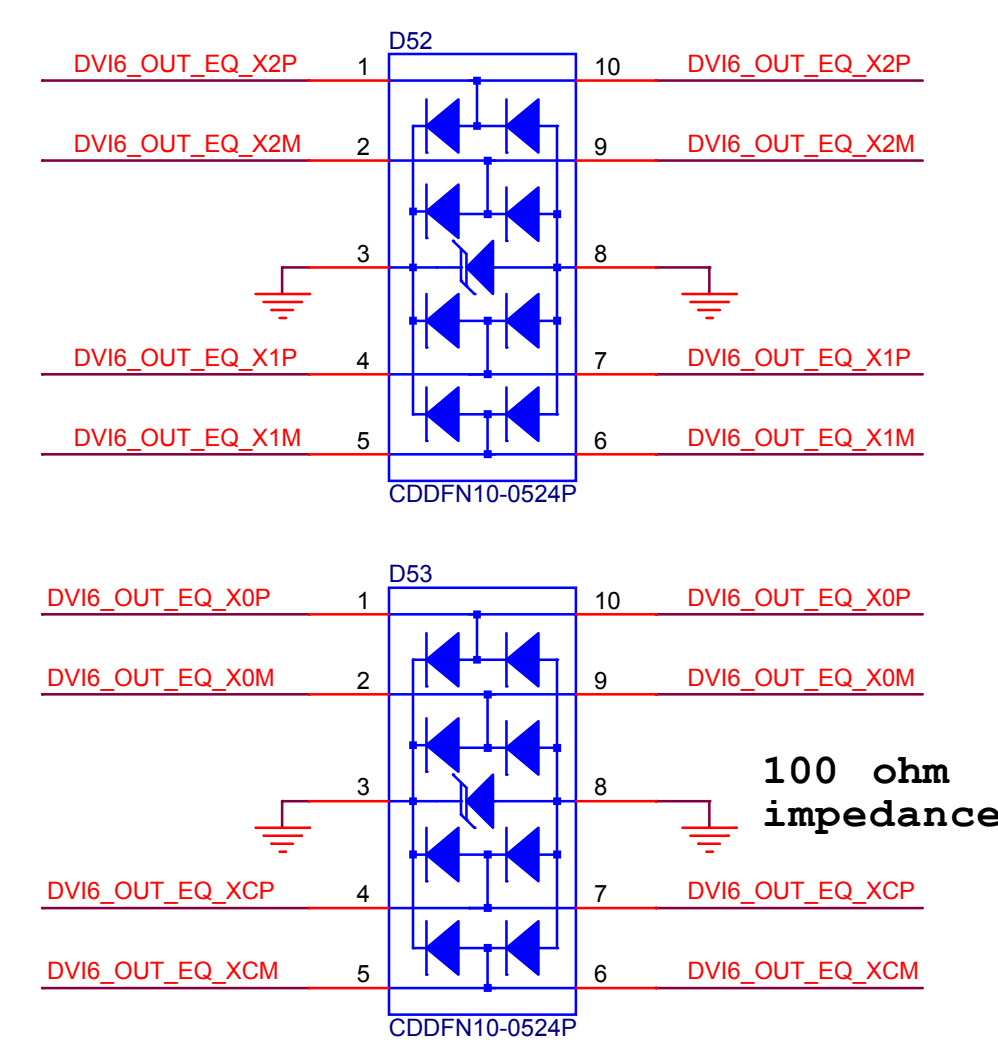
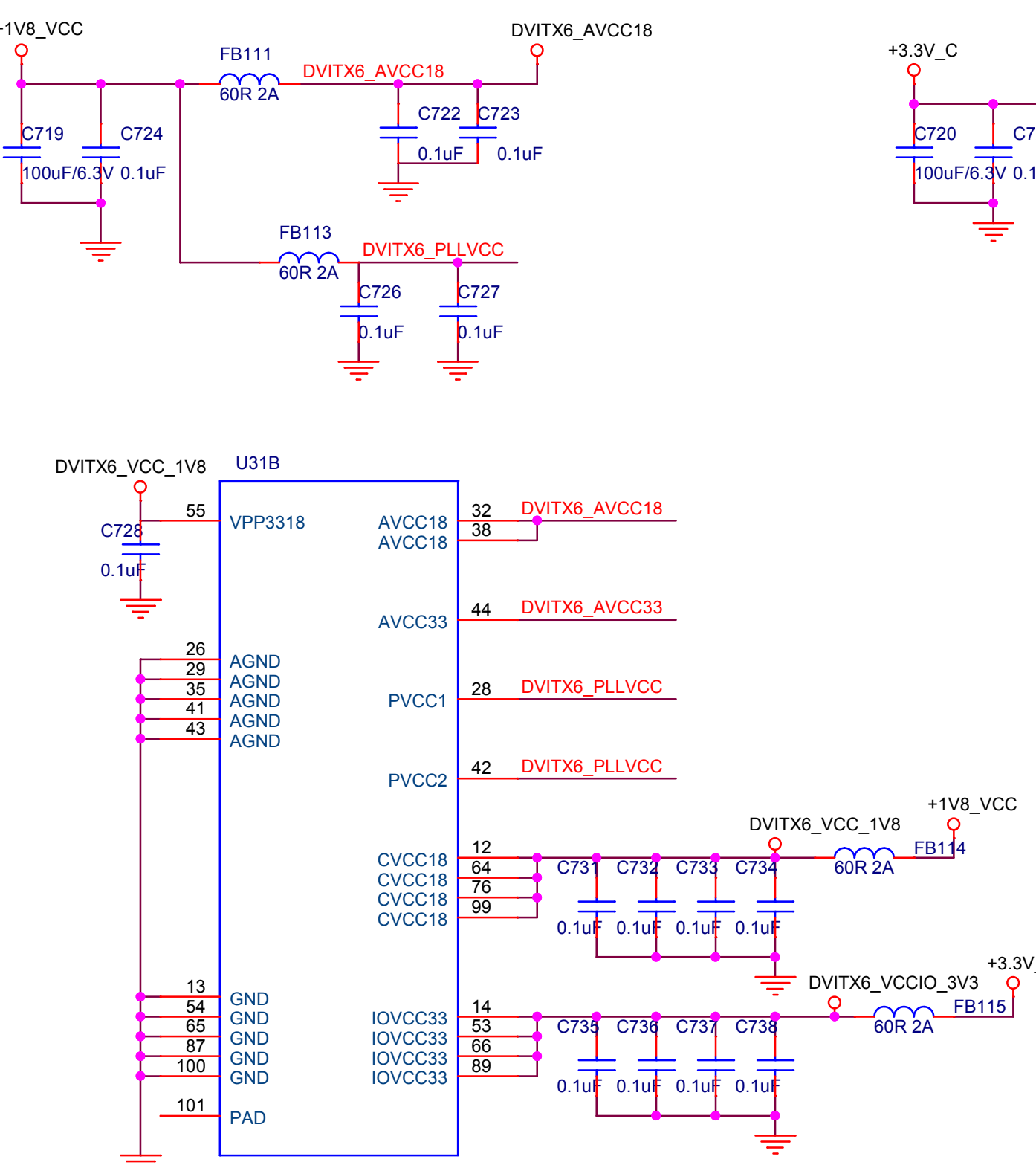
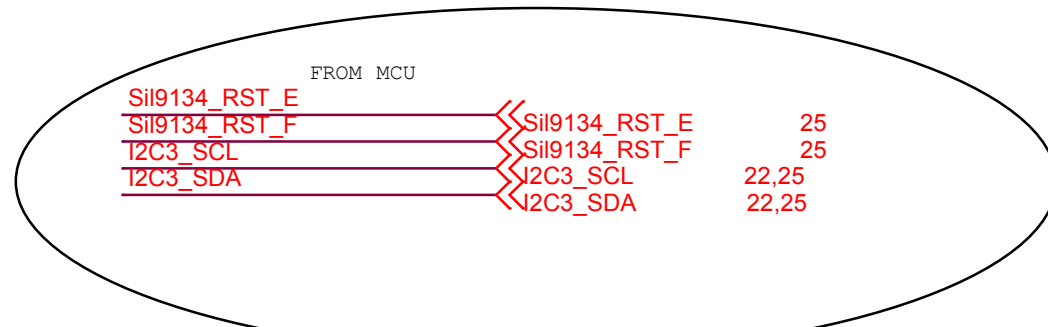
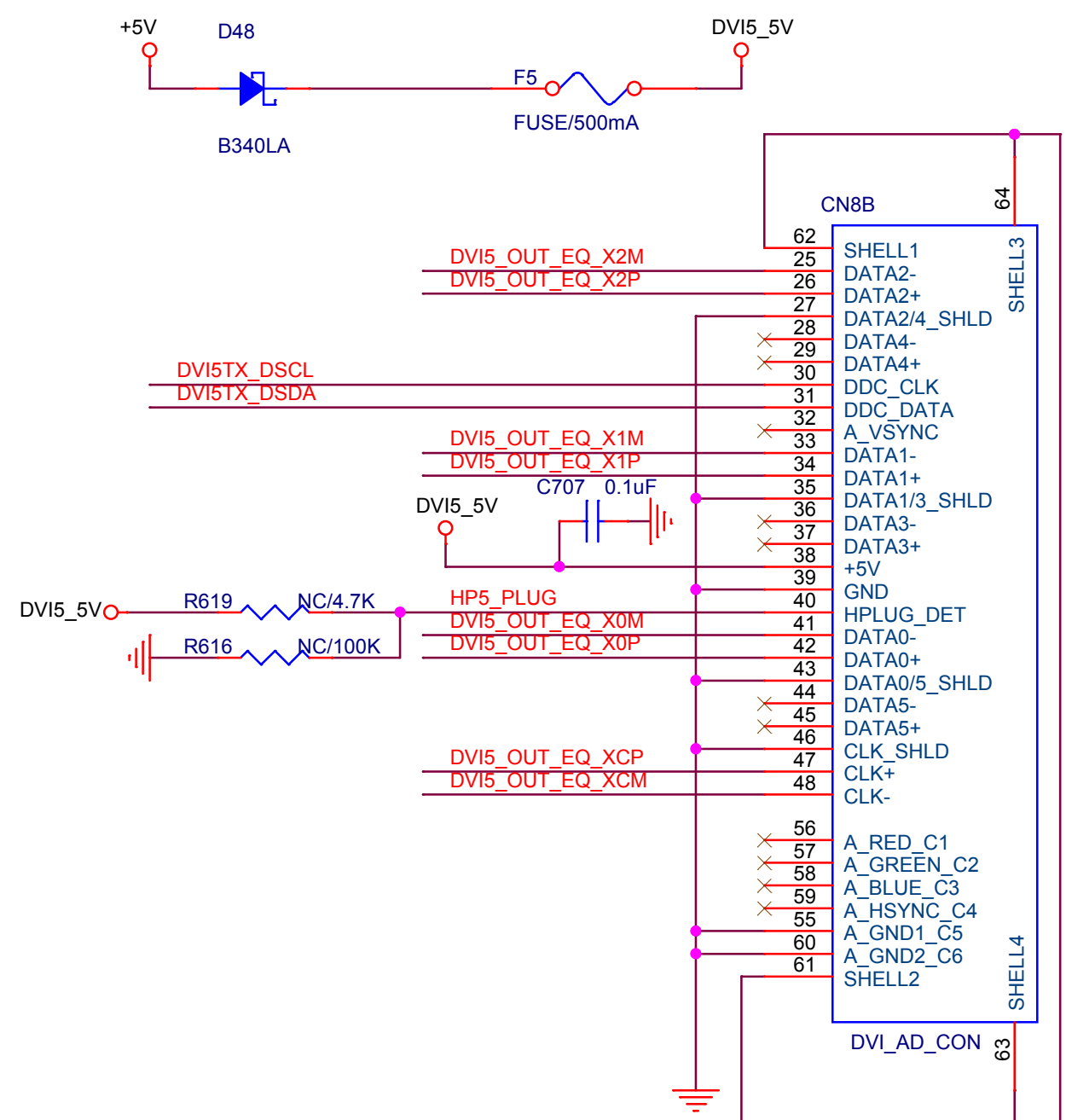
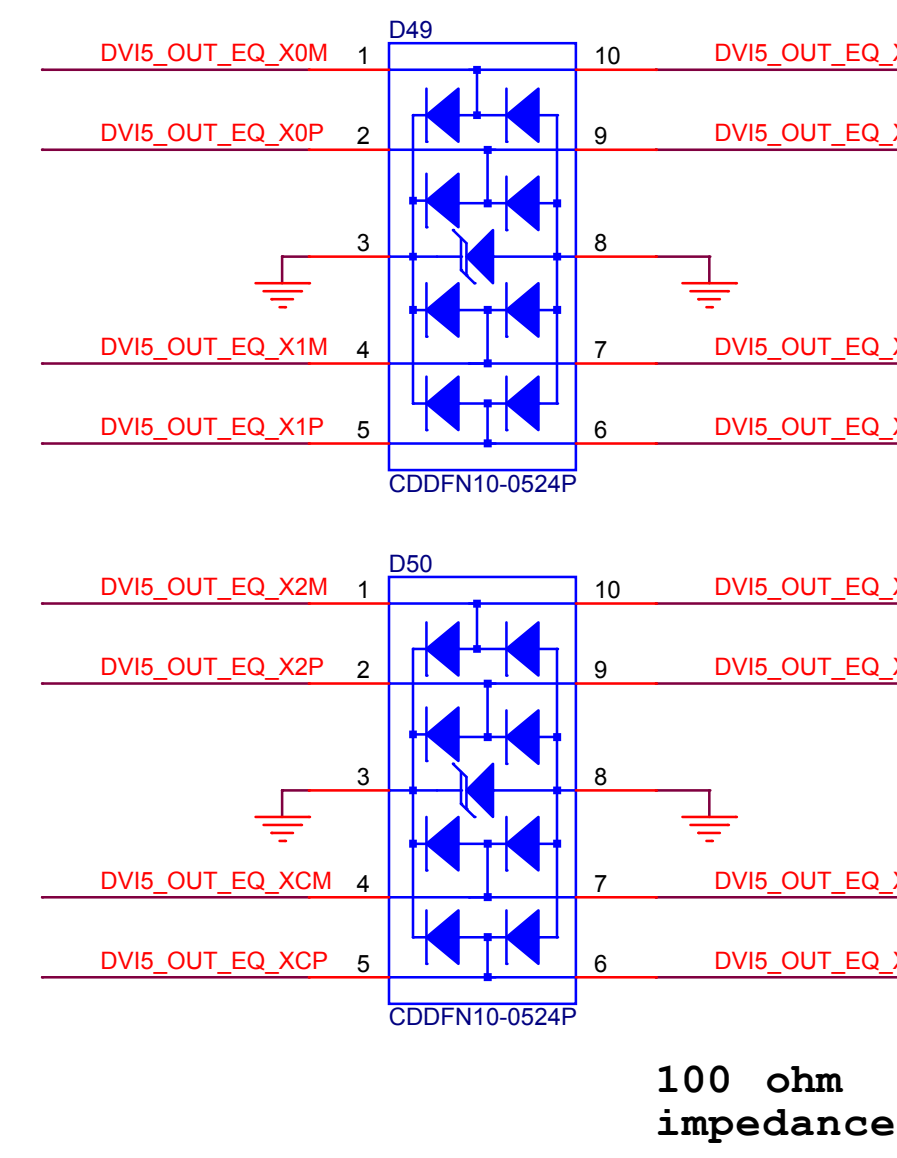
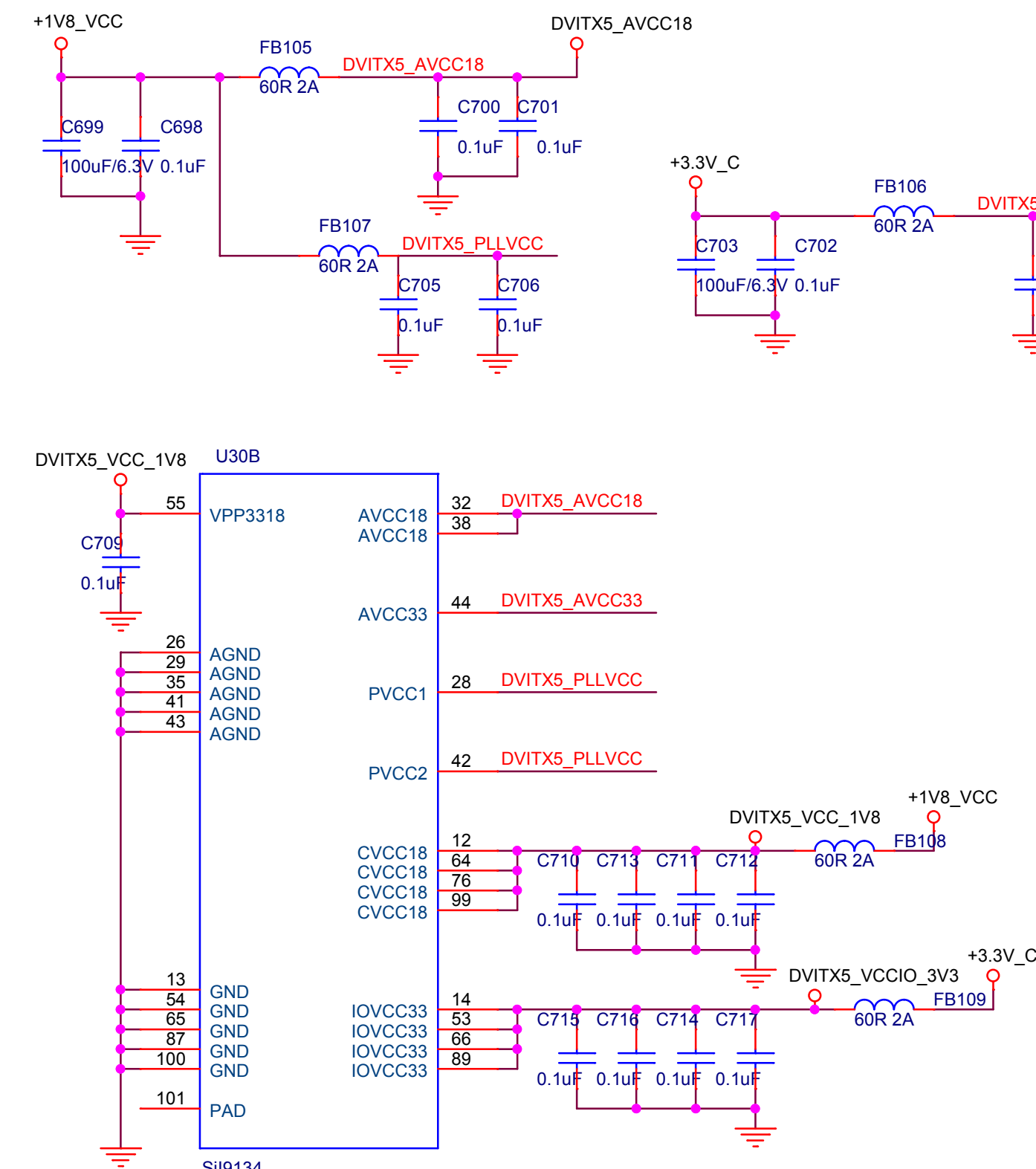
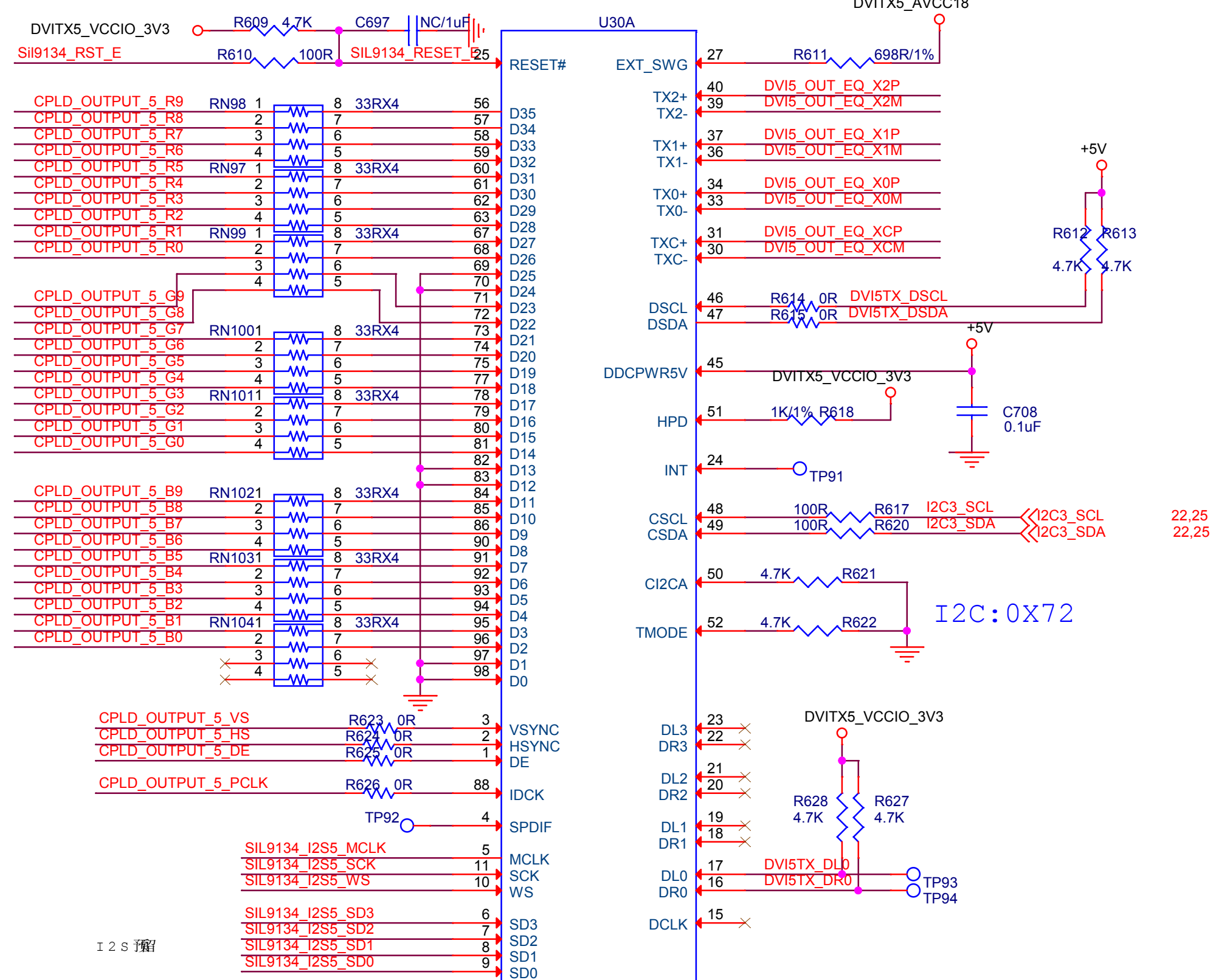







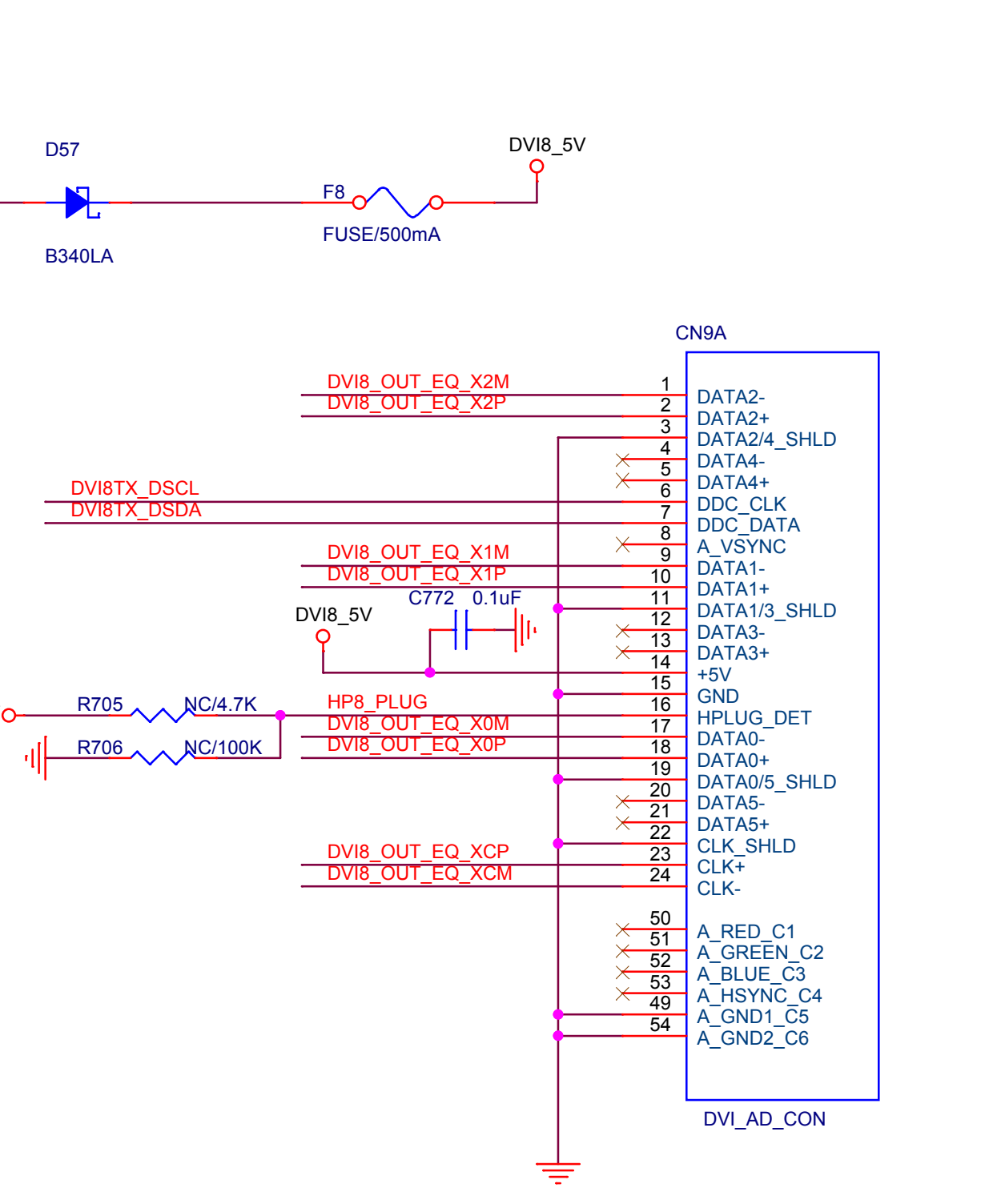
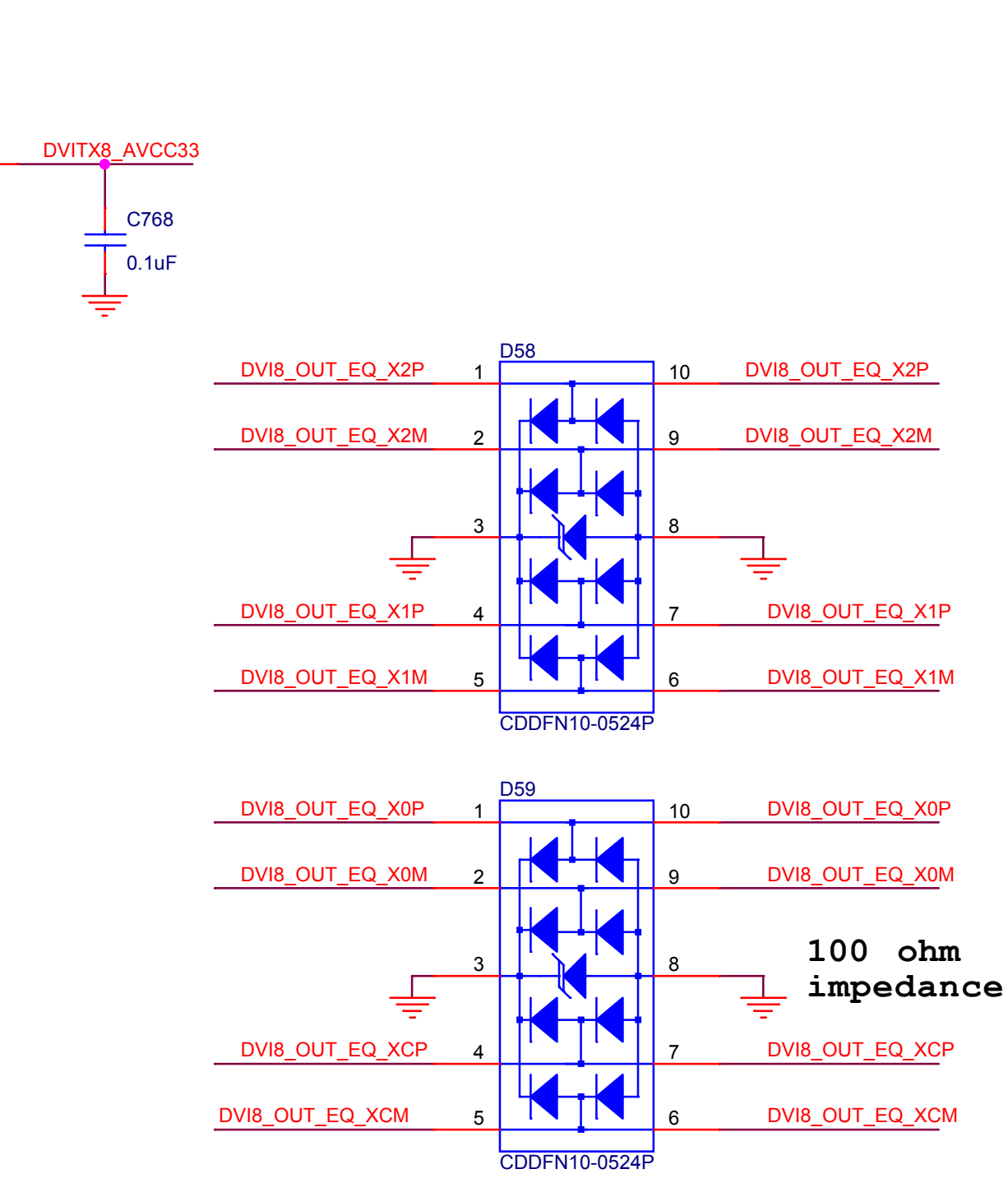
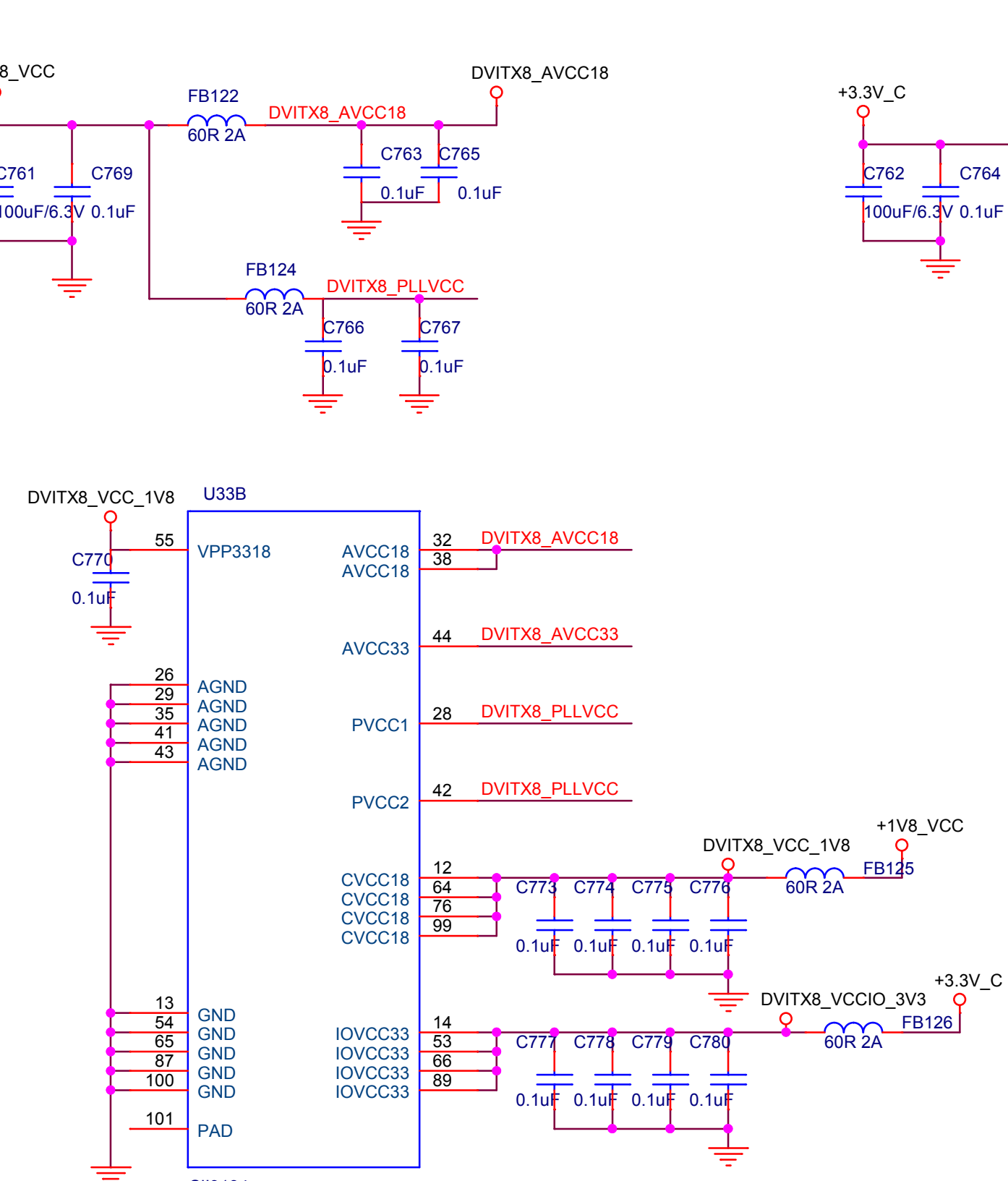
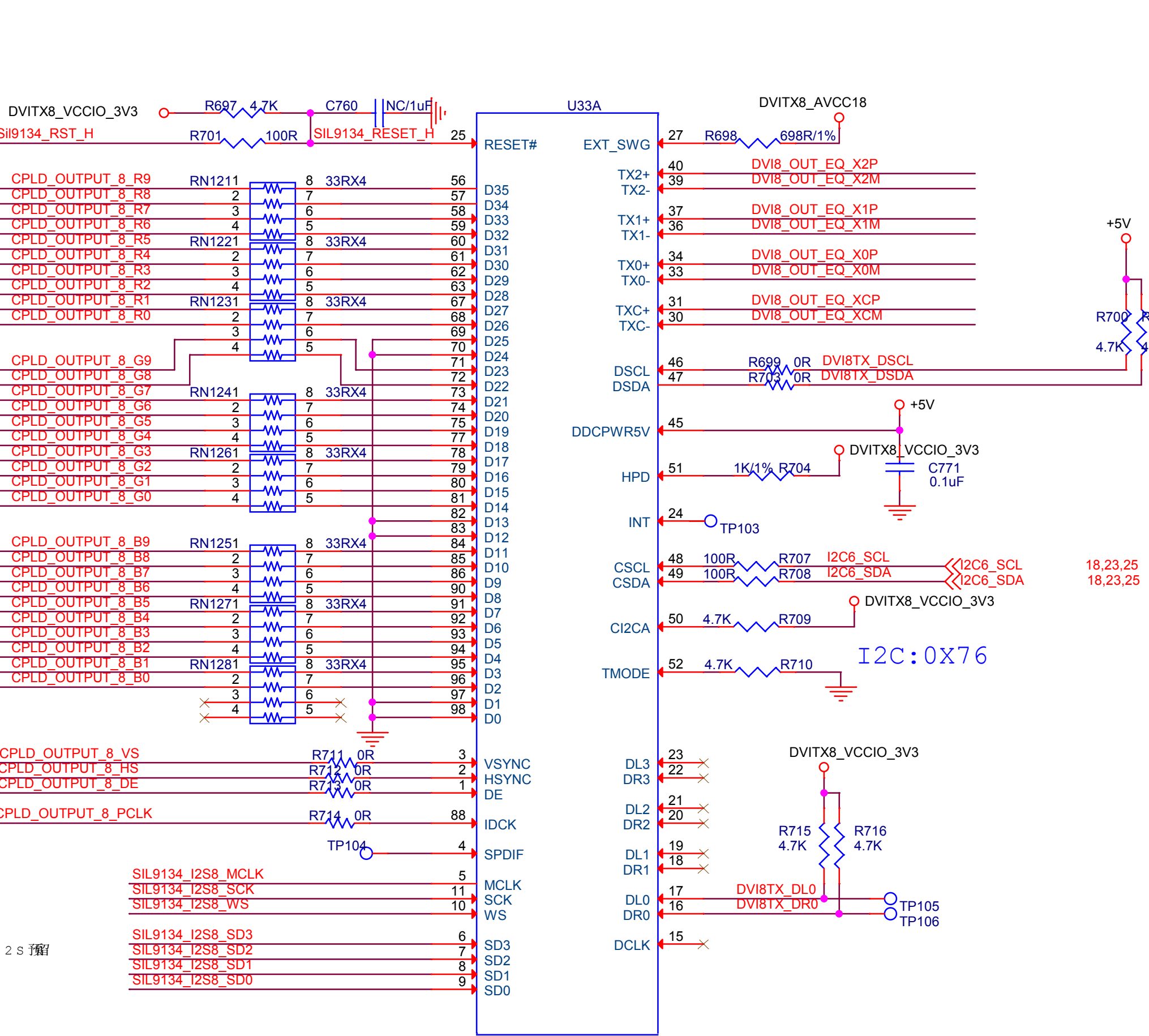
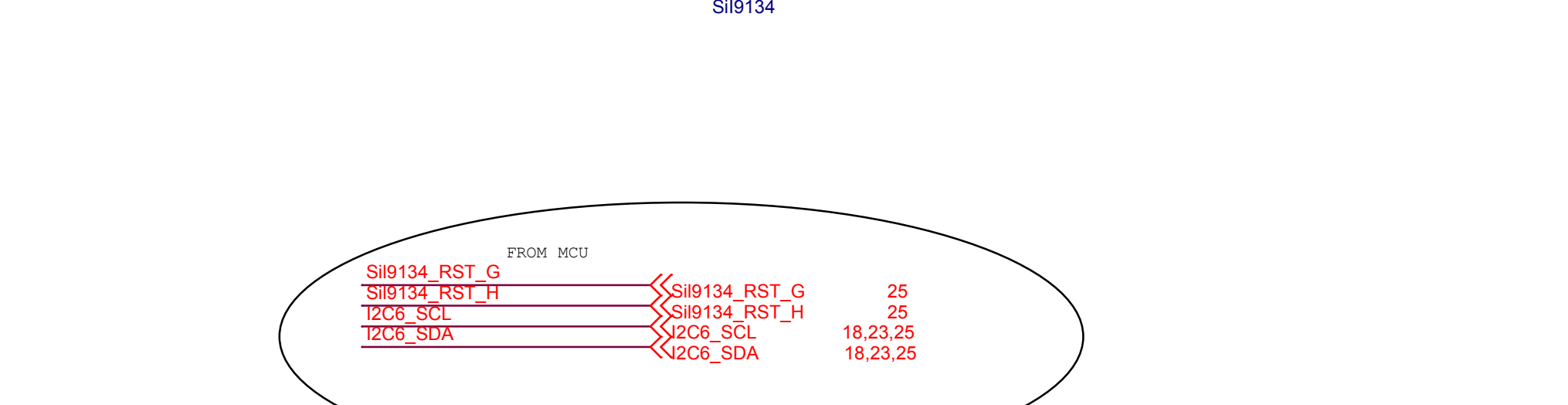
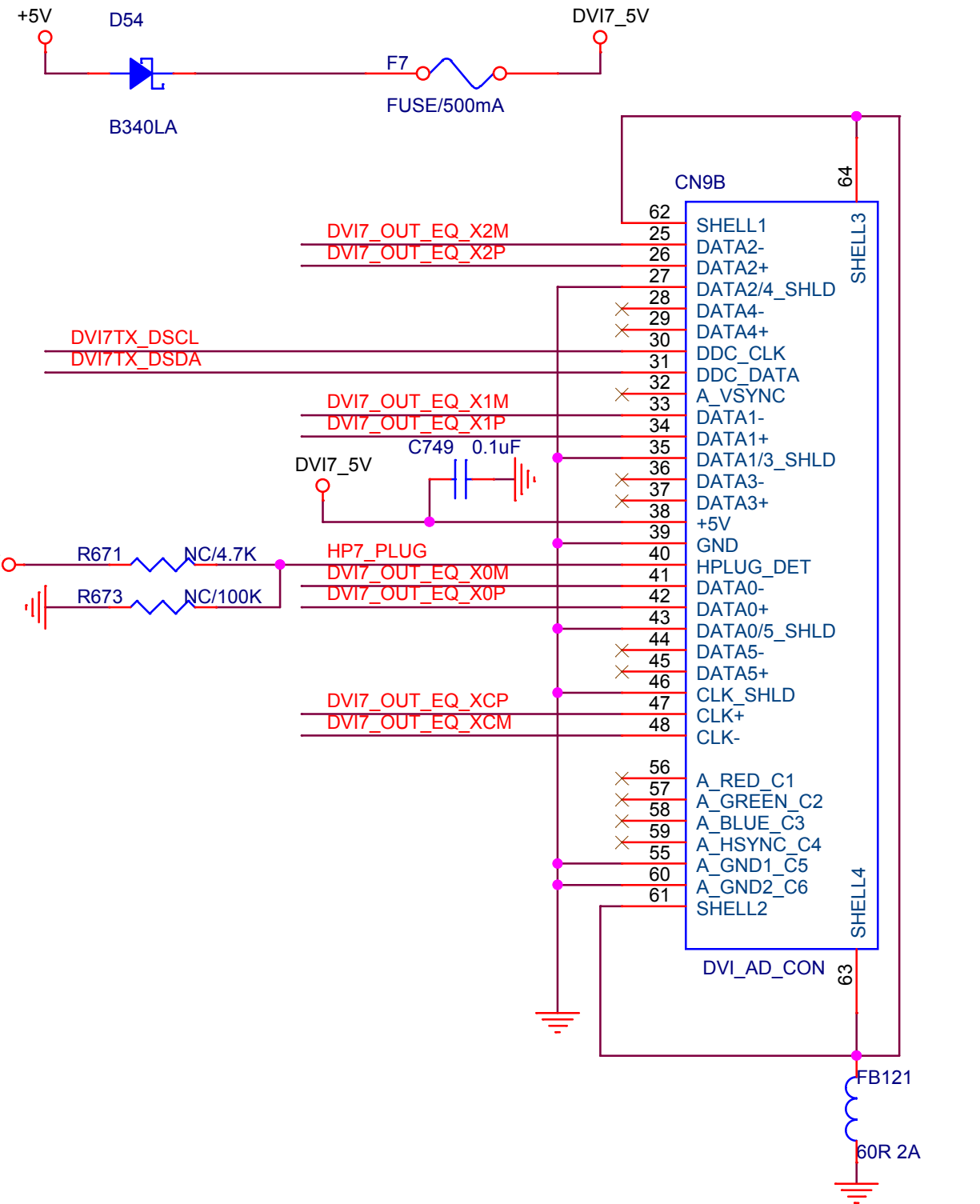
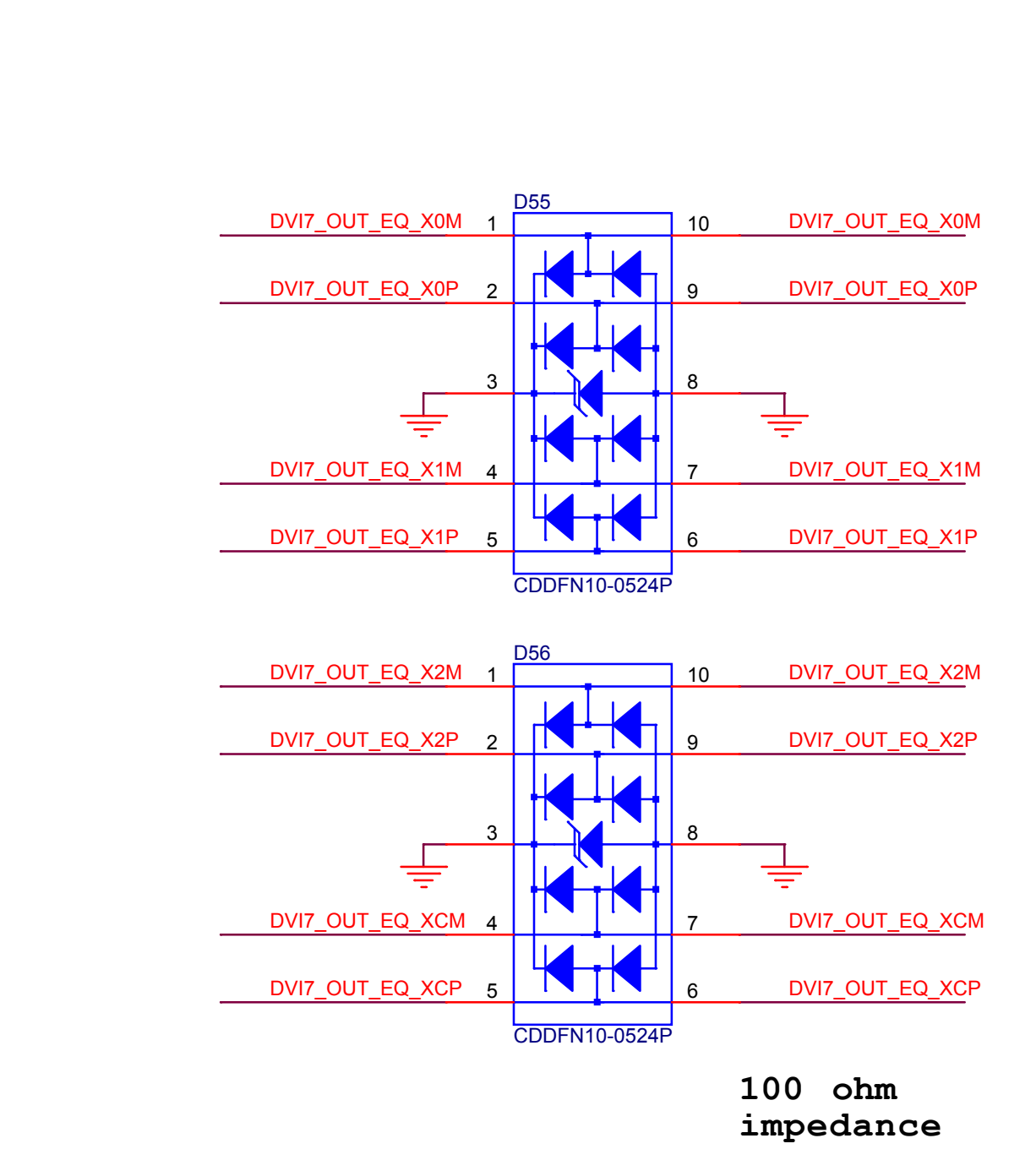
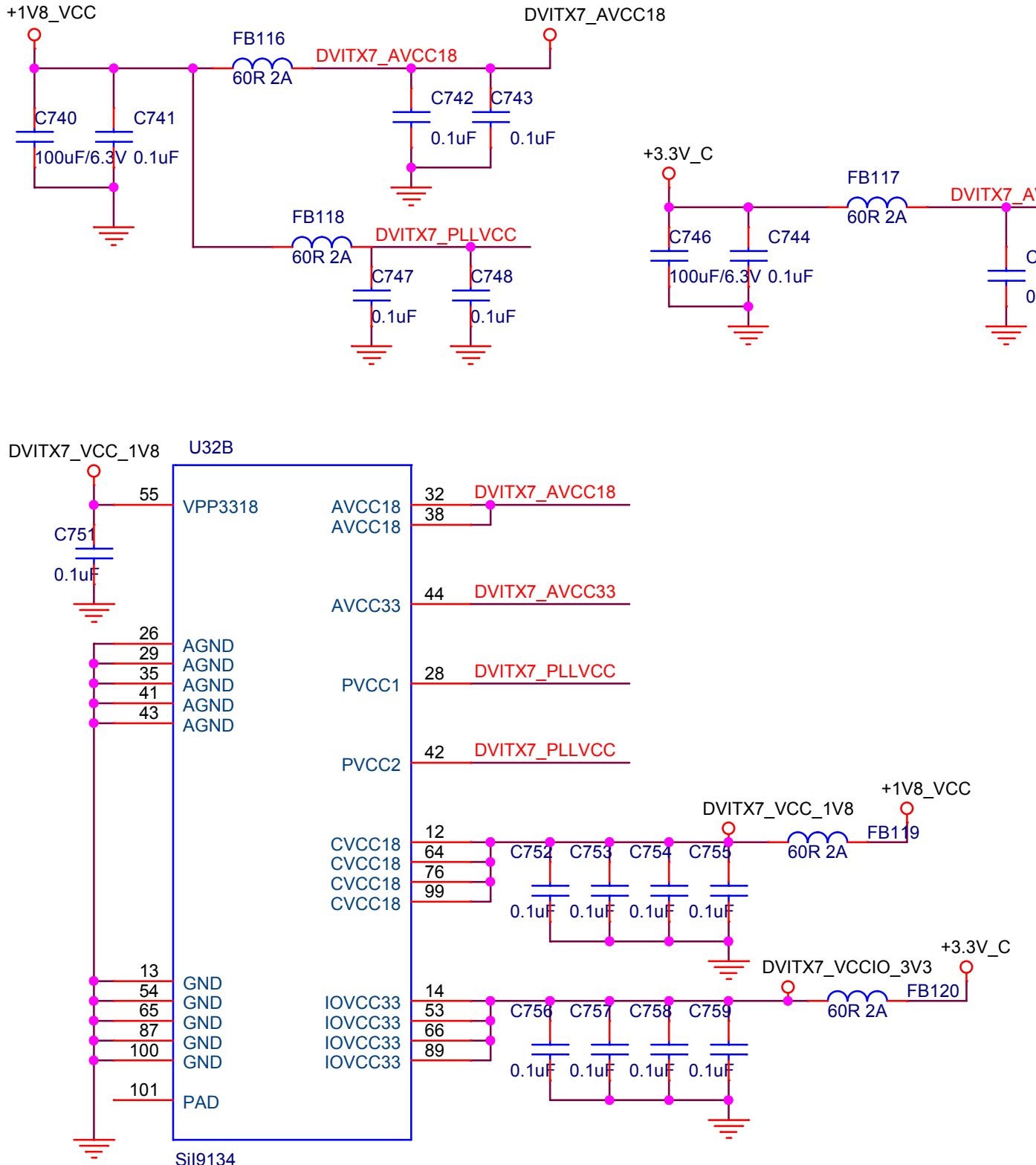
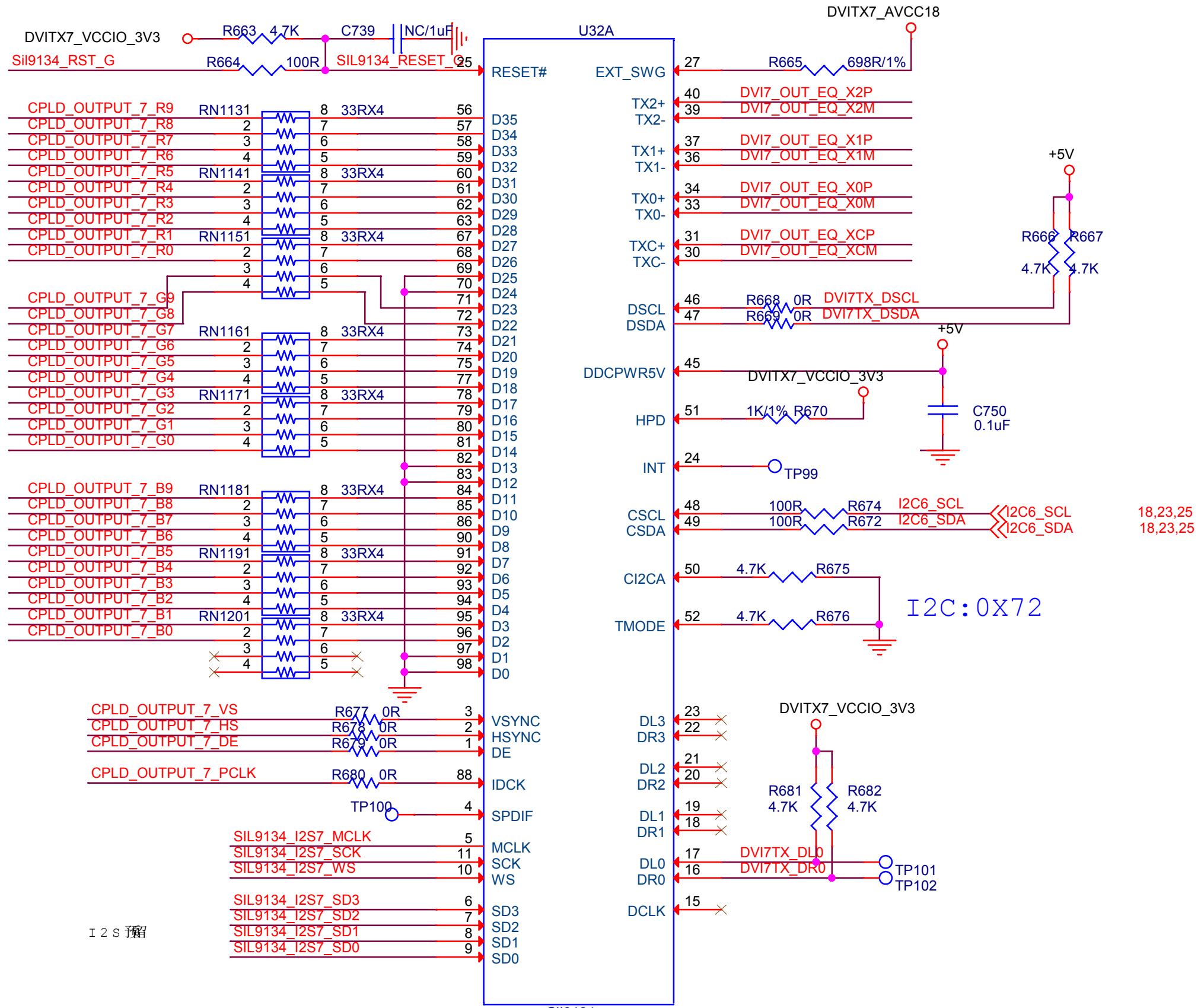
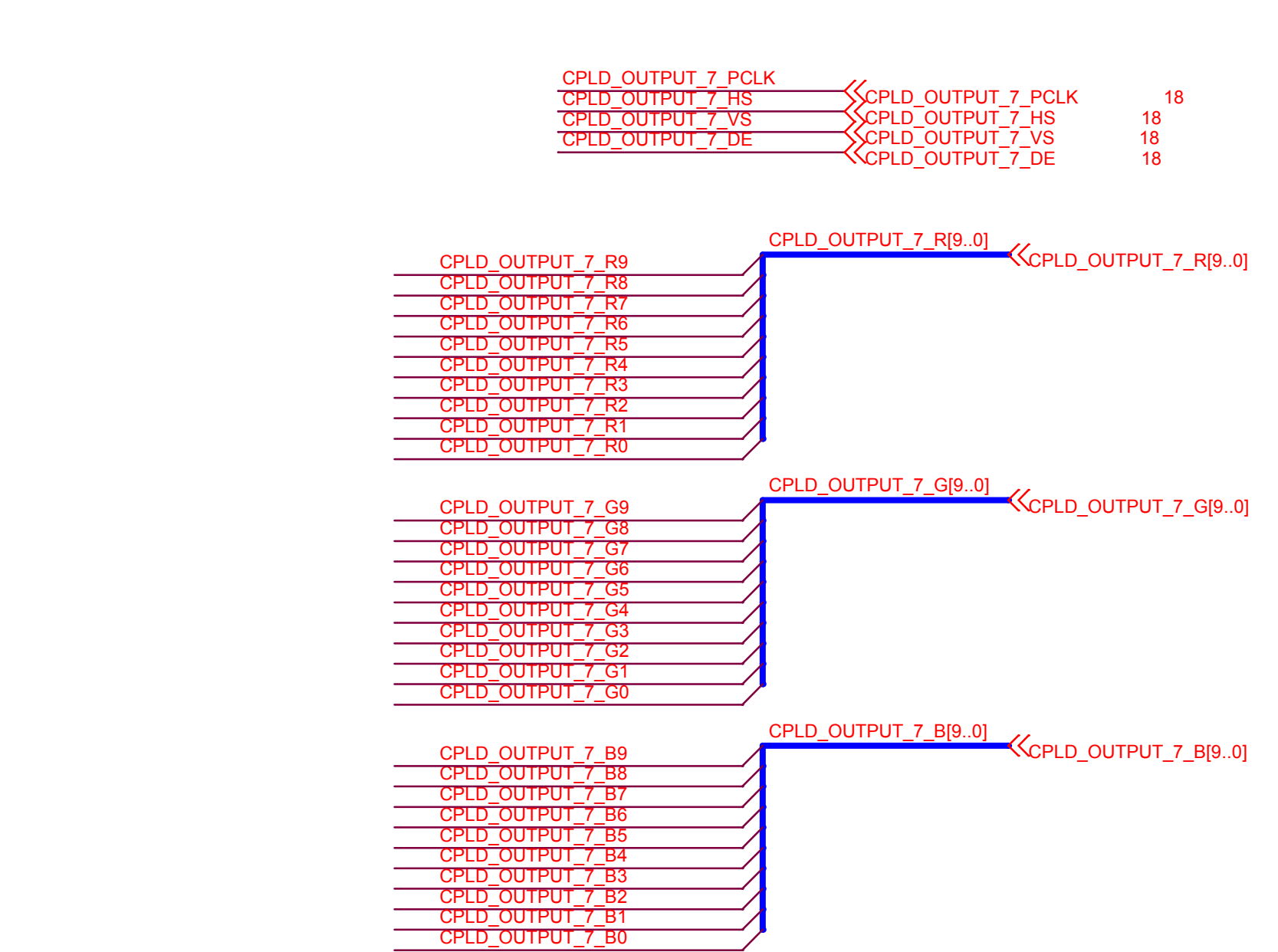




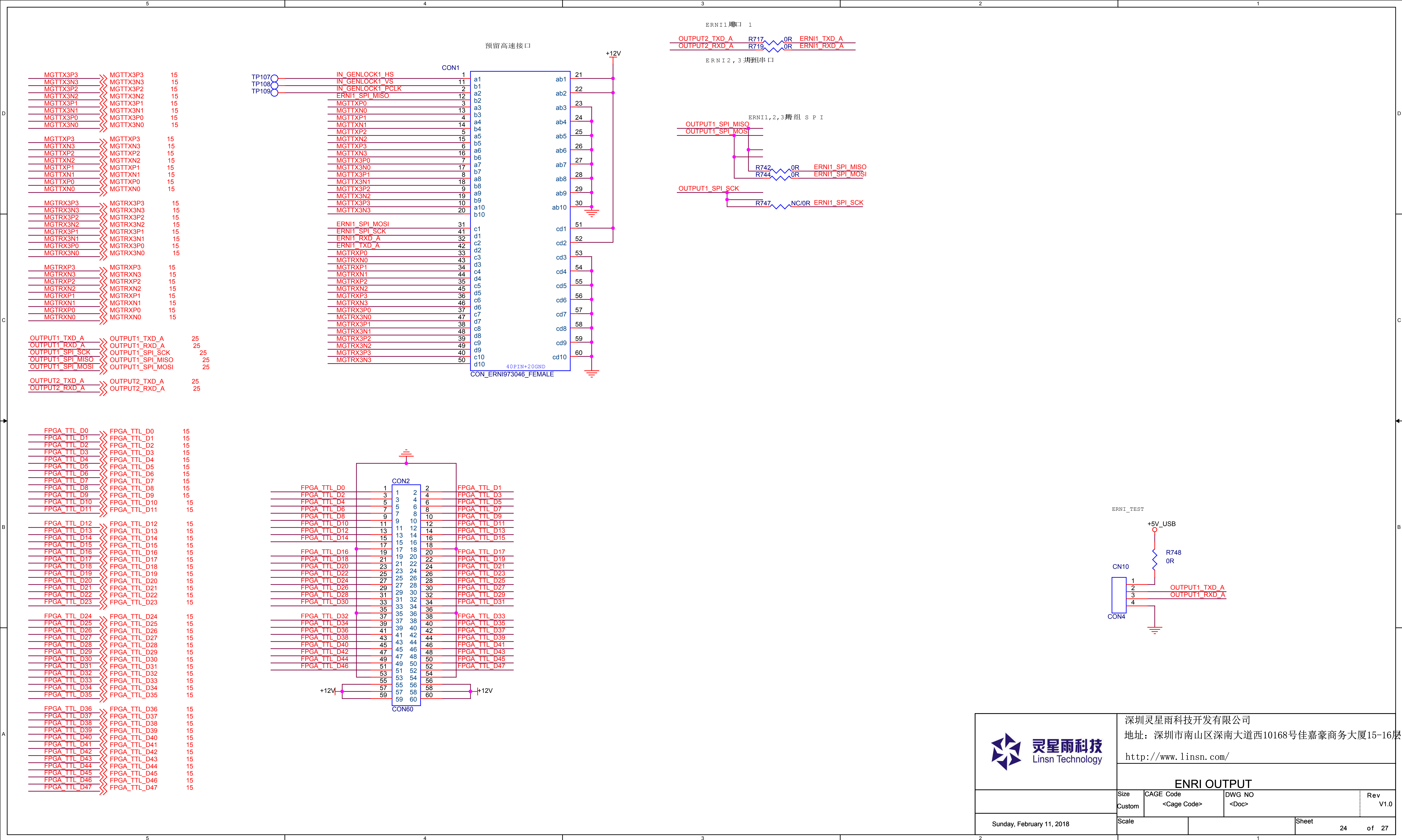


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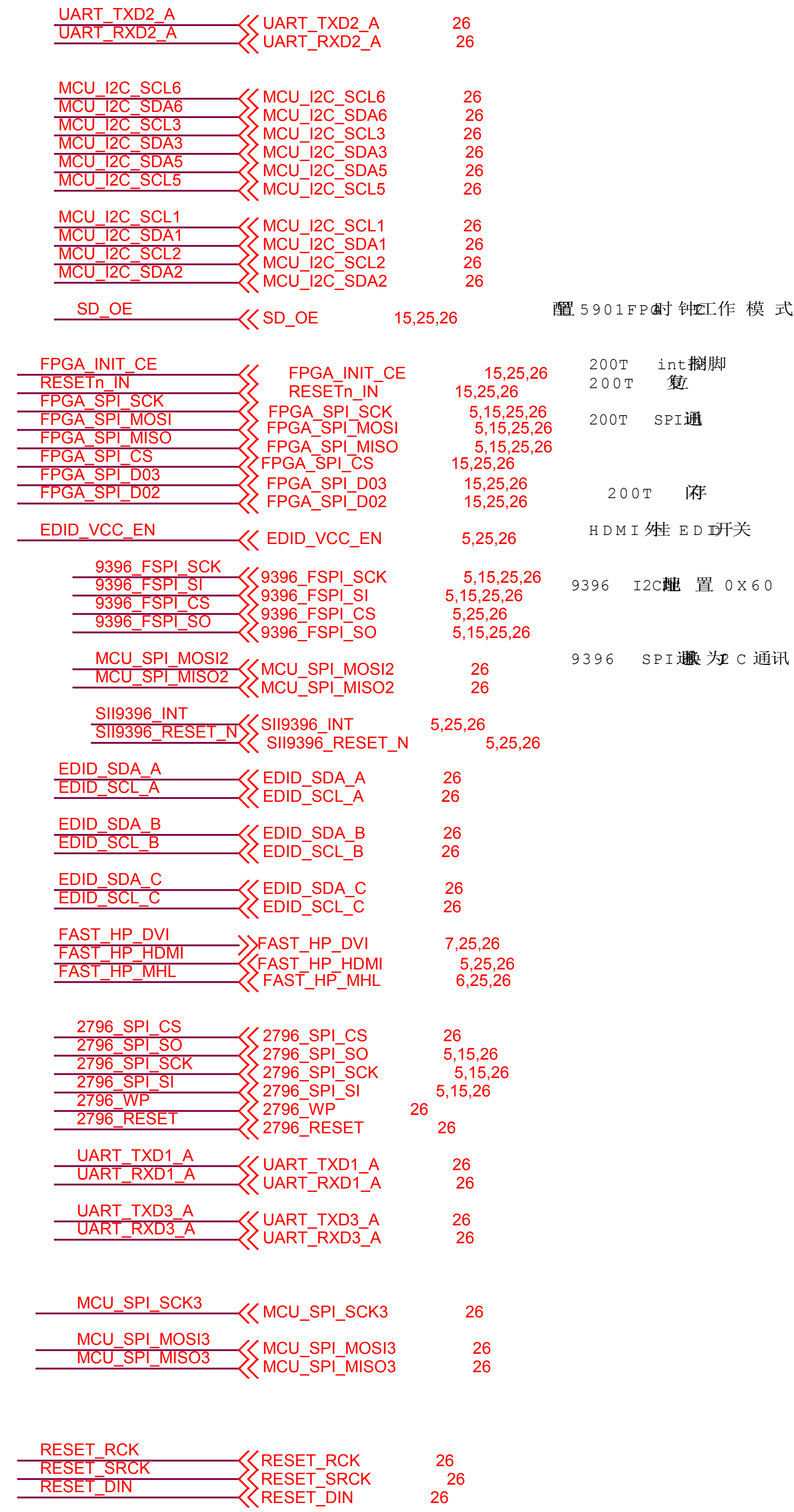
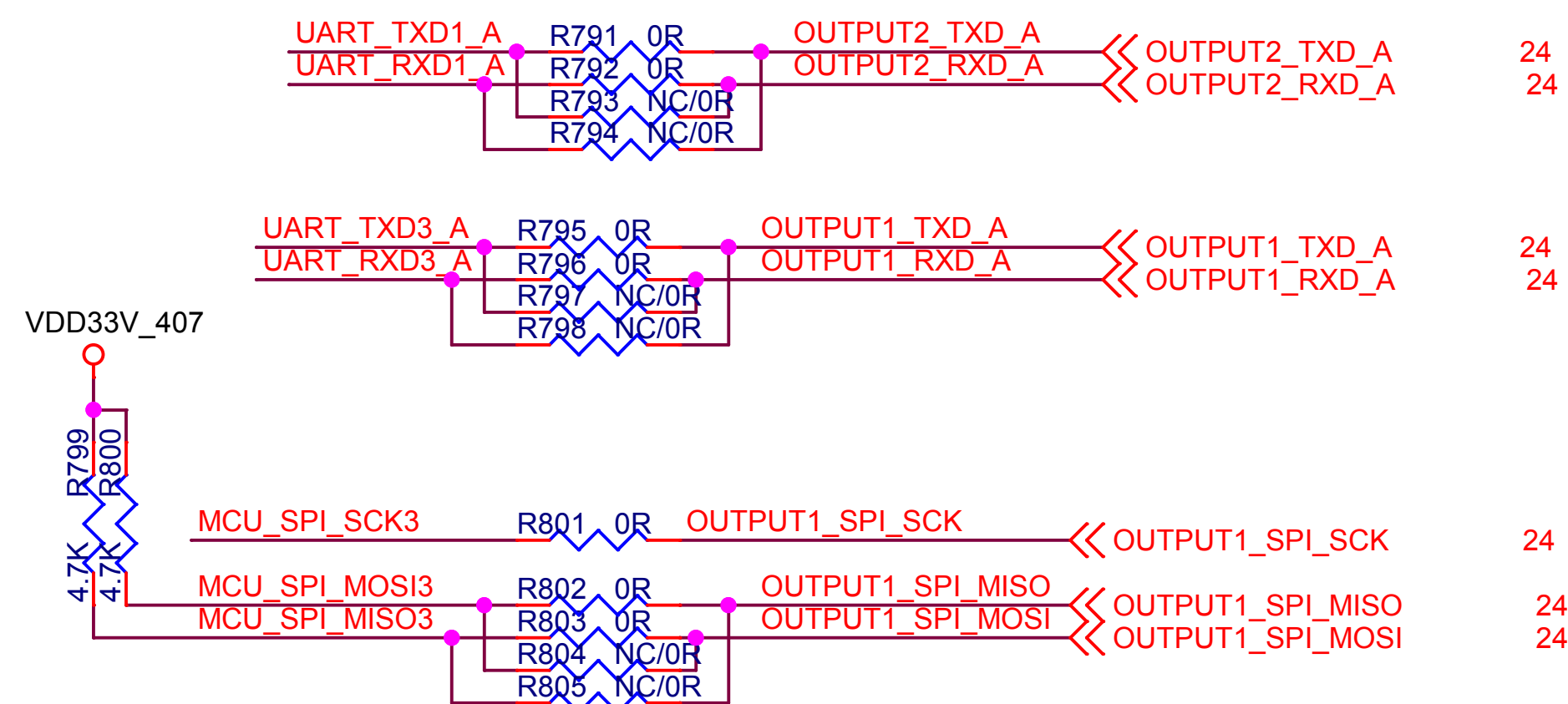
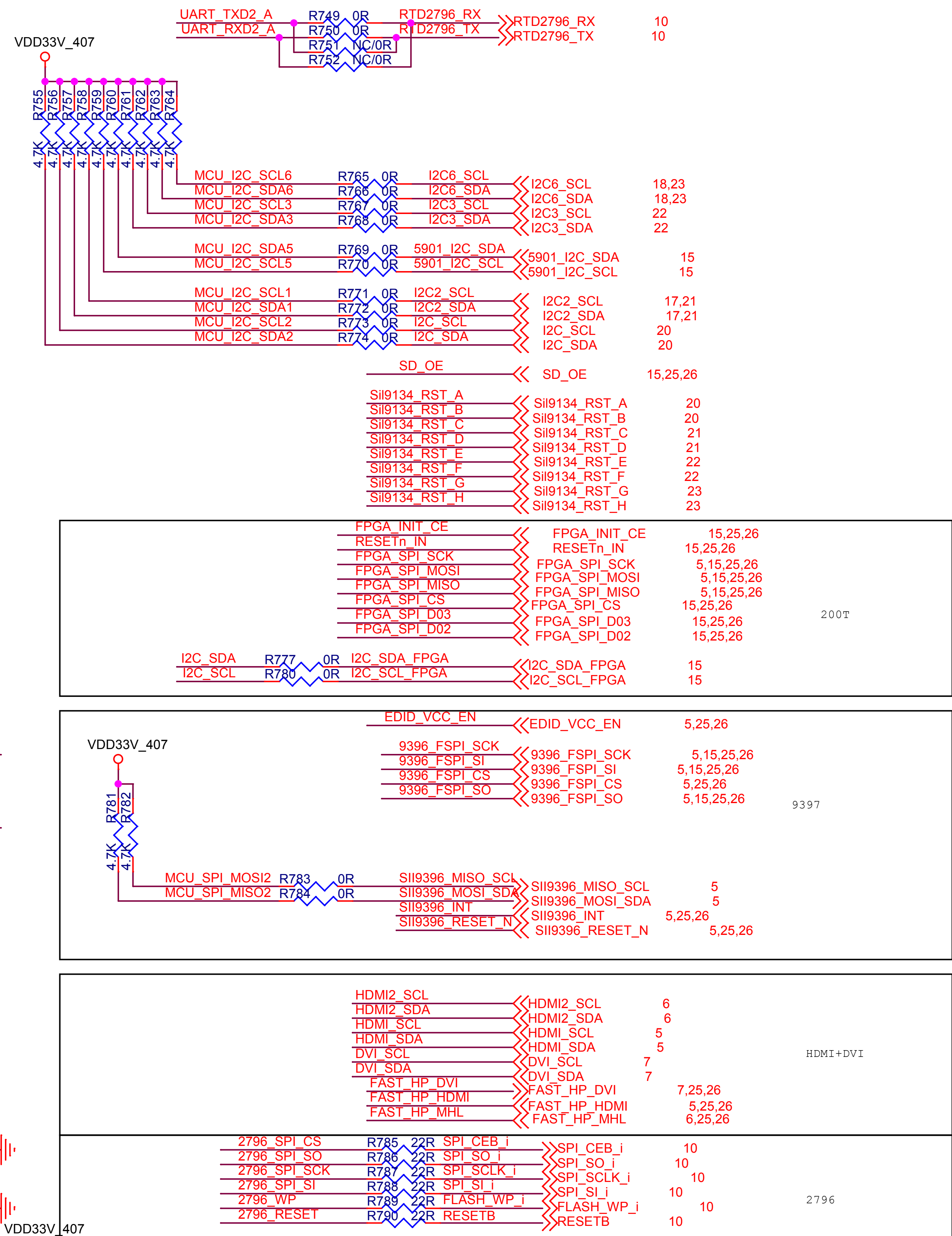
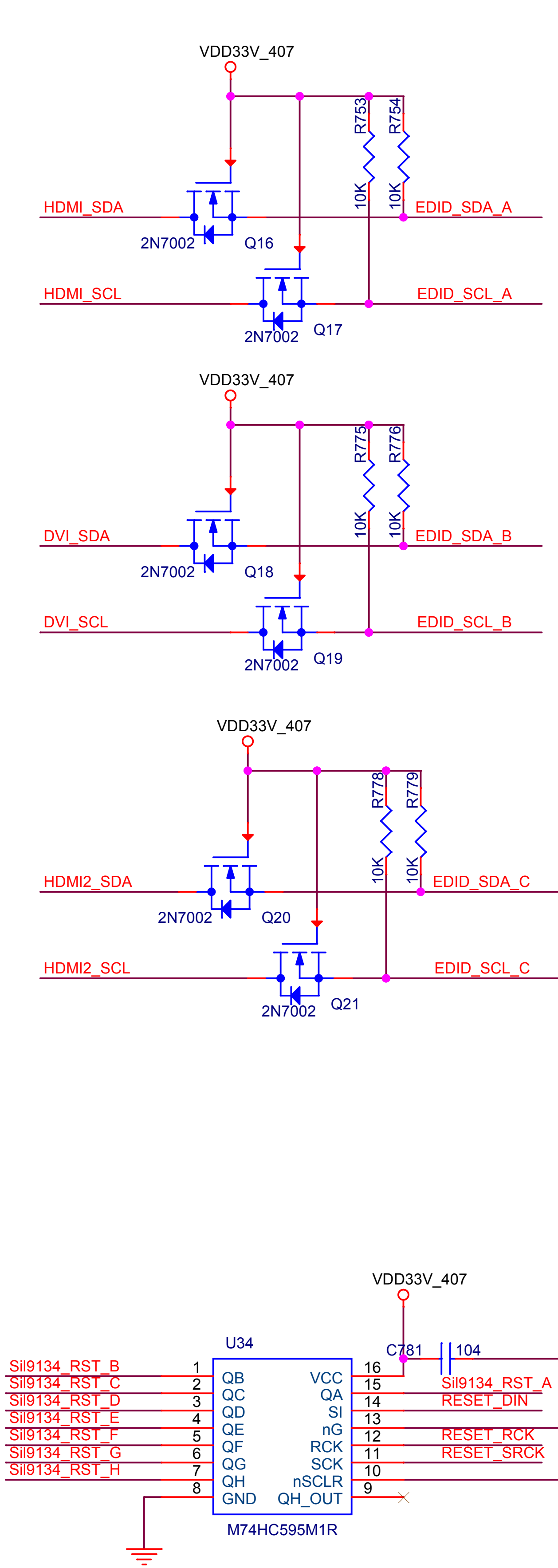















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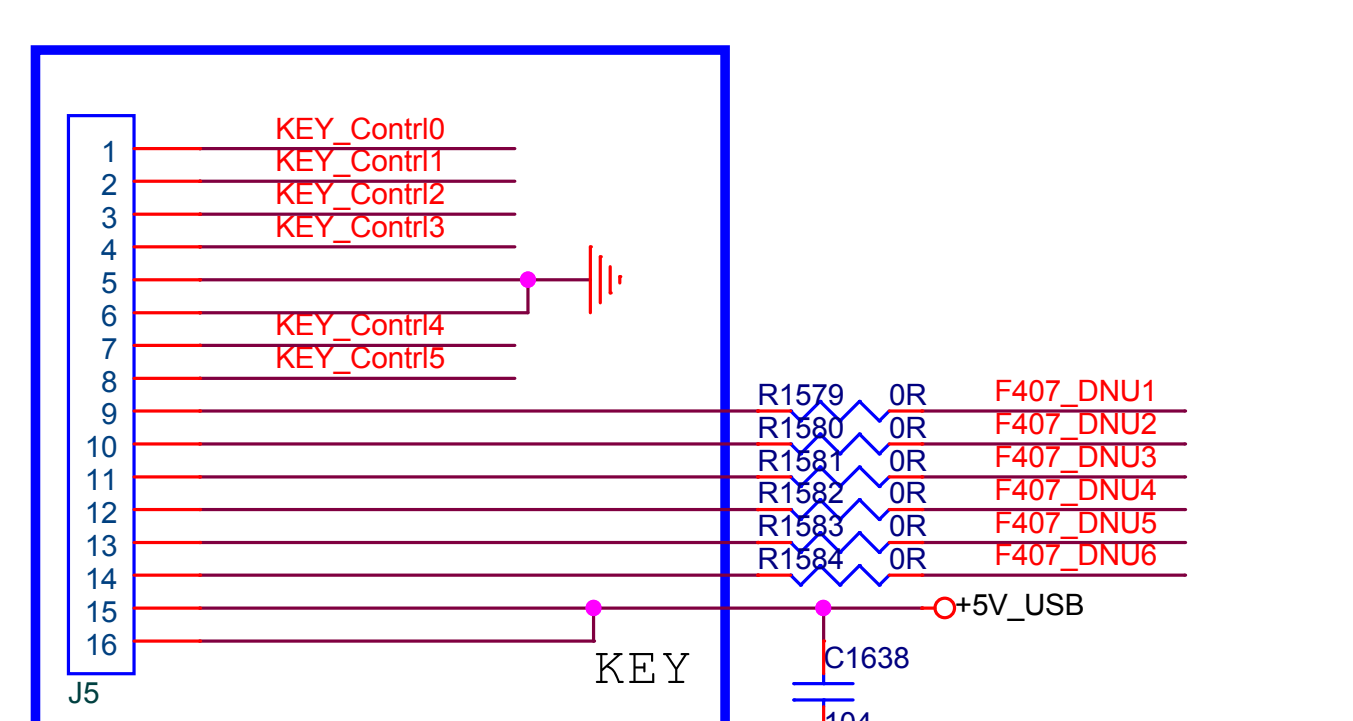
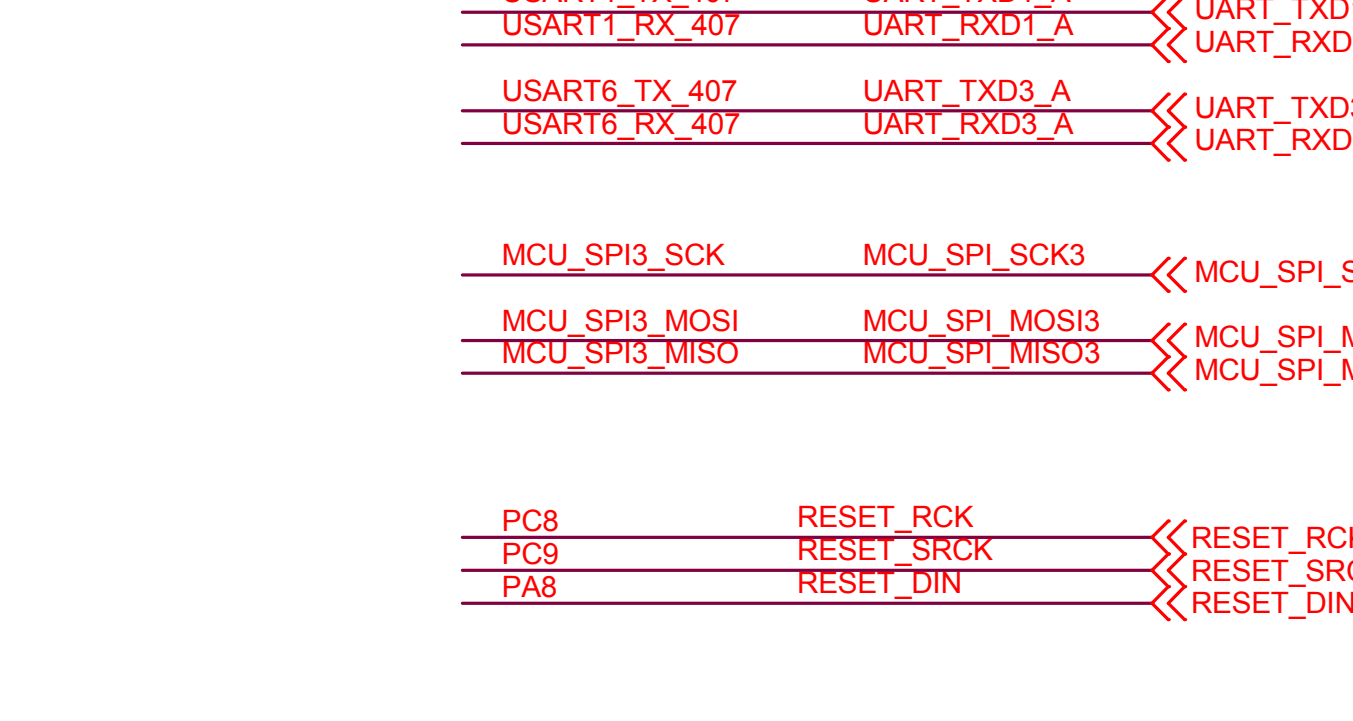
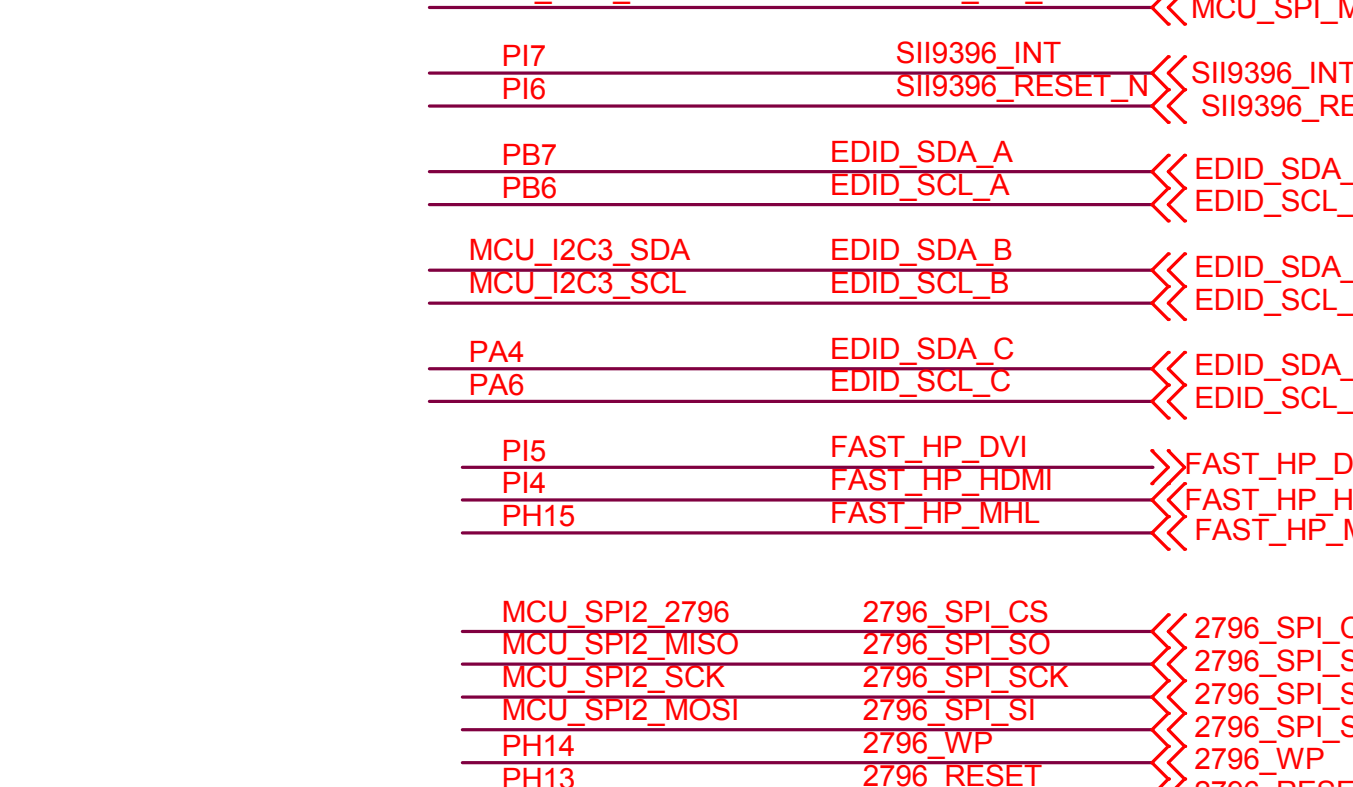
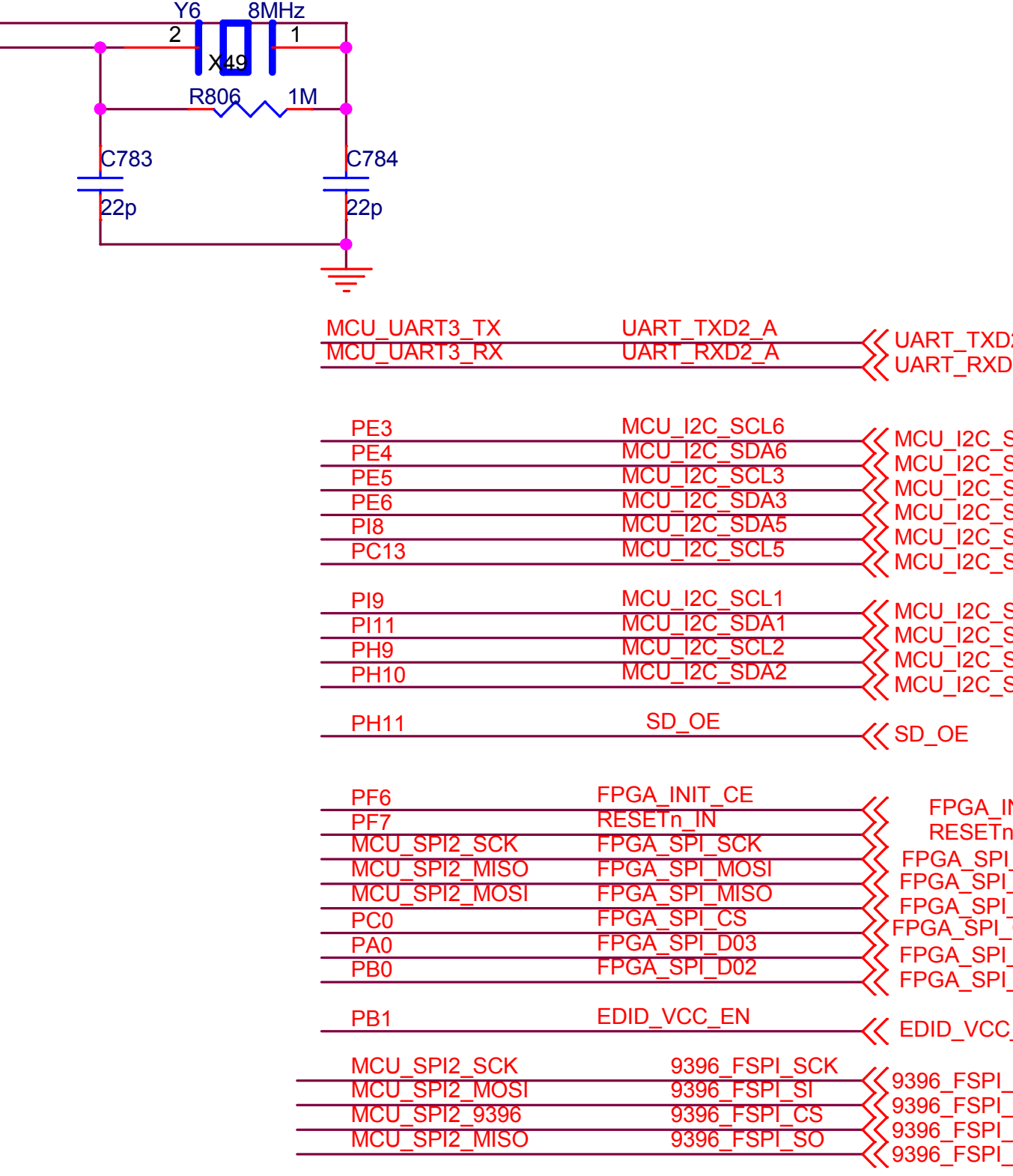
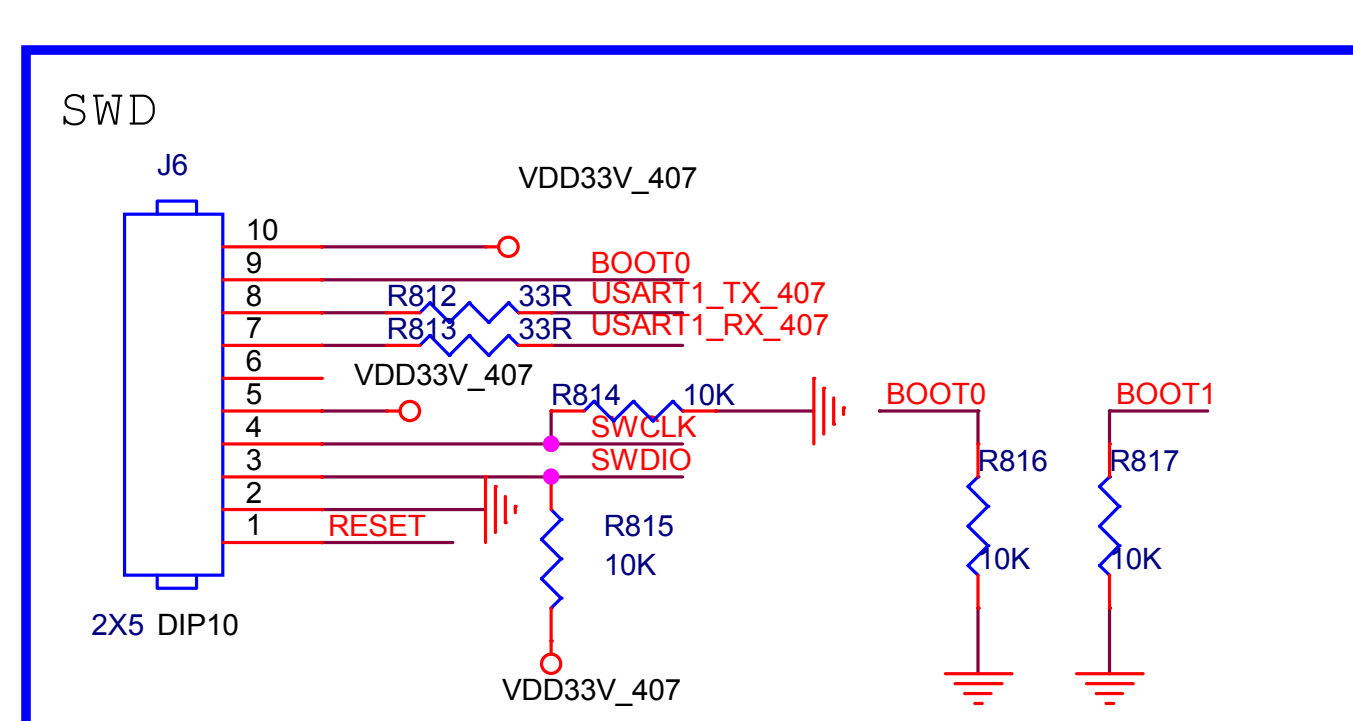
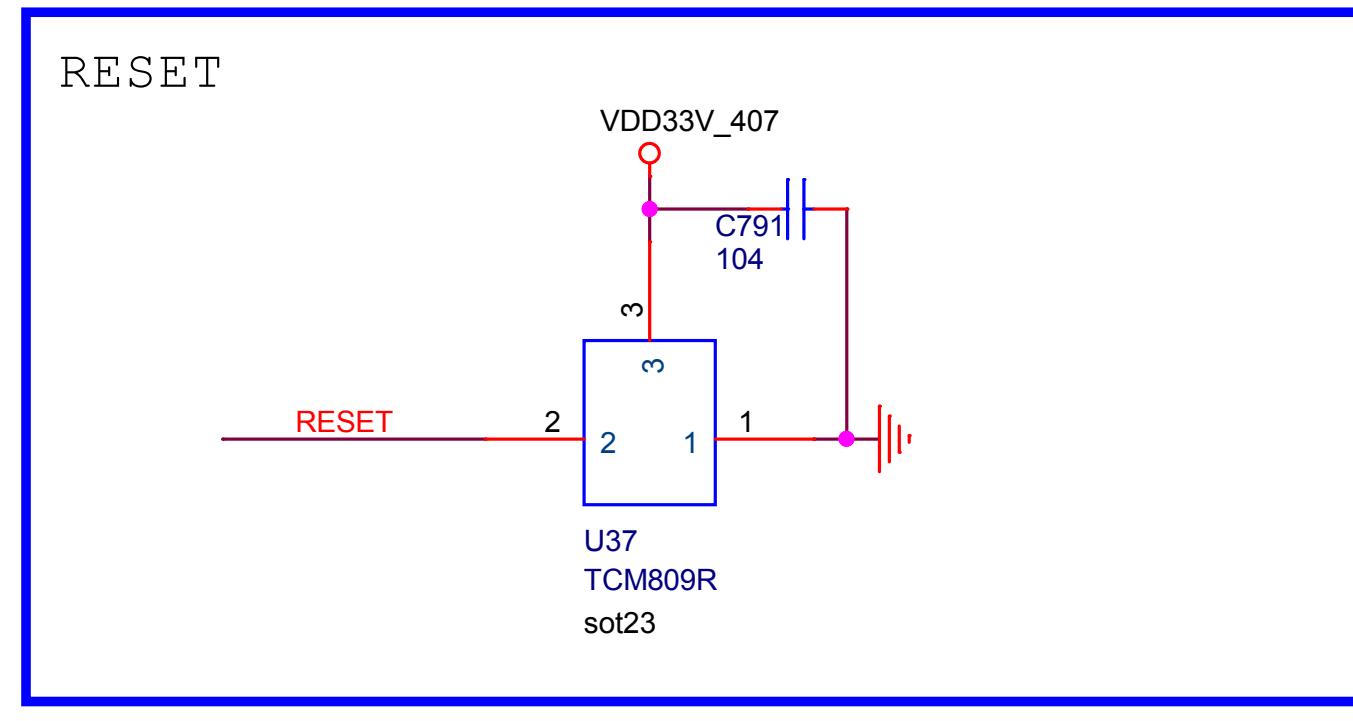
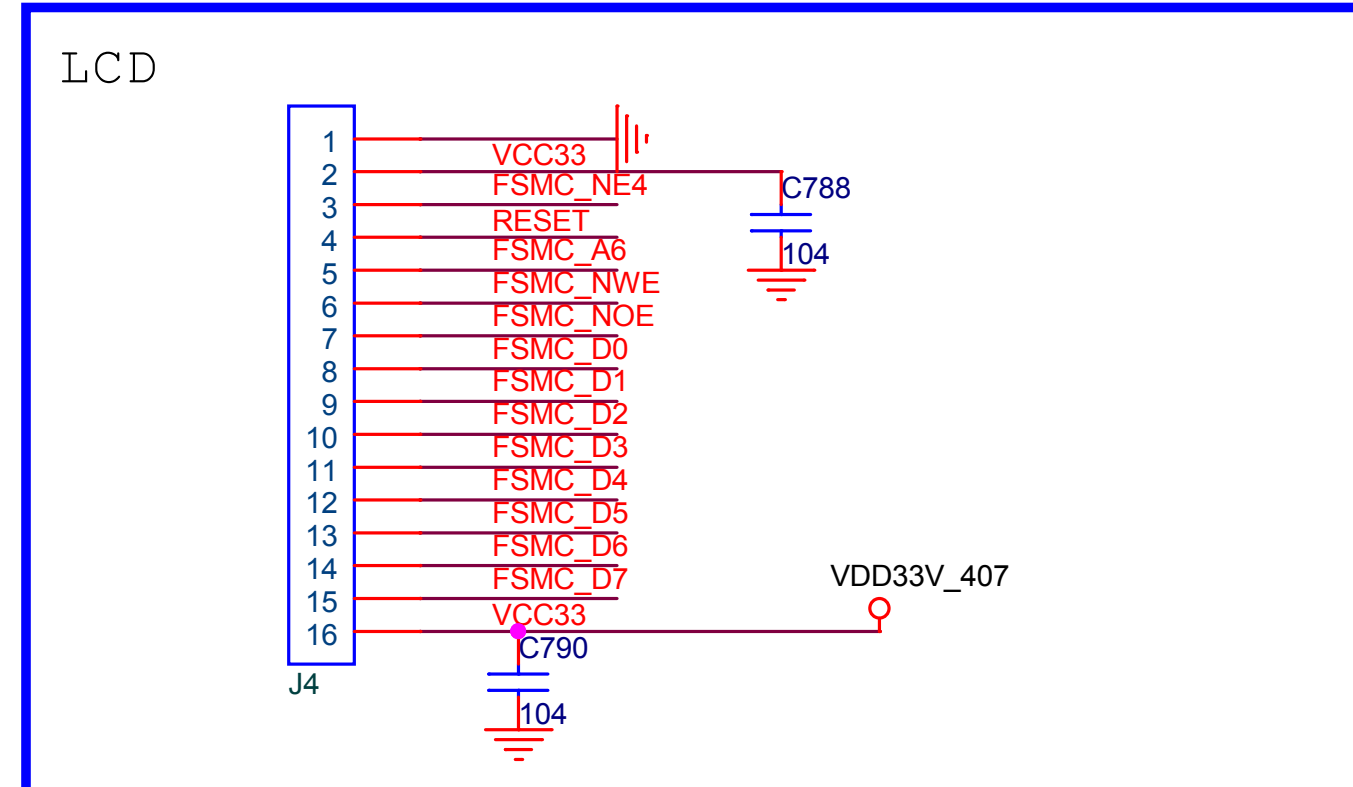
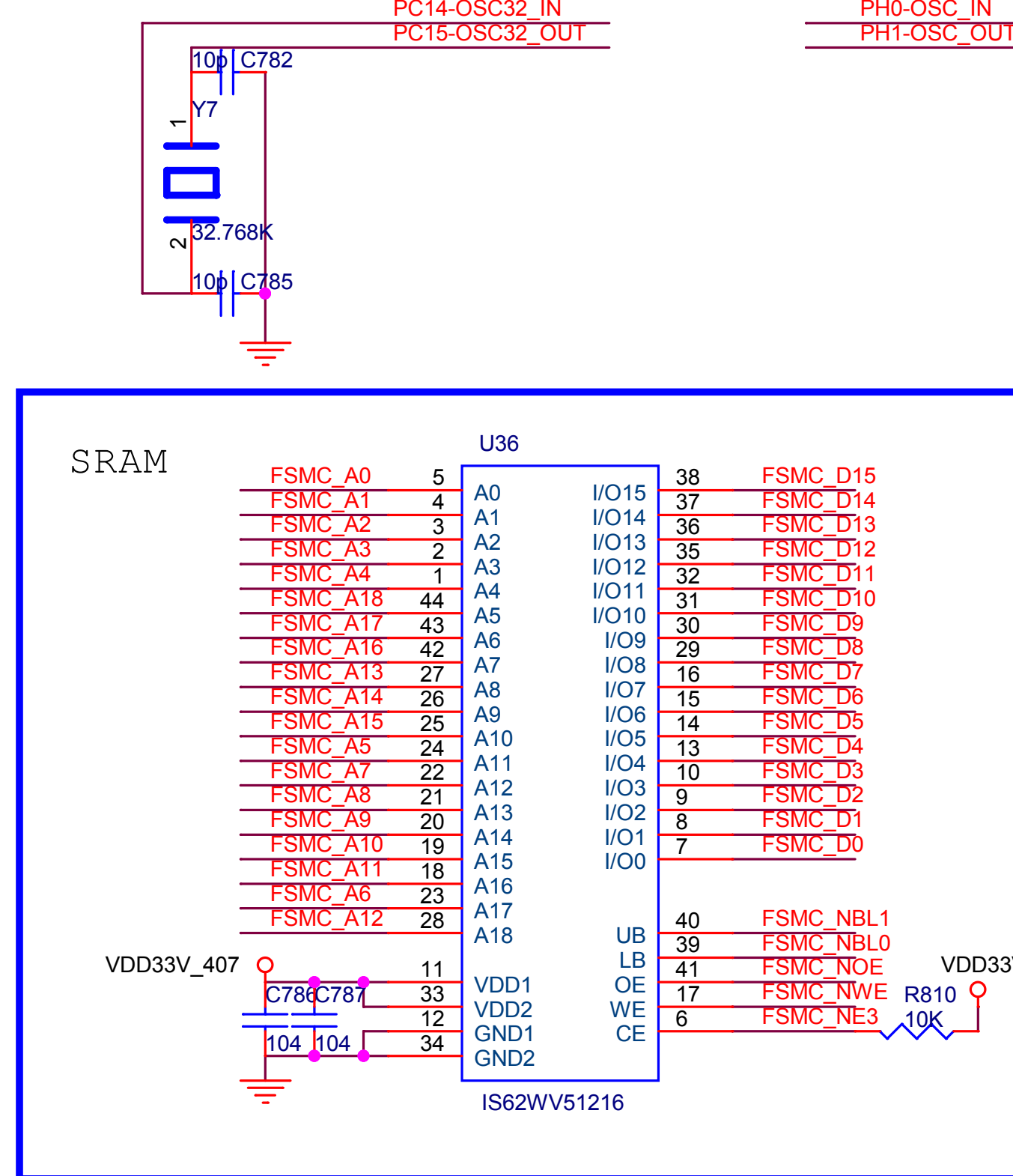
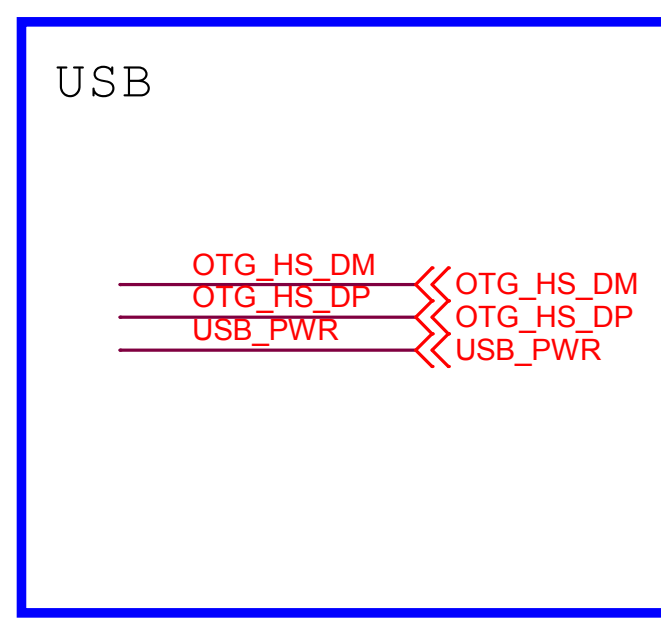
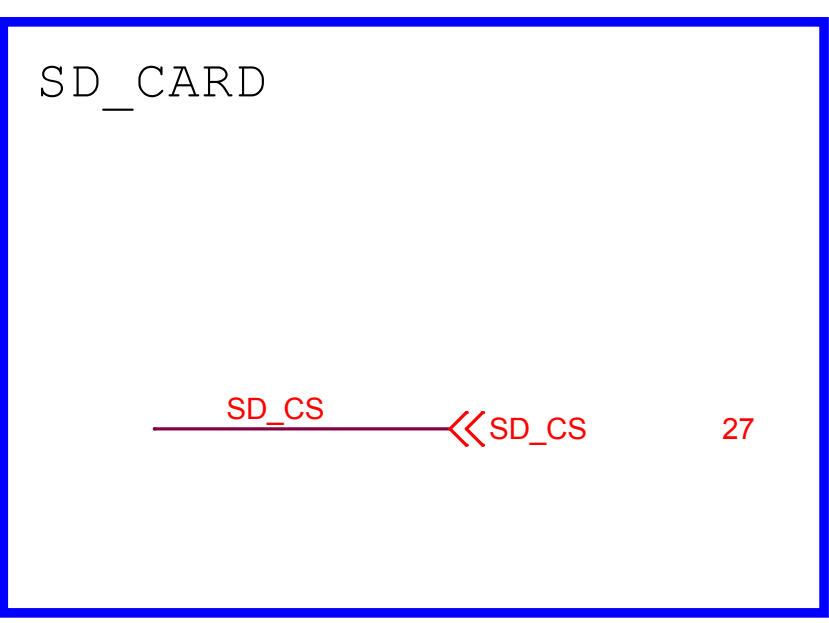
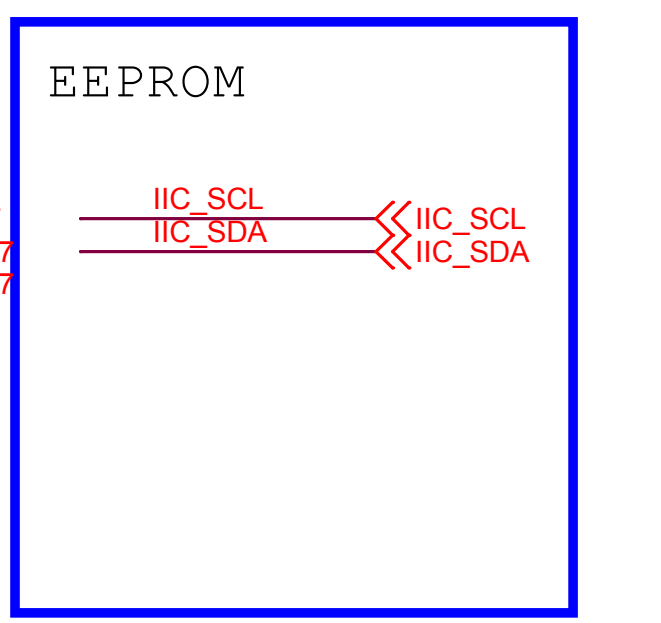
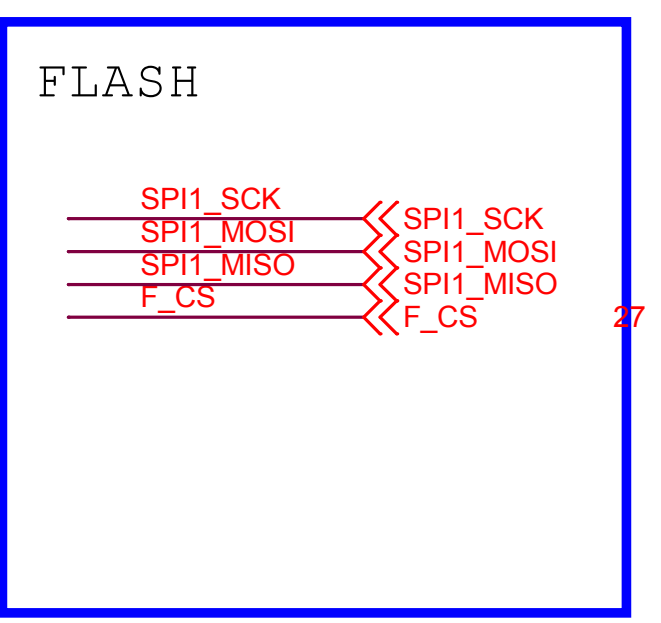
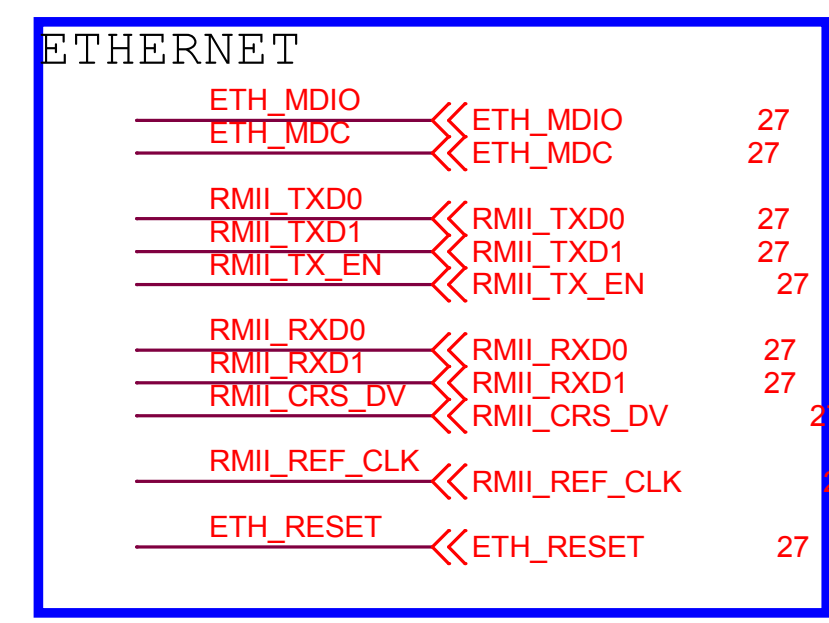
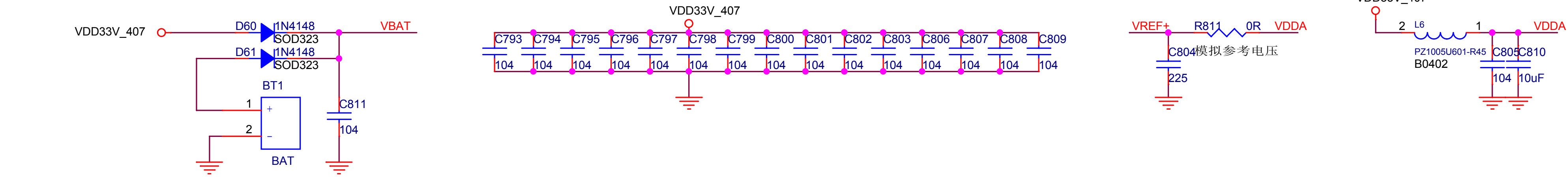
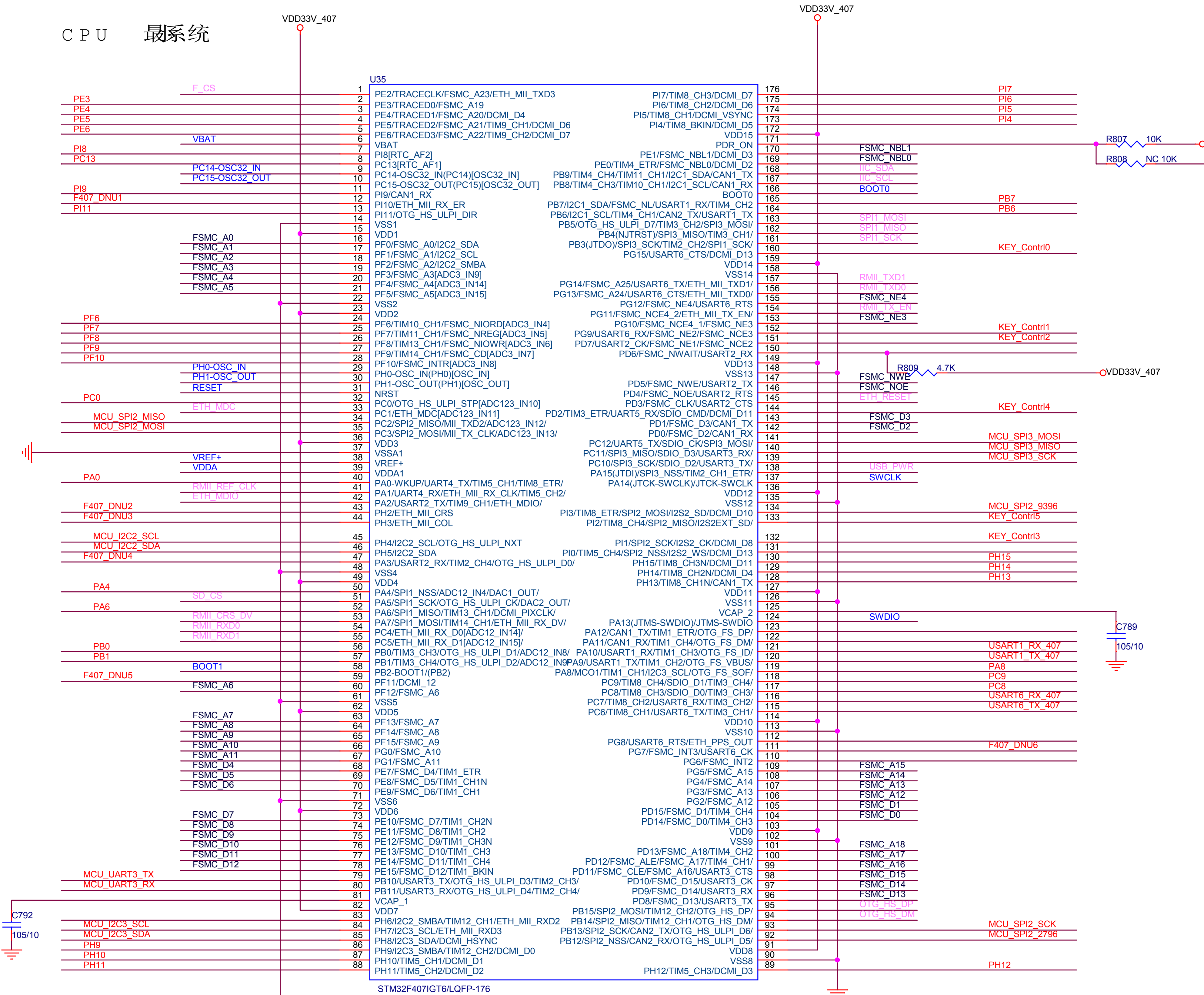
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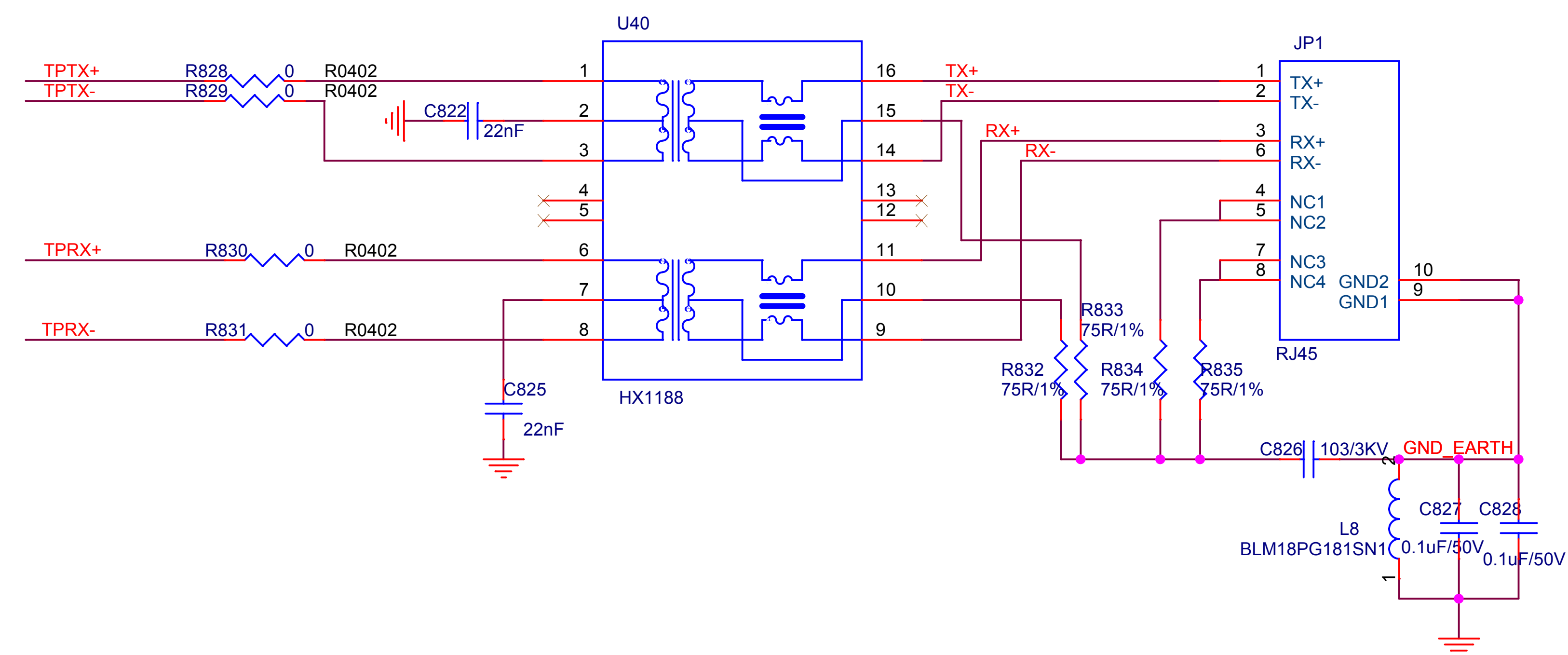
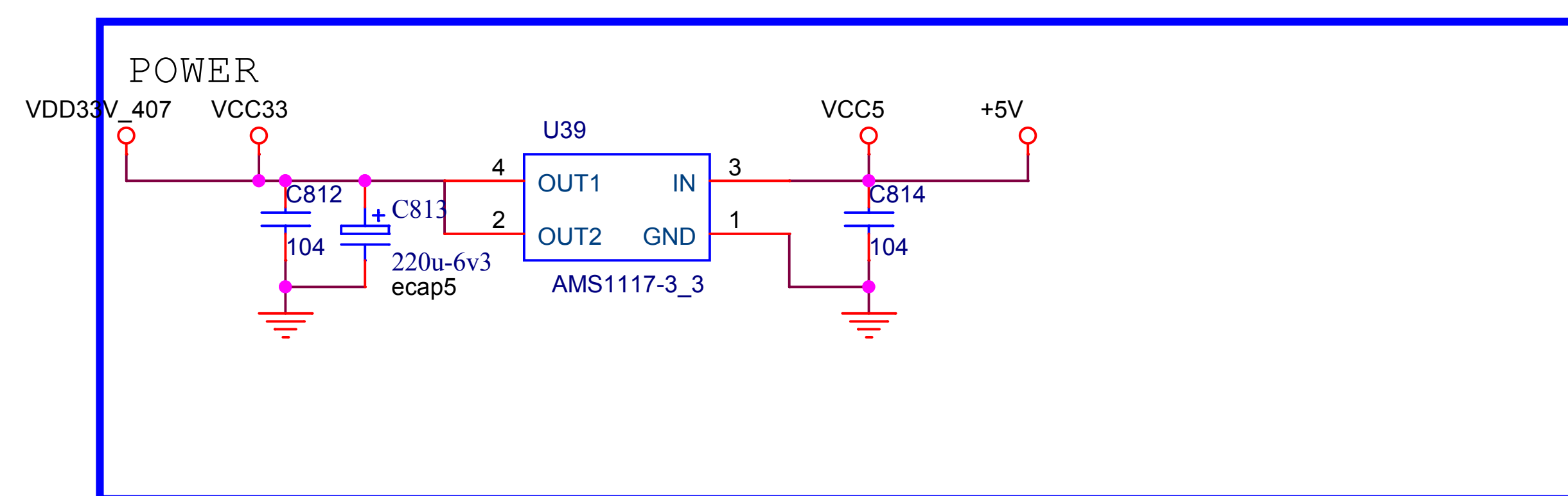
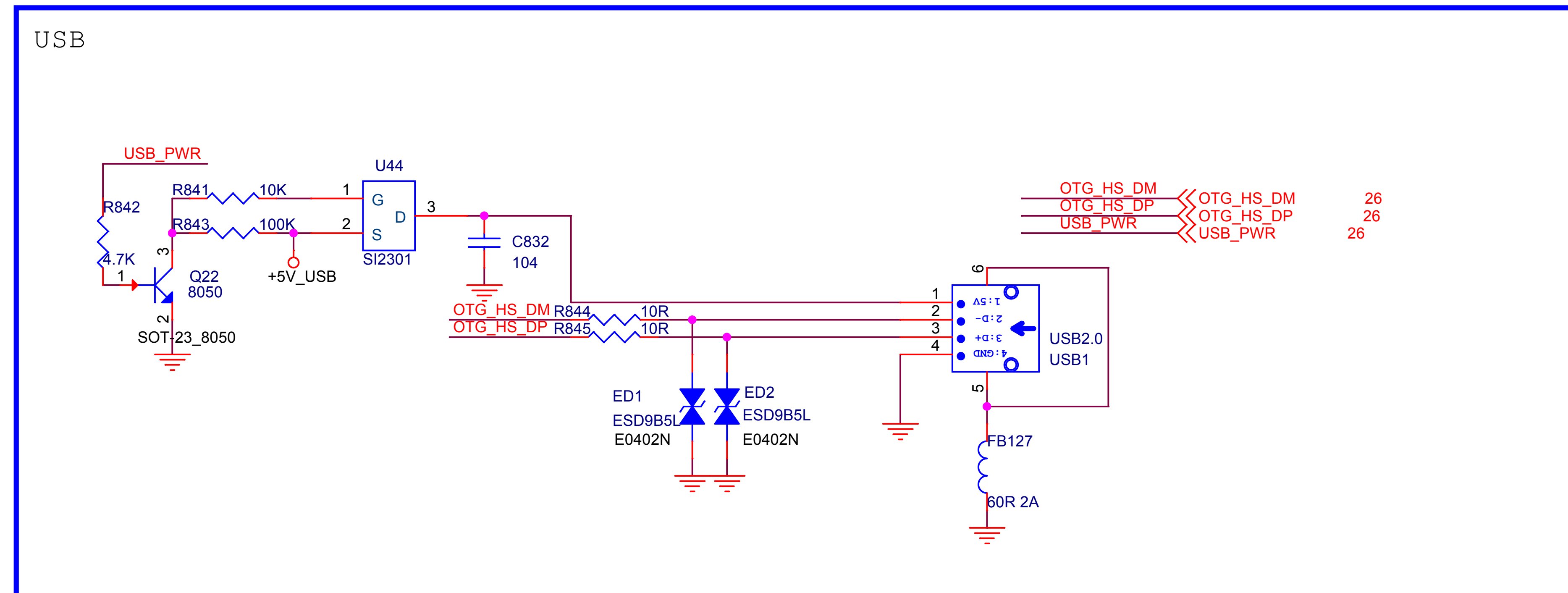
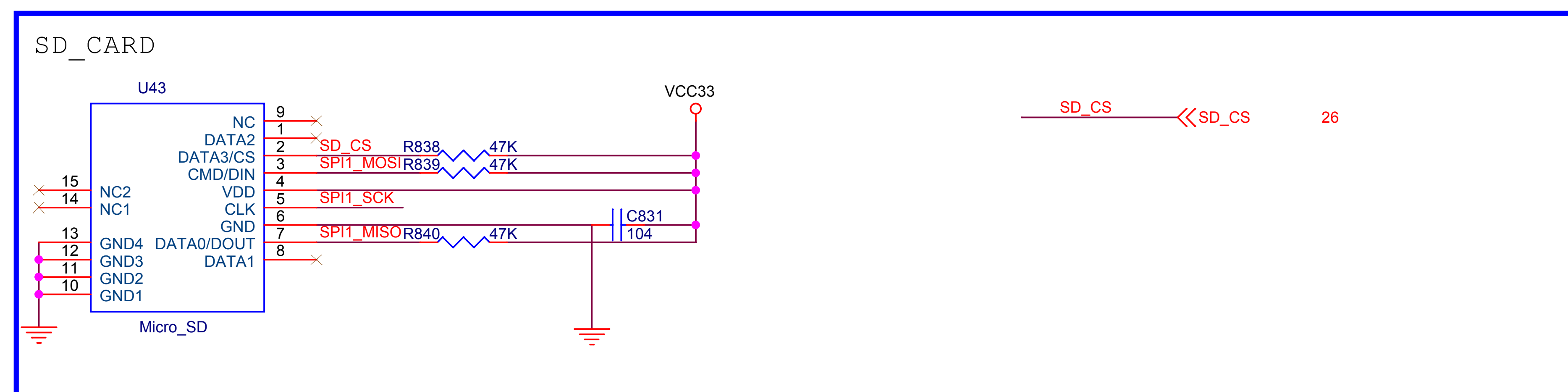
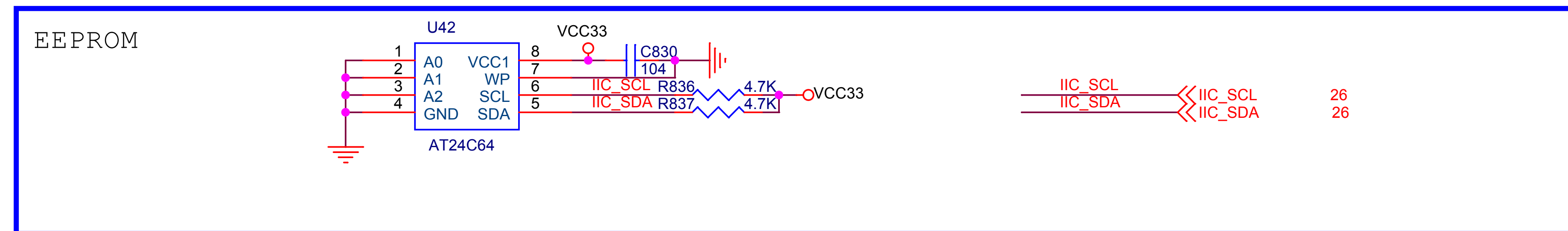
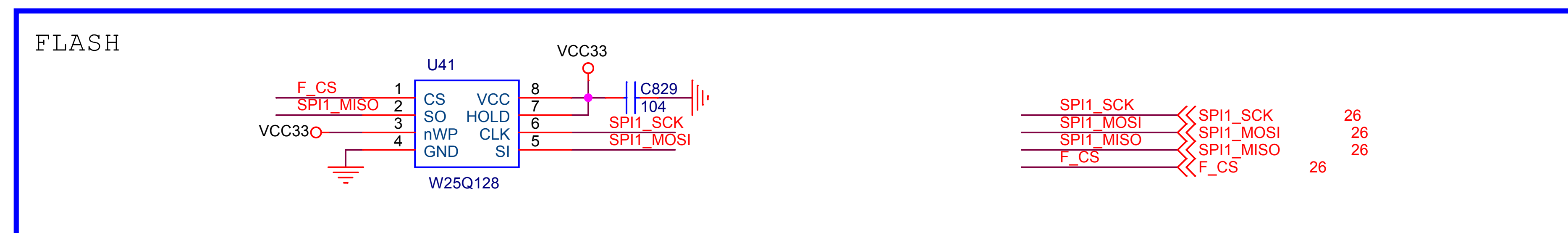
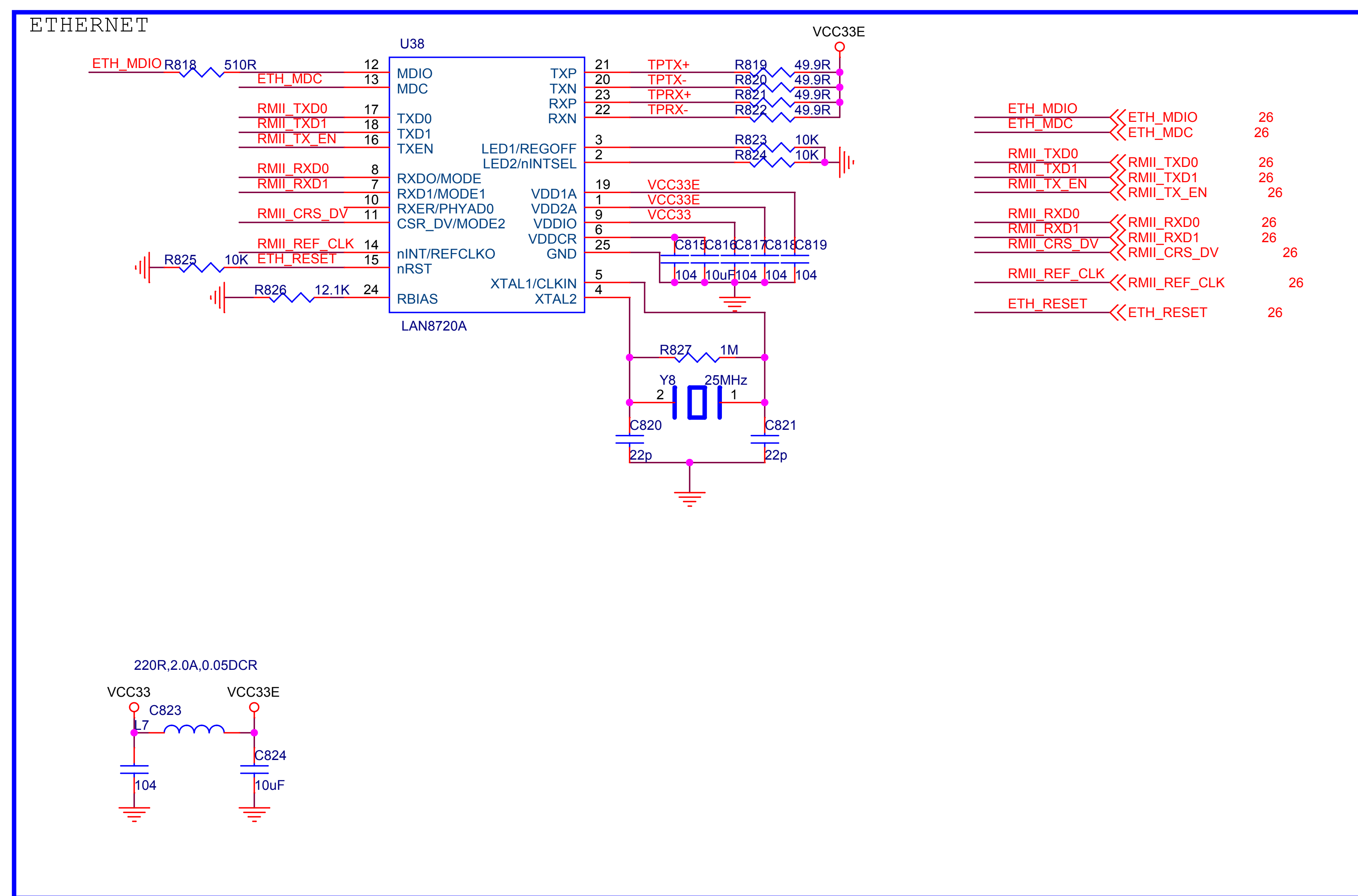



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