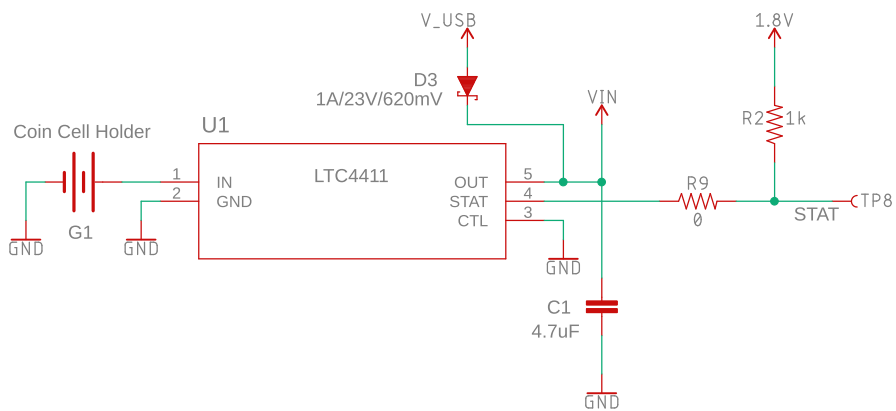


### Battery and USB Power Management Circuit



TITLE: judo-devkit\_v1

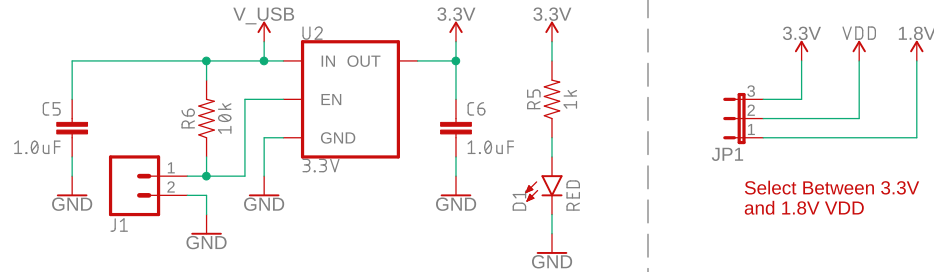
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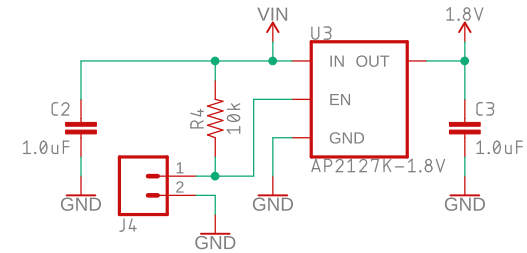
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Sheet: 1/3

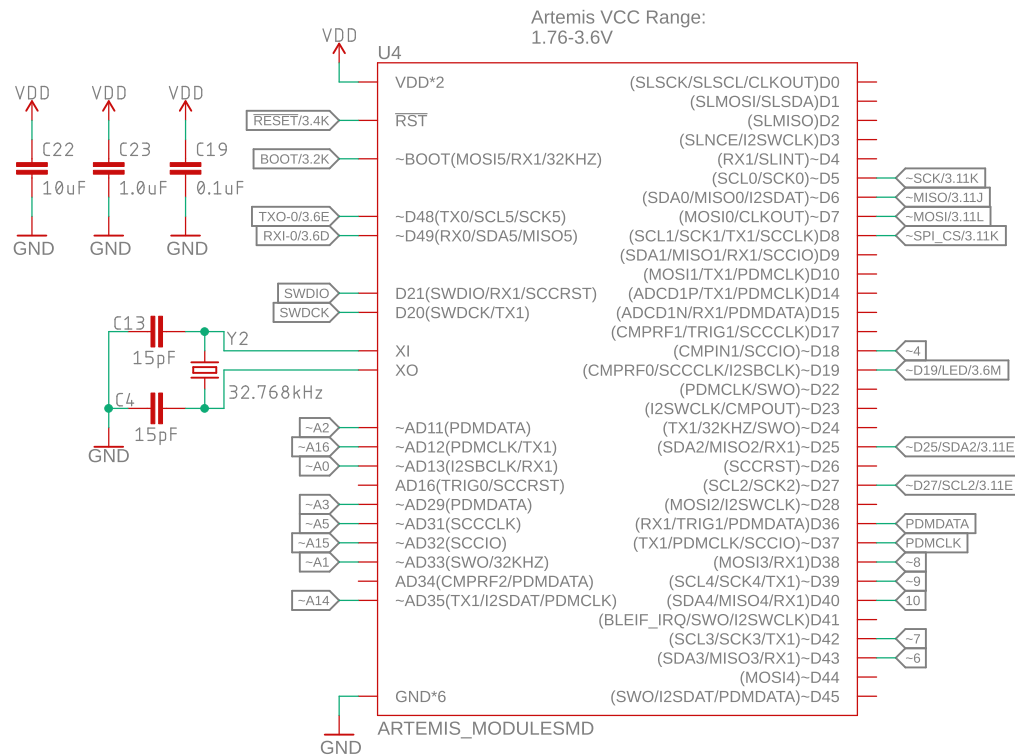
### 3.3V Regulator



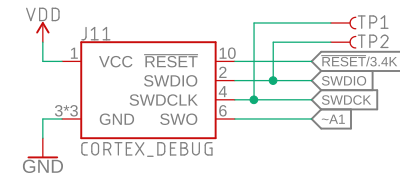
### 1.8V Regulator



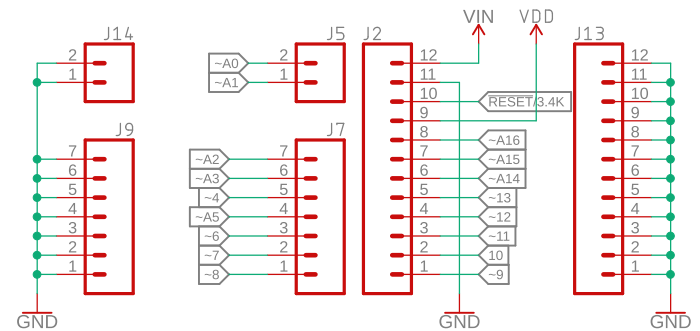
### Artemis (Apollo3)



### SWD Program/Debug Interface



### Headers



TITLE: judo-devkit\_v1

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1

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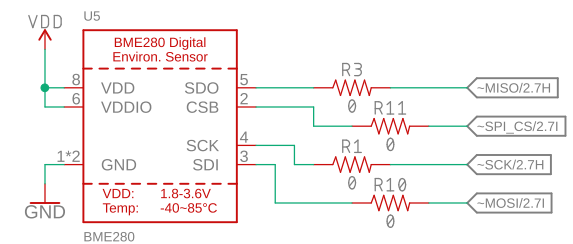
Sheet: 2/3

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QWIIIC\_RIGHT\_ANGLE

### Bootloader Reset Circuit

The diagram illustrates the Bootloader Reset Circuit. It features two RTS pins from the microcontroller. The first RTS pin is connected to a 100k resistor (R20) to GND and a 0.1uF capacitor (C21) to the BOOT/2.3H pin. The second RTS pin is connected to a 220k resistor (R19) to GND and a 0.1uF capacitor (C20) to the RESET/2.1L pin. The RESET/2.1L pin is also connected to a 100k resistor (R21) to VDD and a 1nF capacitor (C28) to GND. A switch (S1) is connected between the RESET/2.1L pin and GND.



For SPI set CSB low at startup  
SDO=MISO, SDI=MOSI, SCK=SCK, CSB=CS/SSEL

For I2C leave CSB pulled high (default value)  
SDI=SDA, SCK=SCL

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