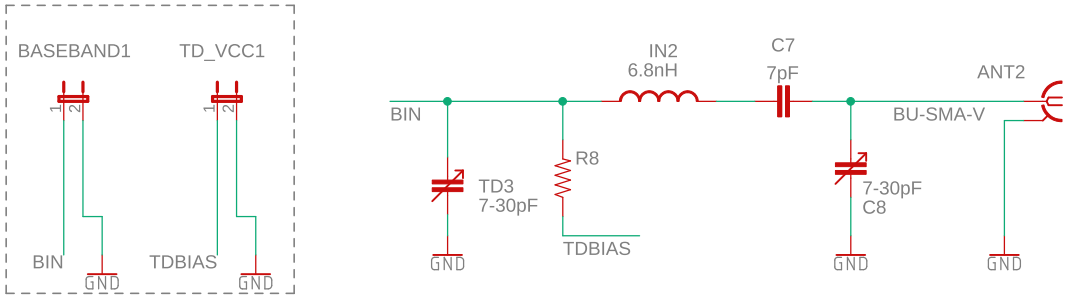
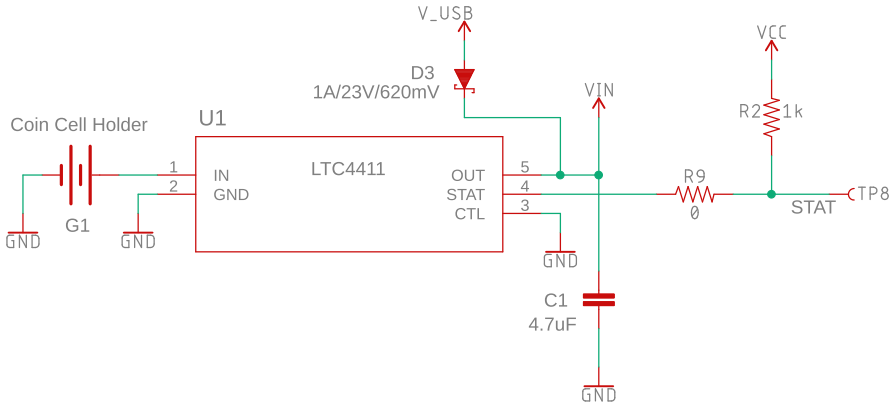
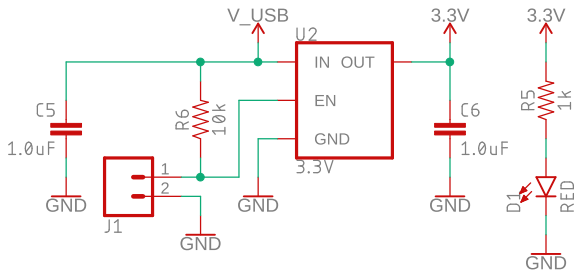


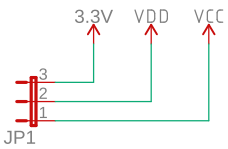
Battery and USB Power Management Circuit



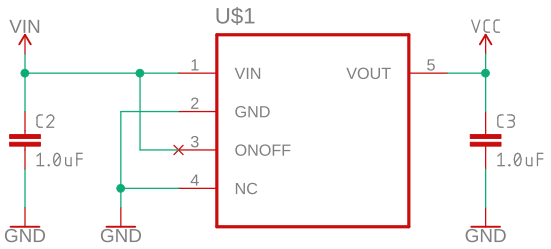
TITLE: judo-devkit_v1		
Document Number:		REV: 1
Date: 12/15/2023 2:04 PM	Sheet: 1/4	

3.3V Regulator



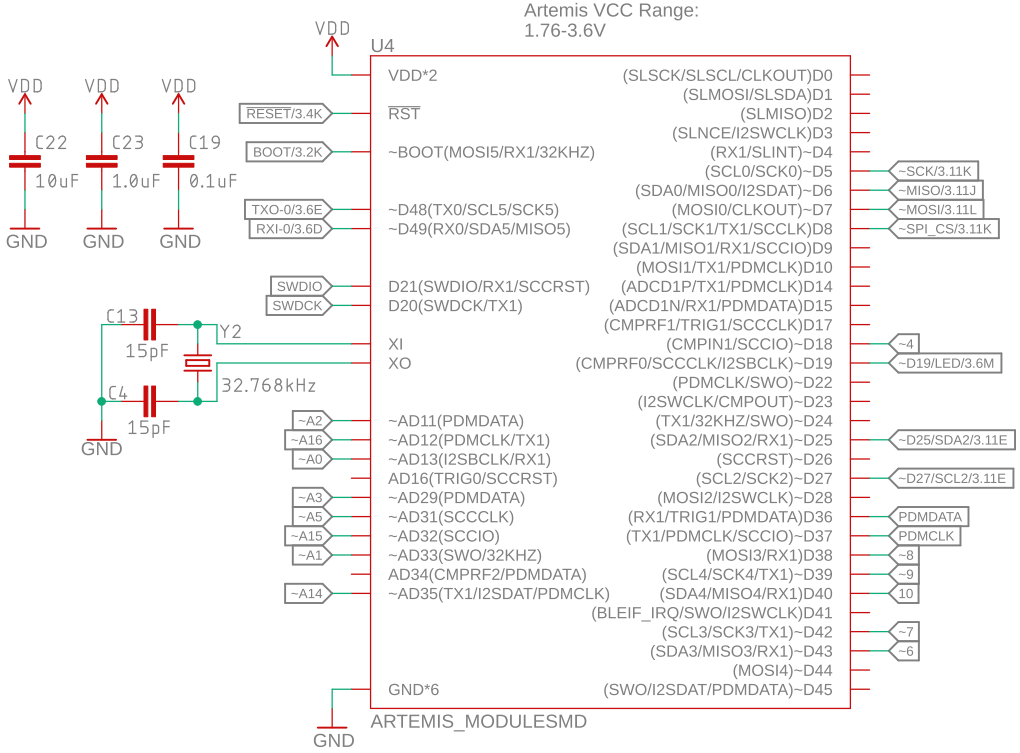


Voltage Regulator

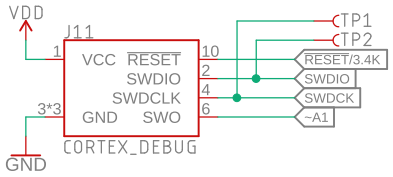


Artemis (Apollo3)

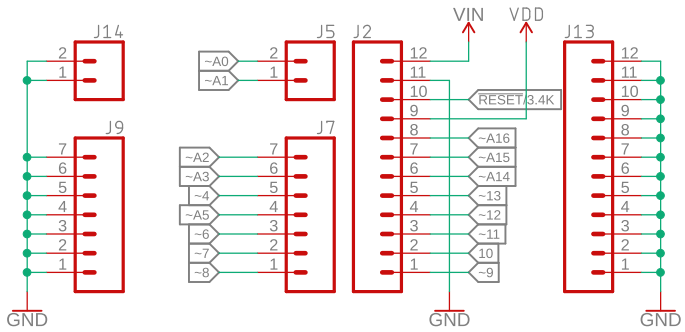
Artemis VCC Range:
1.76-3.6V



SWD Program/Debug Interface



Headers



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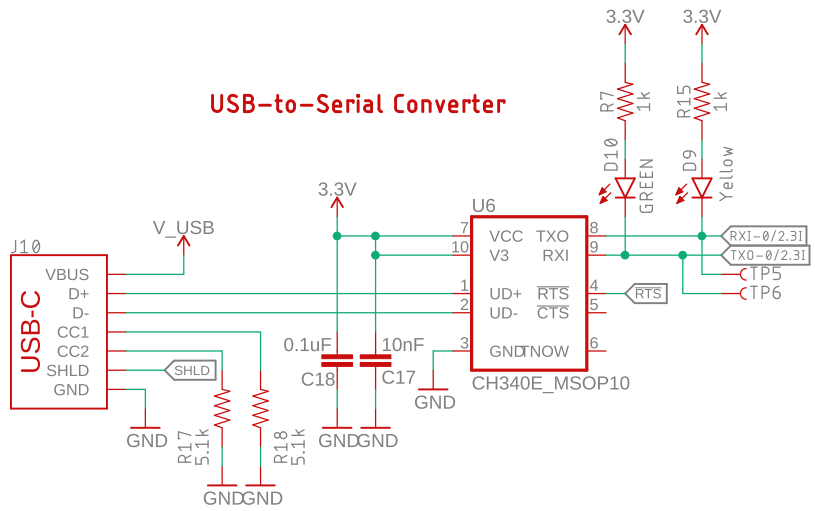
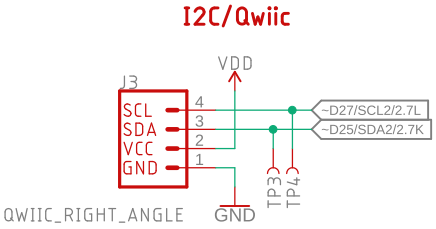
Document Number:

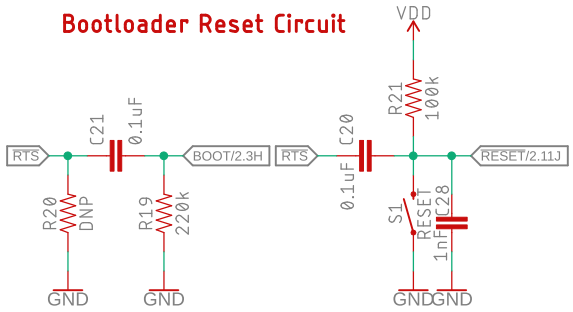
REV: 1

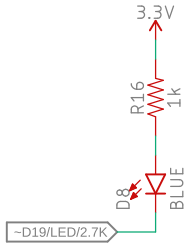
Date: 12/15/2023 2:04 PM

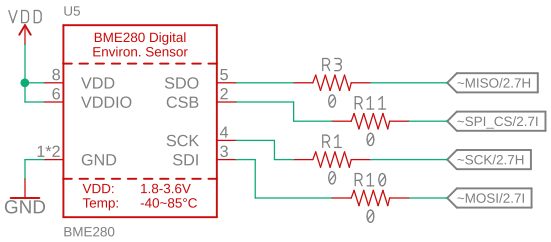
Sheet: 2/4







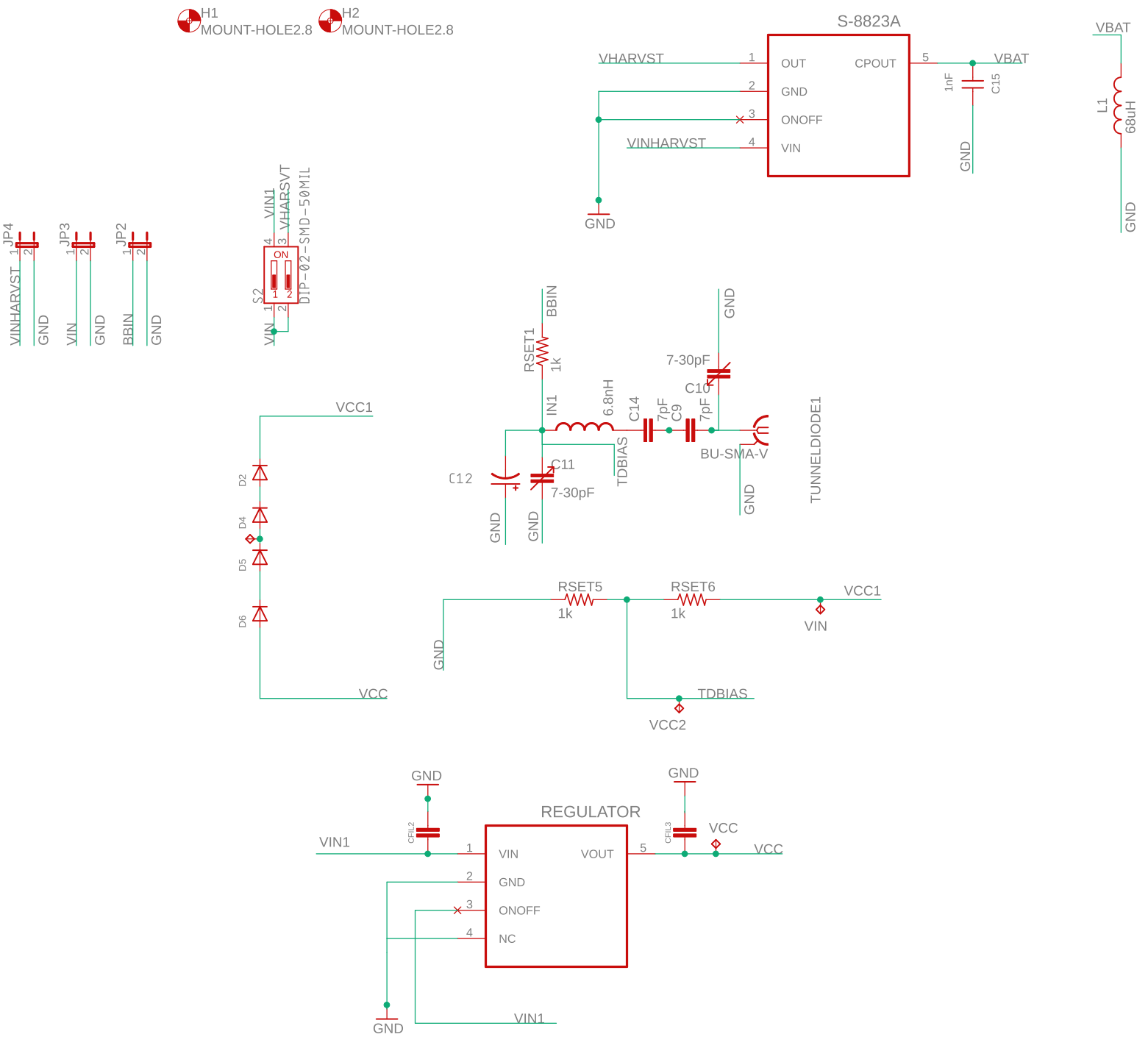




For SPI set CSB low at startup
SDO=MISO, SDI=MOSI, SCK=SCK, CSB=CS/SSEL

For I2C leave CSB pulled high (default value)
SDI=SDA, SCK=SCL

TITLE: judo-devkit_v1		
Document Number:		REV: 1
Date: 12/15/2023 2:04 PM	Sheet: 3/4	


H1
MOUNT-HOLE2.8
H2
MOUNT-HOLE2.8
S-8823A
VHARVST
OUT
CPOUT
GND
ONOFF
VIN
VINHARVST
VBAT
1nF
C15
GND
VBAT
L1
68uH
GND
JP4
VINHARVST
GND
JP3
VIN
GND
JP2
BBIN
GND
VIN1
VHARSVT
S2
ON
DIP-02-SMD-50MIL
VIN
RSET1
1k
BBIN
IN1
6.8nH
C14
7pF
C9
7pF
7-30pF
C10
GND
TDBIAS
C11
7-30pF
C12
GND
GND
BU-SMA-V
GND
TUNNELDIODE1
VCC1
D2
D4
D5
D6
VCC
RSET5
1k
RSET6
1k
VCC1
VIN
GND
TDBIAS
VCC2
REGULATOR
GND
GND
VIN1
CFIL2
VIN
VOUT
GND
ONOFF
NC
CFIL3
VCC
VCC
GND
VIN1