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Task 1: Designing Taint Analysis

Lattice:

Partially ordered by subset inclusion . The bottom is empty set and the top is set with all variables.

Type of the analysis:

Taint analysis is a may analysis with forward data flow. Because if any of the tainted variables is used to execute dangerous commands, the taint checker warns that the program is using a potentially dangerous tainted variable, but not every variable that is warned is necessarily contaminated by "source". As for the direction of the data flow analysis, since the program is running one line of code after another in the forward direction, for each block, we first get its entry set, then combine it with the code in the block to explore the set of variables that may be tainted, and finally generate the exit set. So it's forward and may.

Data structure used in the analysis:

std::map<std::string, std::set<std::string>> analysisMap: A collection of variables used to record the tainted values of each block.

std::stack<std::pair<BasicBlock*,std::set<std::string> > > traversalStack: During the program loop, any sub-block that meets the conditions will be pushed onto the stack. The program won't terminate unless the stack is empty.

std::set<std::string> findTaintedVars(BasicBlock*,std::set<std::string>): For a particular block, given its entry set, the function will get the exit set.

Gen and kill functions:

gen;
$$(\{z:=a\}^L) = \begin{cases} \{z\}, & \text{if } \exists a' \in \mathsf{FV}(a), a' \in \mathsf{TVentry}(L) \ V \text{ the fore}(L) \end{cases}$$

gen; $(\{z:=a\}^L) = \emptyset$

kill; $(\{z:=a\}^L) = \begin{cases} \{z\}, & \text{if } \forall a' \in \mathsf{FV}(a), a' \notin \mathsf{TVentry}(L) \ V \text{ TV before}(L) \end{cases}$

kill; $(\{z:=a\}^L) = \begin{cases} \{z\}, & \text{if } \forall a' \in \mathsf{FV}(a), a' \notin \mathsf{TVentry}(L) \ V \text{ TV before}(L) \end{cases}$

kill; $(\{z:=a\}^L) = \emptyset$

kill; $(\{z\}^L) = \emptyset$

Entry and exit equations:

Reach fixpoint:

Only add successor nodes to the stack if the new union set of tainted variables for the successor node is different from the currently stored set of tainted variables for the successor node.

$$[z:=1]^1$$
; while $[x>0]^2$ do $([z:=z*y]^3; [x:=x-1]^4)$

For example, only when the exit set of block 4 is different from the currently stored set of tainted variables of block 2, will we add pair
block2, tainted variable set> to the stack.

Task 2: Implementing the Taint Analysis in LLVM

After initialization, the entry block is pushed onto the stack, after which it keeps removing block from the stack until the stack is empty. Each time a block is taken out, it is analyzed to get the corresponding set containing tainted variables, and its sub-blocks are pushed onto the stack.

```
std::set<std::string> findTaintedVars(BasicBlock*,std::set<std::string>);
std::string getSimpleNodeLabel(const BasicBlock *Node);
std::string getSimpleVariableName(const Instruction *Ins);
std::string getSimpleValueName(const Value *Value);
std::set<std::string> union_sets(std::set<std::string> A, std::set<std::string> B);
void printAnalysisMap(std::map<std::string,std::set<std::string>> analysisMap);
```

Algorithm used:

Example1:

```
int main() {
    int i,j,k,sink, source;
    // read source from user input or it can be
    // initilized with a tainted value. e.g. source = 1234567
    i = source;
    if (j > 1)
        ;
        else
        k = i;
        sink = k;
}
```

Program: 12

```
%1 = alloca i32, align 4
   %i = alloca i32, align 4
   %j = alloca i32, align 4
   %k = alloca i32, align 4
   %sink = alloca i32, align 4
   %source = alloca i32, align 4
   store i32 0, i32* %1
   %2 = load i32* % source, align 4
   store i32 %2, i32* %i, align 4
   %3 = load i32* %j, align 4
   %4 = icmp sgt i32 %3, 1
br i1 %4, label %5, label %6
                   %6:
%5:
                    \%7 = \text{load i}32*\%\text{i, align 4}
br label %8
                    store i32 %7, i32* %k, align 4
                    br label %8
   %8:
   %9 = load i32* %k, align 4
   store i32 %9, i32* %sink, align 4
   %10 = load i32* %1
   ret i32 %10
```

CFG:

CFG for 'main' function

- entry: {source, i}
- if.then: {source, i}
- if.else: {source, i, k}
- if.end: {source, i, k, sink}

Expected output:

```
PRINTING ANALYSIS MAP:
                 %0:
                                          source
                 %5:
                         %2
                                          source
                 %6:
                         %2
                                  %7
                                                           source
                 %8:
                         %2
                                  %7
                                          %9
                                                                    sink
                                                                            source
Analysis output:
```

Note: %0 -> "entry"; 5% -> "if.then"; 6% -> "if.else"; 8% -> "if.end"

Example2:

Program:

```
%0:
                       %1 = alloca i32, align 4
                       %i = alloca i32, align 4
                       %j = alloca i32, align 4
%k = alloca i32, align 4
                       %sink = alloca i32, align 4
                       %source = alloca i32, align 4
                       store i32 0, i32* %1
                       %2 = load i32* %i, align 4
                      %3 = icmp sgt i32 %2, 1
br i1 %3, label %4, label %6
%4:
%5 = load i32* %source, align 4
                                           %7 = load i32* %j, align 4
store i32 %5, i32* %j, align 4
                                           store i32 %7, i32* %k, align 4
br label %8
                                           br label %8
                    %8:
                     %9 = load i32* %k, align 4
                     store i32 %9, i32* %sink, align 4
                     %10 = load i32* %1
                    ret i32 %10
                         CFG for 'main' function
```

CFG:

```
entry: {source}if.then: {source, j}if.else: {source}if.end: {source, j}
```

Expected output:

```
PRINTING ANALYSIS MAP:

%0:

source

%4:

%5 j source

%6:

source

%8:

Analysis output:

%5 j source
```

Note: %0 -> "entry"; 4% -> "if.then"; 6% -> "if.else"; 8% -> "if.end"

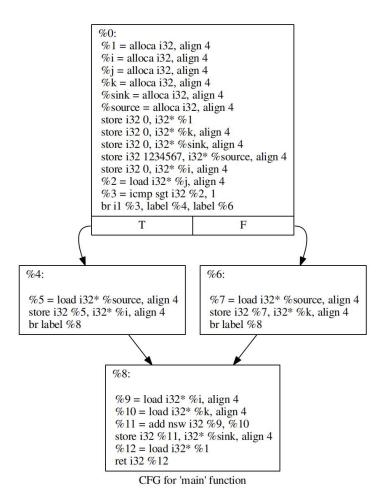
Task 3: Determine the Abstract Tainted Data Flow Paths

Based on task2, the path of each tainted variable is recorded in a roadMap in real time. In the analysis of each basic block, every time a variable is tainted, the path of its tainted cause is updated in time. Finally, we traverse the roadMap and output the analysis results.

Algorithm used:

```
std::set<std::string> findTaintedVars(BasicBlock*,std::set<std::string>);
std::string getSimpleNodeLabel(const BasicBlock *Node);
std::string getSimpleVariableName(const Instruction *Ins);
std::string getSimpleValueName(const Value *Value);
std::set<std::string> union_sets(std::set<std::string> A, std::set<std::string> B);
void printAnalysisMap(std::map<std::string,std::set<std::string>> analysisMap);
void printRoadMap(std::map<std::string, std::vector<std::string>> roadMap);
std::map<std::string, std::vector<std::string>> roadMap;
int roadNum = 0;
```

```
int main() {
    int i,j,k=0,sink=0, source=1234567;
    // read source from input or initilized with
    // a tainted value. e.g. source = 1234567
    i=0;
    if (j > 1)
        i=source;
    else
        k = source;
    sink = i+k;
}
```



Expected output: something like this, spread to every variable.

In the above example program, the abstract path followed by tainted value in the variable source to reach variable k at if.else block is {entry->if.else} and for variable sink at if.end block is as follows {entry->if.then->if.end} or {entry->if.else->if.end}. We can see that there are two possible paths to taint the sink variable.

```
PRINTING ROAD MAP:
%10:
%0 %6 %8
%11:
%0 %4 %8
%5:
%0 %4
%7:
%0 %6
%9:
%0 %4 %8
%0 %4
k:
%0 %6
sink0:
%0 %6 %8
sink1:
%0 %4 %8
source:
```

Analysis output: 30

Note:

CFG:

```
For block "entry" (%0):

Tainted variable: source, path: {entry(%0)}

For block "if.then" (%4):

Tainted variable: i, path: {entry(%0) -> if.then(%4)}

For block "if.else" (%6):

Tainted variable: k, path: {entry(%0) -> if.else(%6)}

For block "if.end" (%8):

Tainted variable: sink, path: {entry(%0) -> if.then(%4) -> if.end(%8) entry(%0) -> if.else(%6) -> if.end(%8)}
```

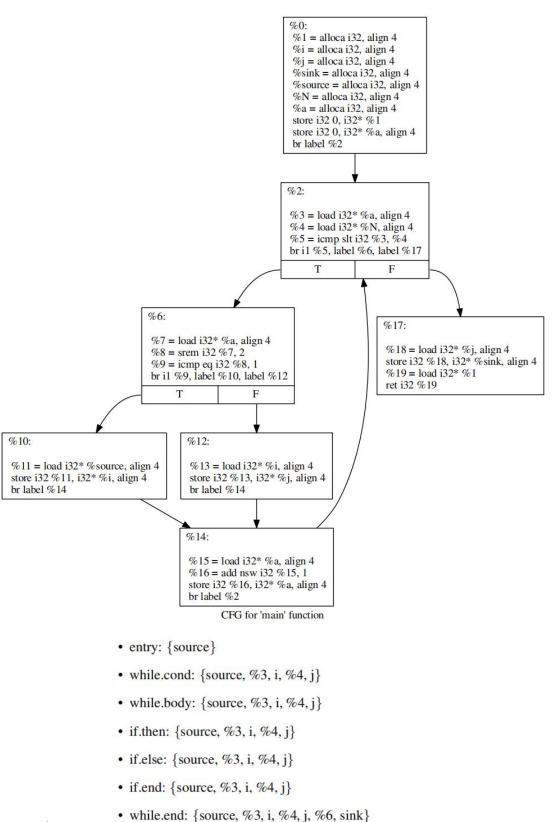
Task 4: Adding Support for Loops to the Analysis

Implementation is the same as for task 2. The only difference is the addition of a new judgment when determining whether a sub-block of each block should be pushed onto the stack or not, so called fixpoint judgement.

Algorithm used: same as task2.

Program:

CFG:



Expected output:

```
PRINTING ANALYSIS MAP:
%0:
        source
%10:
        %11
                 %13
                                  j
                                           source
%12:
        %11
                 %13
                                           source
%14:
        %11
                 %13
                                  j
                                           source
%17:
        %11
                 %13
                          %18
                                           j
                                                    sink
                                                            source
%2:
        %11
                 %13
                                   j
                                           source
%6:
        %11
                 %13
                                           source
```

Analysis output:

Note:

Entry(%0): {source}

While.cond(%2): {source, i, j}

While.body(%6): {source, i, j}

If.then(%10): {source, i, j}

If.else(%12): {source, i, j}

If.end(%14): {source, i, j}

While.end(%17): {source, i, j, sink}

Task 5: Beyond taint analysis: Implement Very Busy Expression Analysis

In contrast to tainted variable analysis, we first push the last exit block onto the stack, and then we take out elements of the stack each time we loop until the stack is empty. For each block we take out, we update the exit set and push all the instructions in it into a new instruction stack, so that we can analyze the instructions in the block backwards from the bottom to the top. For each instruction analyzed, we update the busy expressions until the last instruction is analyzed, record the result in the block's entry set, and push its parent block onto the stack.

```
std::set<std::string> findBusyExpressions(BasicBlock*,std::set<std::string>);
std::string getSimpleNodeLabel(const BasicBlock *Node);
std::string getSimpleVariableName(const Instruction *Ins);
std::string getSimpleValueName(const Value *Value);
std::string getSimpleValueName(std::string> A, std::set<std::string> B);
std::set<std::string> union_sets(std::set<std::string> A, std::set<std::string> B);
std::set<std::string> intersect_sets(std::set<std::string>, std::set<std::string>);
void printAnalysisMap(std::map<std::string, std::set<std::string>> analysisMap;
std::map<std::string, std::set<std::string>> printMap;
```

Algorithm used:

```
int main(){
                   int a,b, x,y;
                   if(a > b){
                         x = b - a;
                                 - b;
                    }else{
                            - b;
                   x = a
Program:
                               %0:
                                %1 = alloca i32, align 4
                                %a = alloca i32, align 4
                                %b = alloca i32, align 4
                                %x = alloca i32, align 4
                                %y = alloca i32, align 4
                                store i32 0, i32* %1
                                %2 = load i32* %a, align 4
                                %3 = load i32* %b, align 4
                                %4 = icmp sgt i32 \%2, \%3
                                br i1 %4, label %5, label %12
                                     T
                                                       F
           %5:
                                                  %12:
           %6 = load i32* %b, align 4
%7 = load i32* %a, align 4
                                                   %13 = load i32* %b, align 4
%14 = load i32* %a, align 4
           %8 = sub nsw i32 %6, %7
                                                   %15 = sub nsw i32 %13, %14
           store i32 %8, i32* %x, align 4
                                                   store i32 %15, i32* %y, align 4
           %9 = load i32* %a, align 4
%10 = load i32* %b, align 4
                                                   %16 = load i32* %a, align 4
                                                   %17 = load i32* %b, align 4
           %11 = sub nsw i32 %9, %10
                                                   %18 = sub nsw i32 %16, %17
           store i32 %11, i32* %y, align 4
                                                   store i32 %18, i32* %x, align 4
           br label %19
                                                   br label %19
                                    %19:
                                     %20 = load i32* %1
                                     ret i32 %20
```

CFG:

Analysis output: if we take every basic block as a unit, then the output should be like this:

CFG for 'main' function

```
PRINTING BUSY MAP:

%0entry:
a-b b-a

%12entry:
a-b b-a

%12exit:

%19entry:
%19exit:
%5entry:
a-b b-a

%5exit:
```

Note:

%0 -> "entry"; 5% -> "if.then"; 12% -> "if.else"; 19% -> "if.end"