



Zhang Wei

Curriculum Vitae

PERSONAL DETAILS

Name Zhang Wei
Birth April 3, 1989
Address Room 723, Building 9, NO.152 Luoyu Road,
Hongshan District, Wuhan, Hubei Province, 430079, P.R. China
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EDUCATION

Ph.D. Paritical physics and nuclear physics

Sept, 2017 to Present

Central China Normal University (CCNU)

During this time, I mainly focus on the integrated circuit design.

MSc. Circuit and system

Sept, 2014 to Jun, 2017

Central China Normal University (CCNU)

BSc. Electronic science and technology

Sept, 2008 to Jun, 2013

Hubei University of Arts and Science

EXCHANGE EXPERIENCE

Visiting Scholar

Feb, 2016 to Jan, 2017

Lawrence Berkeley National Laboratory (LBNL)

During this time, I am as an affiliate to participate in the development of *Topmetal* CMOS charge sensor, and a charge readout plane using *Topmetal* for neutrinoless double-beta decay searches.

RESEARCH WORK

- *Topemtal-II-* sensor chip digital readout test (2016, LBNL)
- *Topemtal-IIa* sensor chip readout test (2017, CCNU)
- *SPORT120* LDD and TIA chips test (2018, CCNU)
- *LALD14* chip design (in processing, CCNU)

SKILLS

Languages Dutch (mother tongue)
English (fluent)
German (fluent)
Software MATLAB, L^AT_EX, ANSYS, COMSOL