



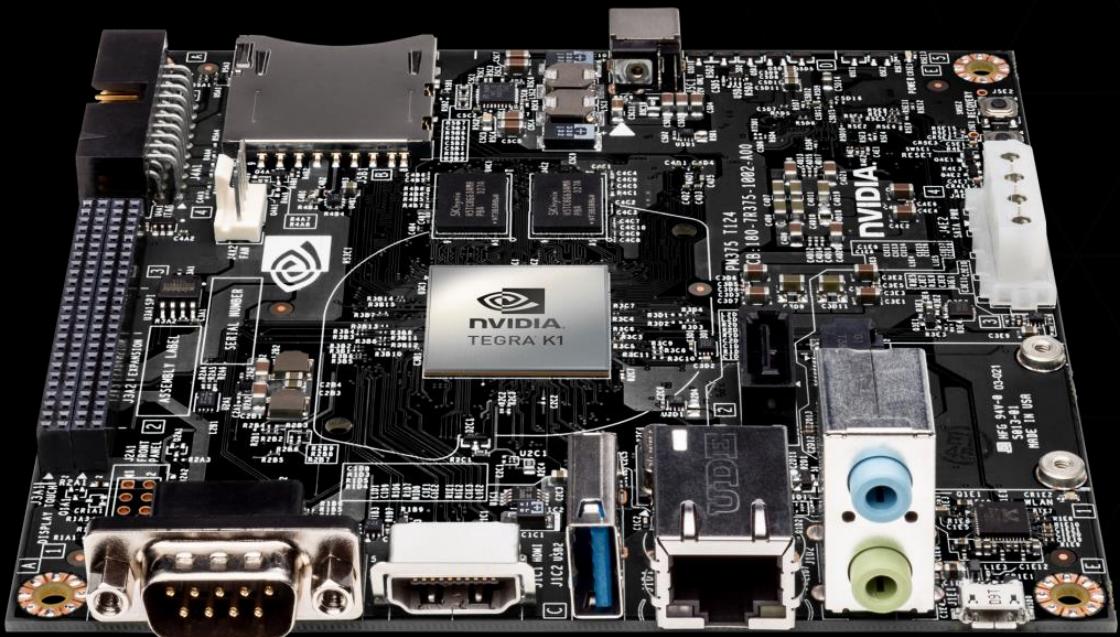
JETSON TK1

Jesse Clayton

Product Manager · Mobile Embedded

JETSON TK1

THE WORLD'S 1st EMBEDDED SUPERCOMPUTER



Tegra K1 Development Kit

CUDA

VisionWorks

Complete development tool suite

Available now

TEGRA K1

IMPOSSIBLY ADVANCED

NVIDIA Kepler Architecture

4-Plus-1 Quad-Core A15

192 NVIDIA CUDA Cores

Over 300 GFLOPS

5 Watts



CUDA : WORLD'S MOST PERVERSIVE PARALLEL PROGRAMMING PLATFORM

8,000

Institutions with
CUDA Developers

2,200,000

Toolkit
Downloads

506,000,000

GPUs Shipped

738 University Courses
In 62 Countries



VISIONWORKS



Toolkit for Image Processing

Optimized for CUDA-enabled GPUs and
SOCs

Example CV pipelines with source

Open and extensible architecture

Jetson TK1 compatibility out-of-the-box

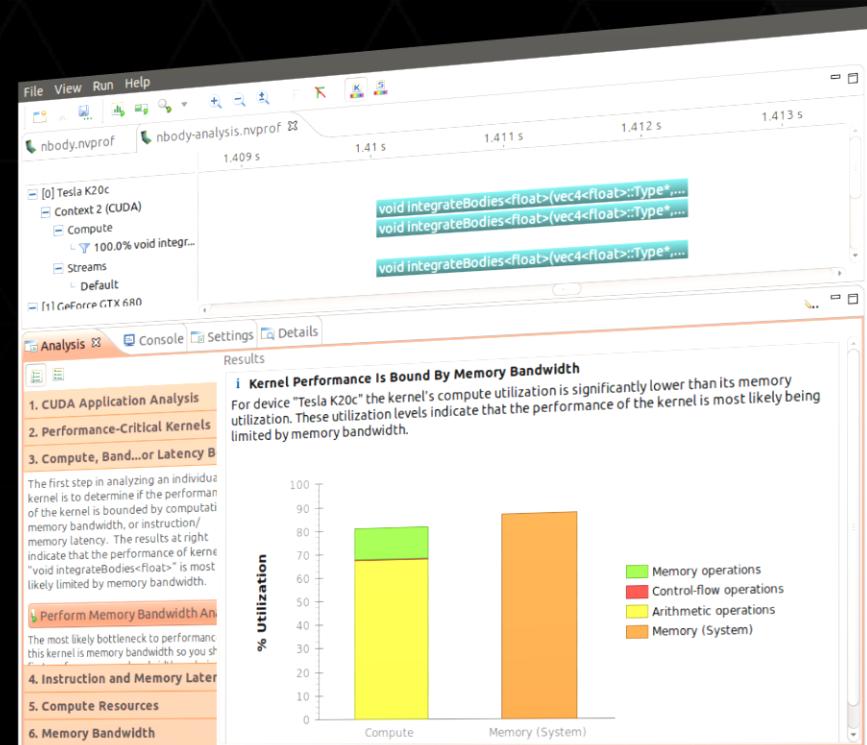
RICH DEVELOPMENT ENVIRONMENT

Nsight Eclipse Edition IDE

NVIDIA Visual Profiler

Native compiler, debugger, profiler

CUDA-enabled libraries (cuFFT,
cuBLAS, cuSparse, etc)





JETSON TK1: UNLOCKING NEW APPLICATIONS

Computer Vision

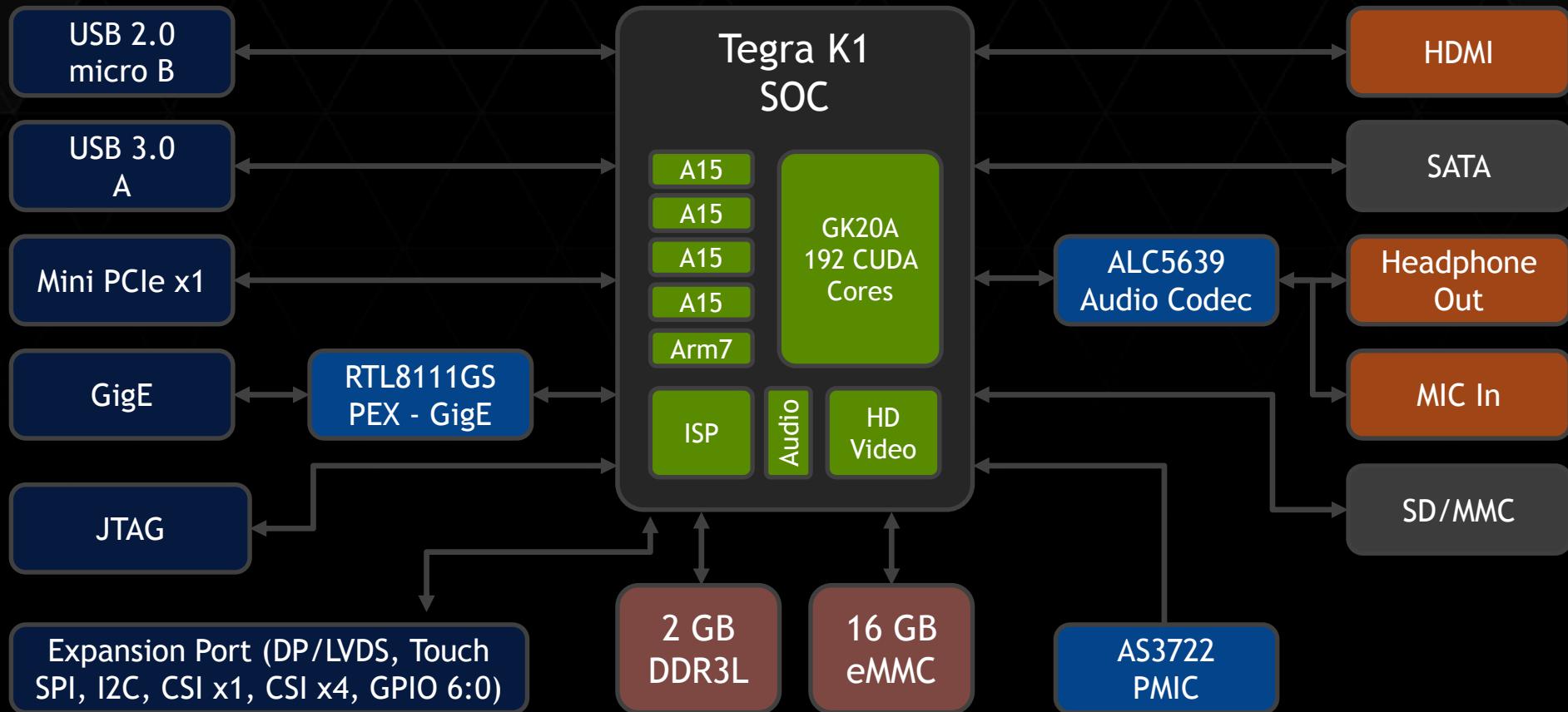
Robotics

Automotive

Medicine

Security and Defense

JETSON TK1 BLOCK DIAGRAM



“Tegra K1 can change what’s possible in the rugged and industrial embedded market. We expect to be able to offer solutions in the sub-10 watt space that previously consumed 100 watts or more.”

Simon Collins, Product Manager
GE Intelligent Platforms



“Having the level of performance and energy efficiency Jetson TK1 offers can potentially support the development of robots with real-time object recognition and compelling autonomous navigation capabilities”

Chris Jones, Director of Strategic Technology Development
iRobot Corporation



ADDITIONAL RESOURCES

Learn, leverage, contribute

Support: <https://developer.nvidia.com/jetson-tk1-support>

Forum: <https://devtalk.nvidia.com/default/board/139/embedded-systems/>

Wiki: http://elinux.org/Jetson_TK1

Get Engaged!

JETSON TK1
ORDER NOW

