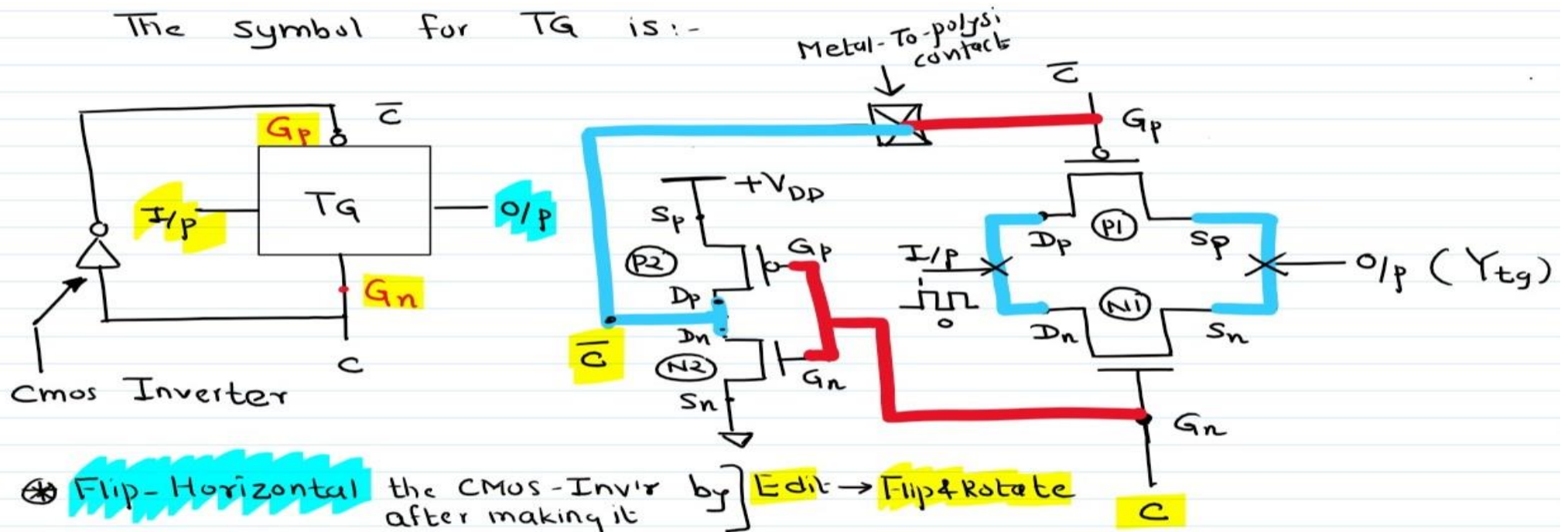


⇒ MOSFET-level Schematic of Tg:-

The symbol for Tg is:-



⊗ **Flip-Horizontal** the CMOS-Invr by **Edit → Flip & Rotate** after making it

⇒ To check functionality of Tg:-

- ① Apply 2.5 GHz Clock @ I/p
- ② For clock = 1; we should get o/p = $+V_{DD} = 1.2V$ (s1)
- ③ For clock = 0; we should get o/p = $-V_{SS} = 0V$ (s0)

⊗ Refer to the YouTube channel video for Demo of Tg-Layout