

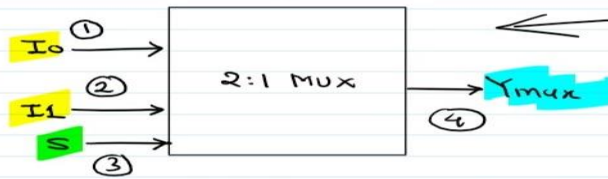
## \* B.2 :- 2:1 MUX

⇒ For Implementing N:1 Mux using TG-Logic

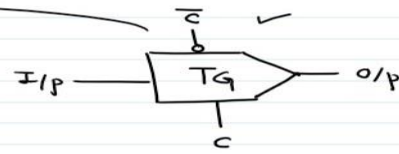
⇒ As Many, I/p's of Mux, Those many TG's

⇒ ∴ We have 2:1 Mux; we need 2 TG's i.e. TG-0, TG-1

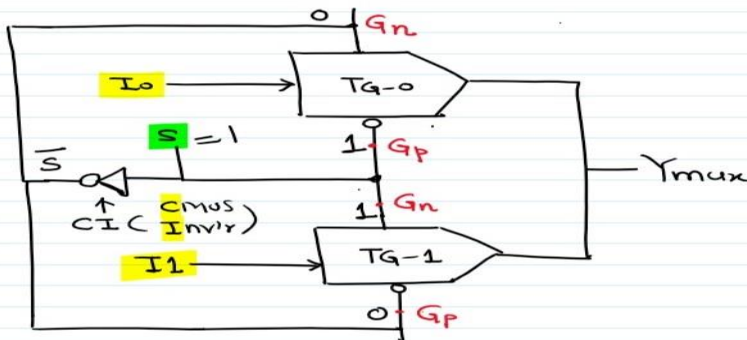
Desired system



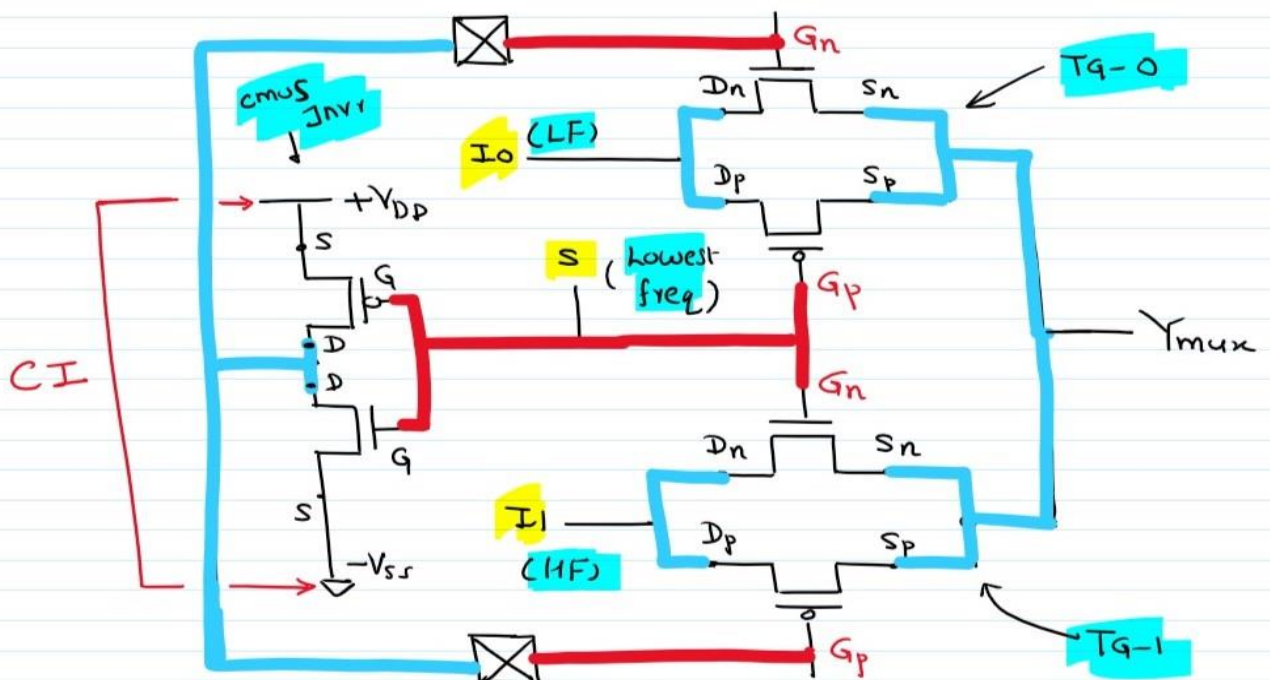
Building-Block



## \* Block-Schematic of 2:1 Mux using TG's :-



## \* MOSFET-level Schematic of 2:1 Mux using TG's :-



\* Refer to the YouTube channel video for Demo of 2:1 MUX Layout