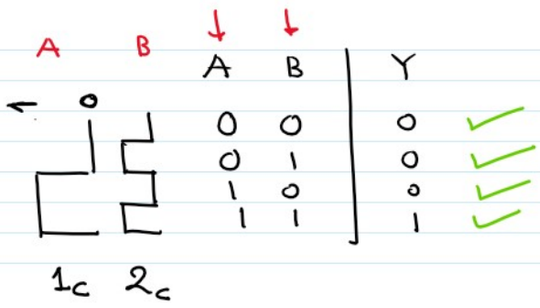
R8_05-08-2021

05 August 2021 10:12

⊛ Simulation :-

⇒ It means verifying the Logical functionality of PDS by observing the o/ps as a function of i/ps

⇒ It means verifying the Truth-Table

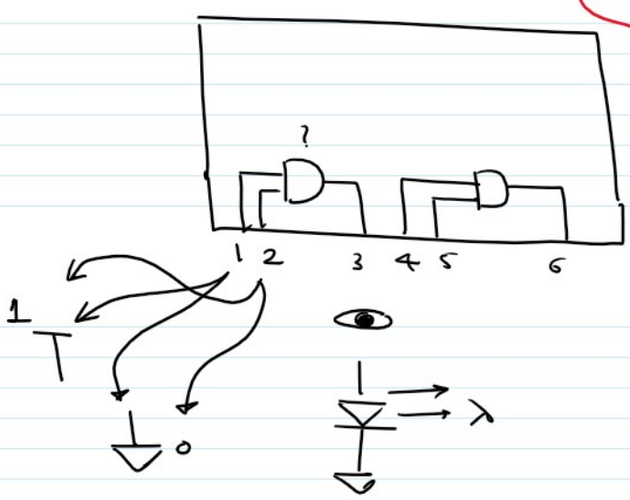


Signal-B completes 2c when Signal-A completes 1c

$T_B = \frac{1}{2} \cdot T_A$ OR $f_B = 2 \cdot f_A$ (X)

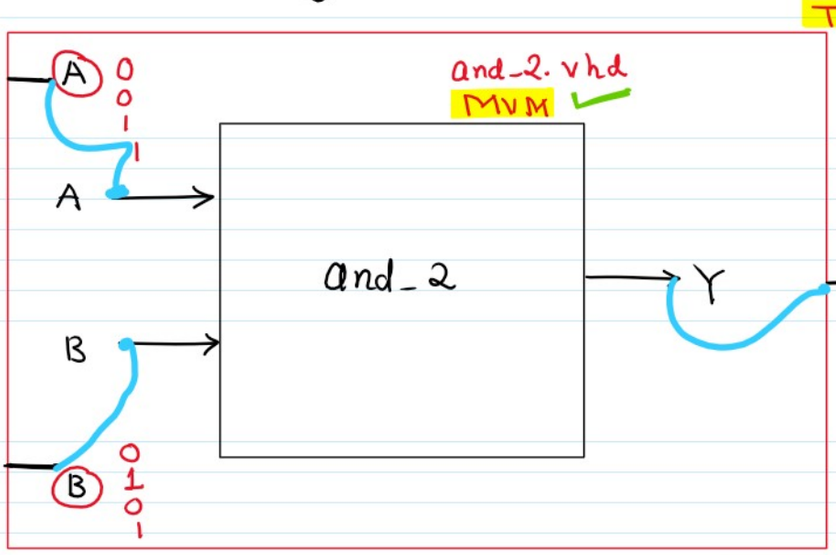
if T_A = x sec. = 100 ns

$T_B = \frac{x}{2}$ sec = 50ns.



Structural MS (SMS)

TVM and_2_tb

Run

w/fls