Html generated by [Symphony EDA](#) Sonata for 'C:/Users/welly/Documents/GitHub/LT38A/pratica06/simili/sistema.vhdl'
Source file last modified on Tue Nov 07 21:30:29 Hora oficial do Brasil 2023
Note: Use IE 5+ or Netscape 6+ or Mozilla Firefox for best results

```vhdl
 1    library ieee;
 2    use ieee.numeric_std.all;
 3    use ieee.std_logic_1164.all;
 4
 5    entity sistema is
 6        port(
 7            A,B: in unsigned(7 downto 0);
 8            Q,R: out std_logic_vector(7 downto 0)
 9        );
10    end entity;
11
12    architecture behavior of sistema is
13
14        component zeroReject is
15            port(
16                A,B: in unsigned(7 downto 0);
17                Q,R: out std_logic_vector(7 downto 0)
18            );
19        end component;
20
21    begin
22
23        zr: zeroReject port map(A,B,Q,R);
24
25    end architecture;
26
```

Note: Modifying your display preferences for the text editor in Sonata will correspondingly modify the look and
feel of the generated HTML file. Also, to properly print background colors, you may have to enable a setting
in your browser (especially IE users).