Html generated by <u>Symphony EDA</u> Sonata for 'C:/Users/welly/Documents/GitHub/LT38A/pratica06/simili/arquitetura.vhdl' Source file last modified on Mon Dec 04 22:52:00 Hora oficial do Brasil 2023 Note: Use IE 5+ or Netscape 6+ or Mozilla Firefox for best results library ieee; use ieee.numeric std.all; 2 3 use ieee.std logic 1164.all; 5 entity arquitetura is 6 port( 7 A,B: in unsigned(7 downto 0); 8 En: in bit; 9 sel: in bit vector(1 downto 0); inibi: in bit; 10 11 12 inibedsevenSeg0, inibedsevenSeg1, inibedsevenSeg2: out std logic vector(6 downto 0) 13 ); 14 end entity; 15 16 architecture behavior of arquitetura is 17 18 component entrada is 19 port ( 20 A,B: in unsigned(7 downto 0); 21 En: in bit; 22 Ae, Be: out unsigned (7 downto 0) 23 ); 24 end component; 25 26 component sistema is 27 port ( A,B: in unsigned(7 downto 0); 28 29 Q,R: out std logic vector(7 downto 0) 30 ); 31 end component; 32 33 component saida is 34 port ( 35 dividendo,divisor: in unsigned(7 downto 0); 36 quociente, resto: in std logic vector(7 downto 0); 37 sel: in bit vector(1 downto 0); 38 inibi: in bit; 39 40 inibedsevenSeg0,inibedsevenSeg1,inibedsevenSeg2: out std logic vector(6 downto 0) 41 ); 42 end component; 43

saida01: saida port map(Ae,Be,Q,R,sel,inibi,inibedsevenSeg0,inibedsevenSeg1,inibedsevenSeg2);

Note: Modifying your display preferences for the text editor in Sonata will correspondingly modify the look and feel of the generated HTML file. Also, to properly print background colors, you may have to enable a setting in your browser (especially IE users).

signal Ae, Be: unsigned(7 downto 0);

signal Q,R: std\_logic\_vector(7 downto 0);

entrada01: entrada port map(A,B,En,Ae,Be);

sistema01: sistema port map(Ae, Be, Q, R);



begin

end architecture;

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