





```
1  module Multiplexer(output reg [2:0]out, input[2:0]Inp0,Inp1, input sel);
2
3      always @(sel)
4      begin
5          case(sel)
6              1'b0 : out = Inp0;
7              1'b1 : out = Inp1;
8              default: out = 3'bx;
9          endcase
10     end
11
12 endmodule
```