














Entity:Instance		Logic Cells	sd Logic F	I/O Registers	Memory Bits	M9Ks	DSP Elements	DSP 9x9	DSP 18x18	Pins	Virtual Pins	LUT-Only LCs	Register-Only LCs	LUT/Register LCs	Full Hierarchy Name
 Cyclone IV E: EP4CE115F29C7															
▼	 arquitetura 	411 (0)	204 (0)	0 (0)	0	0	0	0	0	26	0	207 (0)	44 (0)	160 (0)	arquitetura
▼	 Entrada:inst00	167 (0)	103 (0)	0 (0)	0	0	0	0	0	0	0	64 (0)	3 (0)	100 (0)	arquitetura Entrada...
	 ClockDivider:inst01	155 (155)	94 (94)	0 (0)	0	0	0	0	0	0	0	61 (61)	3 (3)	91 (91)	arquitetura Entrada...
>	 keypadEncoder:inst02	12 (5)	9 (5)	0 (0)	0	0	0	0	0	0	0	3 (0)	0 (0)	9 (2)	arquitetura Entrada...
▼	 Sistema:inst01	129 (5)	79 (0)	0 (0)	0	0	0	0	0	0	0	50 (5)	40 (0)	39 (0)	arquitetura Sistem...
	 ControlUnit:inst00	5 (5)	3 (3)	0 (0)	0	0	0	0	0	0	0	2 (2)	1 (1)	2 (2)	arquitetura Sistem...
	 Timer:inst01	11 (11)	8 (8)	0 (0)	0	0	0	0	0	0	0	3 (3)	1 (1)	7 (7)	arquitetura Sistem...
	 RamMemory:inst02	108 (108)	68 (68)	0 (0)	0	0	0	0	0	0	0	40 (40)	38 (38)	30 (30)	arquitetura Sistem...
▼	 Saida:inst03	116 (0)	22 (0)	0 (0)	0	0	0	0	0	0	0	93 (0)	1 (0)	22 (0)	arquitetura Saida:in...
>	 PhraseBank:inst01	74 (35)	7 (0)	0 (0)	0	0	0	0	0	0	0	67 (35)	0 (0)	7 (0)	arquitetura Saida:in...
	 DisplayControlUnit:inst02	42 (42)	15 (15)	0 (0)	0	0	0	0	0	0	0	26 (26)	1 (1)	15 (15)	arquitetura Saida:in...