

```
1  LIBRARY ieee;
2  USE ieee.std_logic_1164.all;
3  USE ieee.numeric_std.all;
4
5  entity multiplexador is
6      port
7      (
8          Inp0,Inp1: in unsigned(3 downto 0);
9          selA: in std_logic;
10
11          OutMux: out unsigned(3 downto 0)
12      );
13  end entity;
14
15  architecture behavior of multiplexador is
16
17  begin
18
19  with selA select
20      OutMux <= Inp0 when '0',
21               Inp1 when others;
22
23  end architecture;
```