

```
1  module GeradorSync (  
2      input wire clk25,  
3      output wire Hsync, Hactive,  
4      output wire Vsync, Vactive,  
5      output wire enable  
6  );  
7  
8      GeradorHorizontal gH(clk25,Hsync, Hactive);  
9      GeradorVertical gV(Hsync,Vsync, Vactive);  
10     assign enable = Hactive & Vactive;  
11  
12 endmodule
```