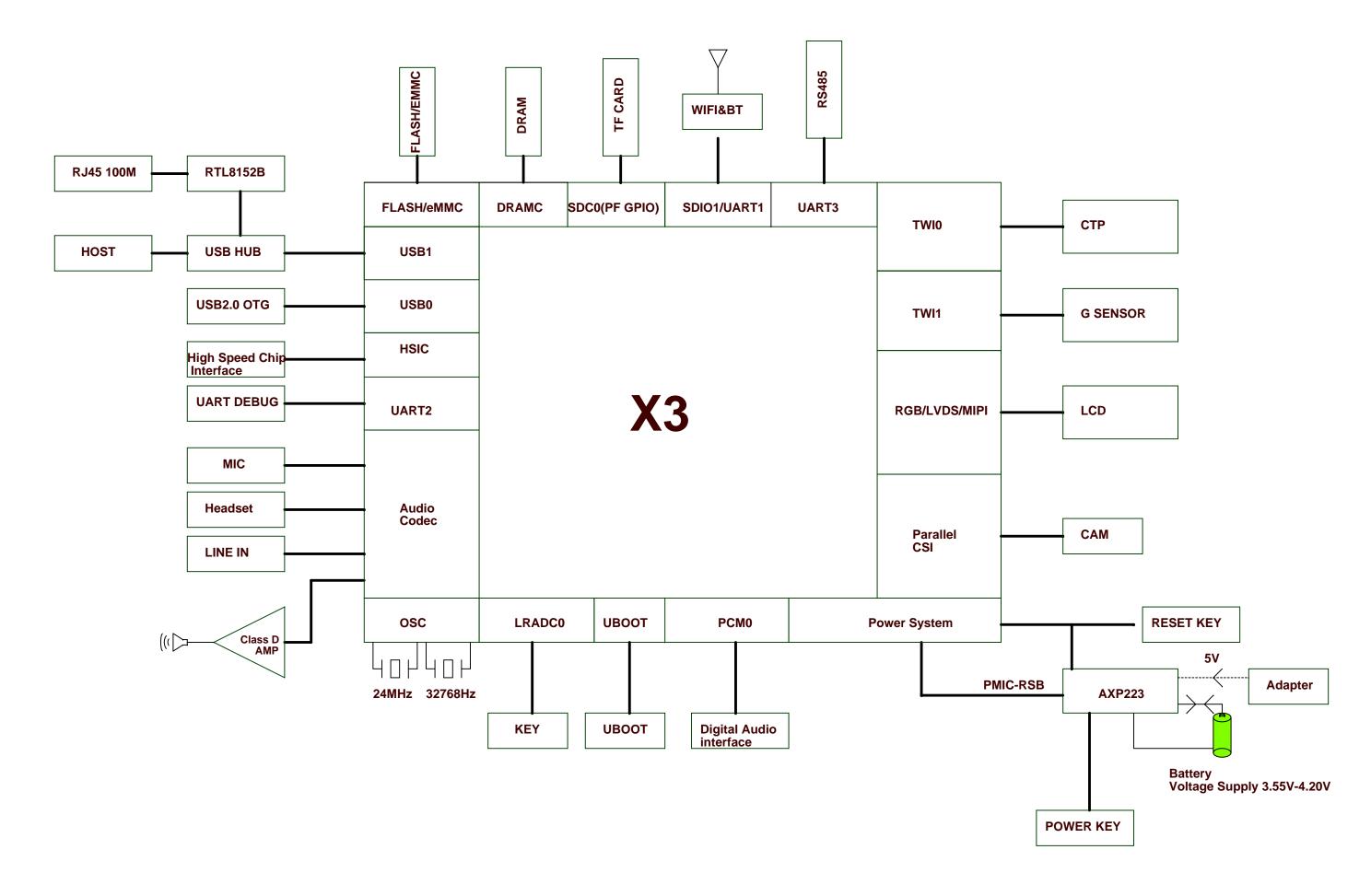
VERSION HISTORY

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Revision	Description	Date	Drawn	Checked	Approved
Ver 00	First release	2019-8-26	ZSC		

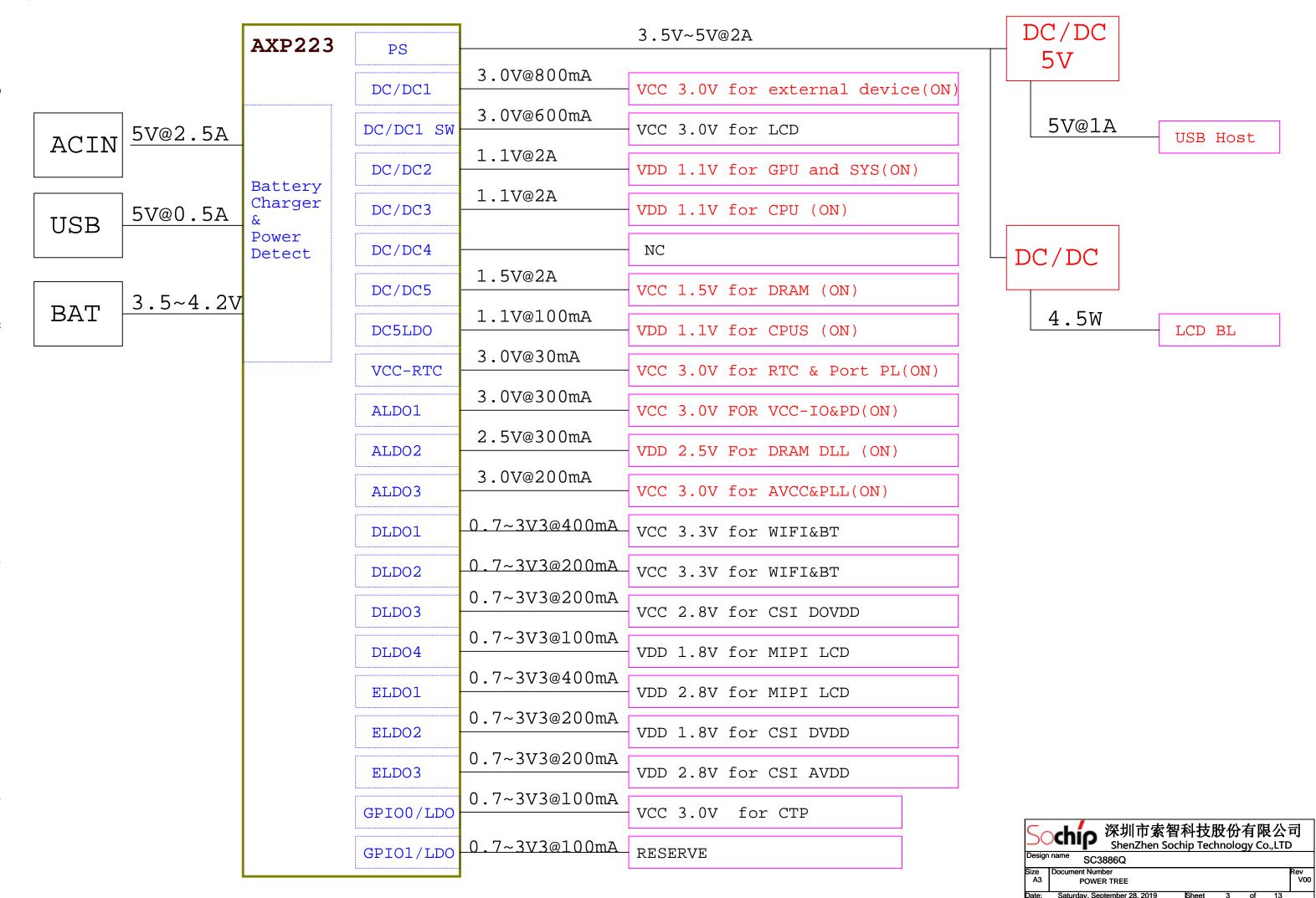
BLOCK DIAGRAM



Size BLOCK DIAGRAM

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POWER TREE



GPIO ASSIGNMENT

PIN	Name	Function	
PB0	UART2-TX	DEBUG	
PB1	UART2-RX	DEBOG	
PB2	PB2	Digital Audio	
PB3	GS-INT	G-Sensor	
PB4	PCM0-SYNC	Digital Audio	
PB5	PCM0-CLK		
PB6	PCM0-DOUT		
PB7	PCM0-DIN		

Name	Function
WL-SDIO-CLK	
WL-SDIO-CMD	
WL-SDIO-D0	
WL-SDIO-D1	
WL-SDIO-D2	
WL-SDIO-D3	
AP-UART1-TX	
AP-UART1-RX	WLAN&BT
AP-UART1-RTS	
AP-UART1-CTS	
WL-WAKE-AP	
BT-WAKE-AP	
AP-WAKE-BT	
BT-RST-N	
	WL-SDIO-CLK WL-SDIO-CMD WL-SDIO-D0 WL-SDIO-D2 WL-SDIO-D3 AP-UART1-TX AP-UART1-RX AP-UART1-RX AP-UART1-CTS WL-WAKE-AP BT-WAKE-AP

PIN	Name	Function
PH0	LCD-PWM0	LCM
PH1	PWM1	Digital Aud
PH2	TWI0-SCK	ТР
PH3	TWI0-SDA	1F
PH4	TWI1-SCK	SENSOR
PH5	TWI1-SDA	SENSOR
РН6	UART3_TX	RS485
PH7	UART3-RX	C0F6J
РН8	REVERSE	Digital Audi
PH9	PA-SHDN	WIFI&BT

PIN	Name	Function	
PL0	PMIC-SCK	PMIC	
PL1	PMIC-SDA		
PL2	RJ45-EN	RJ45	
PL3	HUB-RST	HUB	
PL4	CTP-RST	CTP	
PL5	CTP-INT		
PL6	USB-ID	USB	
PL7	LCD-RESET	LCD	
PL8	USB-HOST-EN	HOST	
PL9	T-CARD-DET	TF CARD	
PL10	LCD-BL-EN	LCD	
PL11	PA-SHDN-N	SPK	



