



1. Description

1.1. Project

| | |
|-----------------|-------------------|
| Project Name | STM32L0_BMA253 |
| Board Name | custom |
| Generated with: | STM32CubeMX 6.6.1 |
| Date | 10/09/2022 |

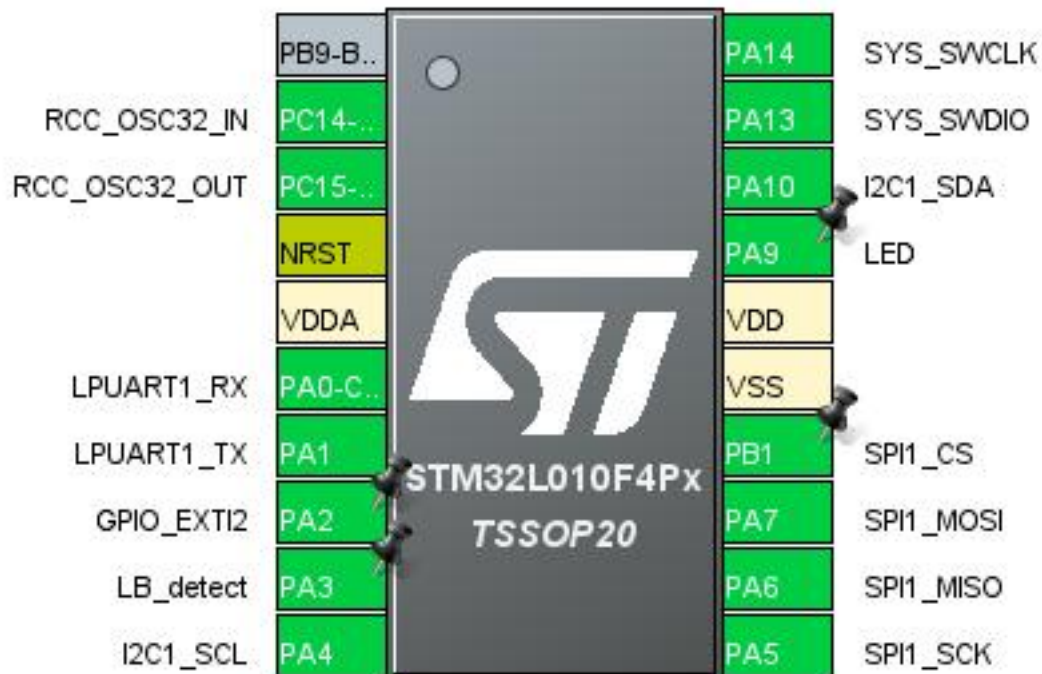
1.2. MCU

| | |
|----------------|----------------------|
| MCU Series | STM32L0 |
| MCU Line | STM32L0x0 Value Line |
| MCU name | STM32L010F4Px |
| MCU Package | TSSOP20 |
| MCU Pin number | 20 |

1.3. Core(s) information

| | |
|---------|----------------|
| Core(s) | Arm Cortex-M0+ |
|---------|----------------|

2. Pinout Configuration

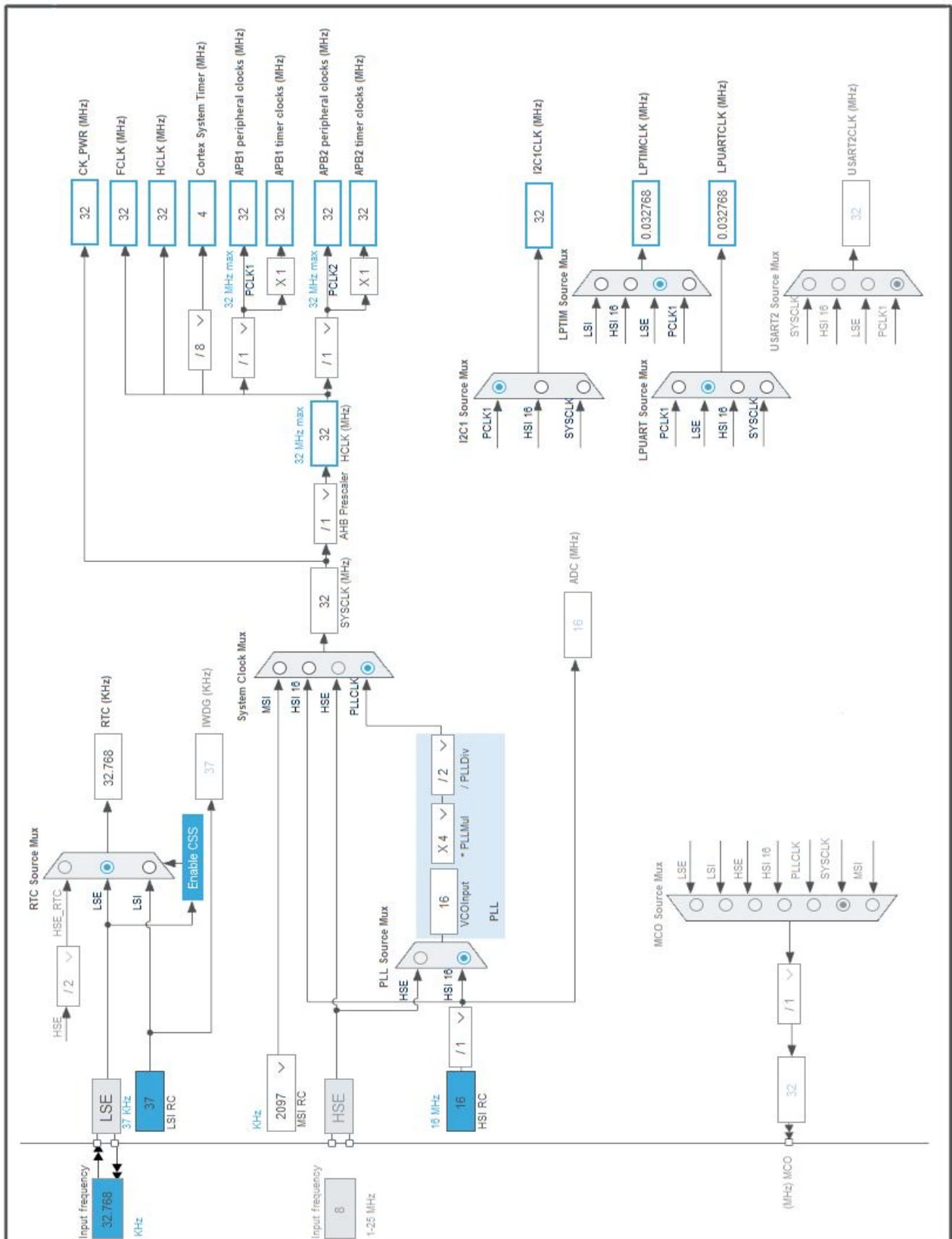


3. Pins Configuration

| Pin Number TSSOP20 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|-----------------------|---------------------------------------|----------|--------------------------|-----------|
| 2 | PC14-OSC32_IN | I/O | RCC_OSC32_IN | |
| 3 | PC15-OSC32_OUT | I/O | RCC_OSC32_OUT | |
| 4 | NRST | Reset | | |
| 5 | VDDA | Power | | |
| 6 | PA0-CK_IN | I/O | LPUART1_RX | |
| 7 | PA1 | I/O | LPUART1_TX | |
| 8 | PA2 | I/O | GPIO_EXTI2 | |
| 9 | PA3 * | I/O | GPIO_Input | LB_detect |
| 10 | PA4 | I/O | I2C1_SCL | |
| 11 | PA5 | I/O | SPI1_SCK | |
| 12 | PA6 | I/O | SPI1_MISO | |
| 13 | PA7 | I/O | SPI1_MOSI | |
| 14 | PB1 * | I/O | GPIO_Output | SPI1_CS |
| 15 | VSS | Power | | |
| 16 | VDD | Power | | |
| 17 | PA9 * | I/O | GPIO_Output | LED |
| 18 | PA10 | I/O | I2C1_SDA | |
| 19 | PA13 | I/O | SYS_SWDIO | |
| 20 | PA14 | I/O | SYS_SWCLK | |

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

| Name | Value |
|-----------------------------------|--|
| Project Name | STM32L0_BMA253 |
| Project Folder | D:\src\0_examples\STM32\STM32L010F4P6_BMA253 |
| Toolchain / IDE | MDK-ARM V5.32 |
| Firmware Package Name and Version | STM32Cube FW_L0 V1.12.1 |
| Application Structure | Advanced |
| Generate Under Root | No |
| Do not generate the main() | No |
| Minimum Heap Size | 0x100 |
| Minimum Stack Size | 0x100 |

5.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | Yes |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | Yes |
| Enable Full Assert | No |

5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|----------------------|--------------------------|
| 1 | MX_GPIO_Init | GPIO |
| 2 | MX_DMA_Init | DMA |
| 3 | SystemClock_Config | RCC |
| 4 | MX_LPUART1_UART_Init | LPUART1 |
| 5 | MX_RTC_Init | RTC |
| 6 | MX_SPI1_Init | SPI1 |
| 7 | MX_LPTIM1_Init | LPTIM1 |
| 8 | MX_I2C1_Init | I2C1 |

6. Power Consumption Calculator report

6.1. Microcontroller Selection

| | |
|-----------|----------------------|
| Series | STM32L0 |
| Line | STM32L0x0 Value Line |
| MCU | STM32L010F4Px |
| Datasheet | DS12323_Rev1 |

6.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.0 |

6.3. Battery Selection

| | |
|-------------------|------------------|
| Battery | Li-SOCL2(AAA700) |
| Capacity | 700.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 10.0 mA |
| Max Pulse Current | 30.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

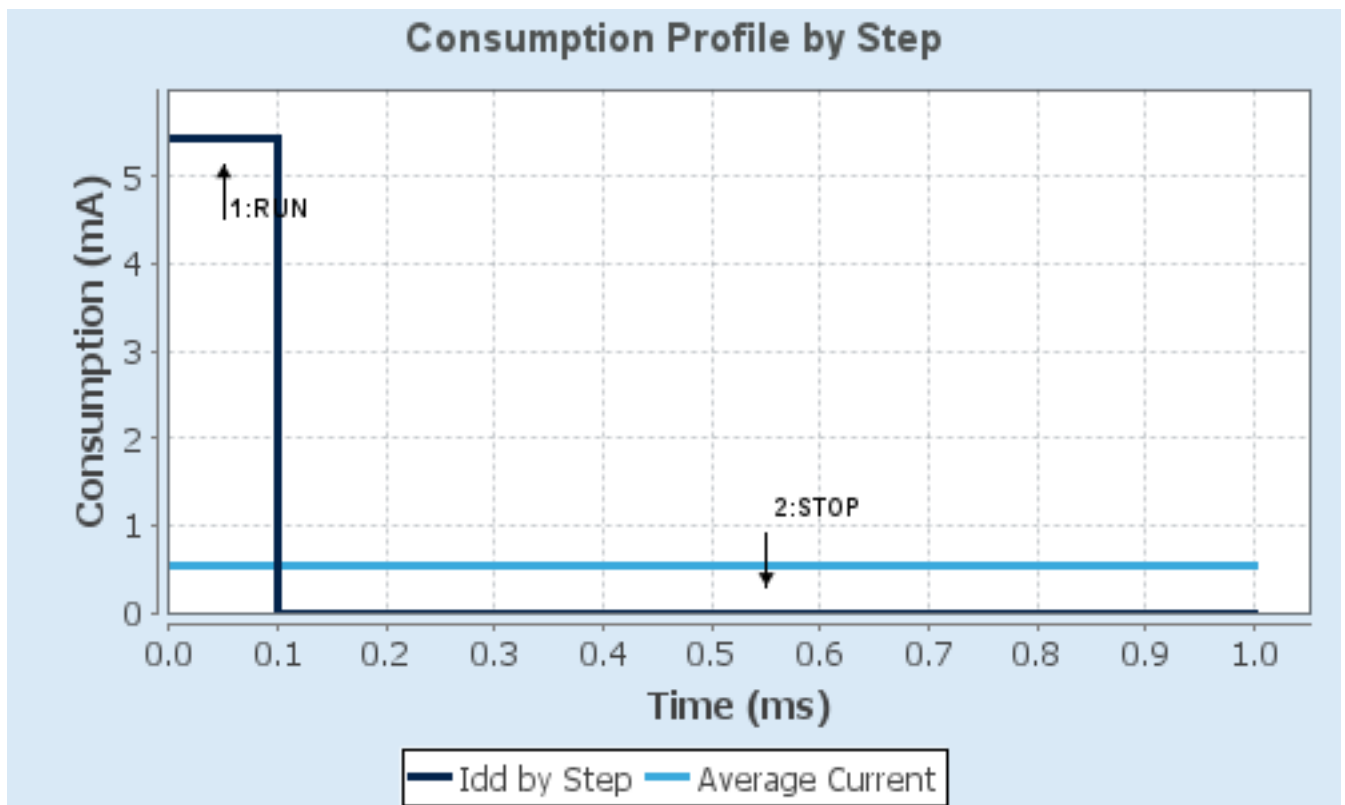
6.4. Sequence

| | | |
|-------------------------------|-------------------|----------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP |
| Vdd | 3.0 | 3.0 |
| Voltage Source | Battery | Battery |
| Range | Range1-High | NoRange |
| Fetch Type | FLASH/PREFETCH | FLASH |
| CPU Frequency | 32 MHz | 0 Hz |
| Clock Configuration | HSI PLL2 Flash-ON | ALL CLOCKS OFF |
| Clock Source Frequency | 16 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 5.45 mA | 344 nA |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 30.0 | 0.0 |
| Ta Max | 103.79 | 105 |
| Category | In DS Table | In DS Table |

6.5. Results

| | | | |
|---------------|-------------------------------|-----------------|----------------|
| Sequence Time | 1 ms | Average Current | 545.31 μ A |
| Battery Life | 1 month, 22 days, 23 hours | Average DMIPS | 3.04 DMIPS |

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. I2C1

I2C: I2C

7.1.1. Parameter Settings:

Timing configuration:

| | |
|-------------------------------|---------------------|
| I2C Speed Mode | Standard Mode |
| I2C Speed Frequency (KHz) | 100 |
| Rise Time (ns) | 0 |
| Fall Time (ns) | 0 |
| Coefficient of Digital Filter | 0 |
| Analog Filter | Enabled |
| Timing | 0x00707CBB * |

Slave Features:

| | |
|----------------------------------|----------|
| Clock No Stretch Mode | Disabled |
| General Call Address Detection | Disabled |
| Primary Address Length selection | 7-bit |
| Dual Address Acknowledged | Disabled |
| Primary slave address | 0 |

7.2. LPTIM1

Mode: Counts internal clock events

7.2.1. Parameter Settings:

Clock:

| | |
|-----------------|----------------|
| Clock Prescaler | Prescaler Div1 |
|-----------------|----------------|

Preload:

| | |
|-------------|------------------|
| Update Mode | Update Immediate |
|-------------|------------------|

Trigger:

| | |
|----------------|------------------|
| Trigger Source | Software Trigger |
|----------------|------------------|

7.3. LPUART1

Mode: Asynchronous

7.3.1. Parameter Settings:

Basic Parameters:

| | |
|-------------|------------------------------------|
| Baud Rate | 9600 * |
| Word Length | 8 Bits (including Parity) * |
| Parity | None |
| Stop Bits | 1 |

Advanced Parameters:

| | |
|----------------|----------------------|
| Data Direction | Receive and Transmit |
| Single Sample | Disable |

Advanced Features:

| | |
|-------------------------------|---------|
| TX Pin Active Level Inversion | Disable |
| RX Pin Active Level Inversion | Disable |
| Data Inversion | Disable |
| TX and RX pins Swapping | Disable |
| Overrun | Enable |
| DMA on RX Error | Enable |
| MSB First | Disable |

7.4. RCC

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

7.4.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Buffer Cache | Enabled |
| Prefetch | Disabled |
| Preread | Enabled |
| Flash Latency(WS) | 1 WS (2 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|---|
| HSI Calibration Value | 16 |
| MSI Calibration Value | 0 |
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |
| LSE Drive Capability | LSE oscillator high drive capability * |

Power Parameters:

| | |
|-------------------------------|---------------------------------|
| Power Regulator Voltage Scale | Power Regulator Voltage Scale 1 |
|-------------------------------|---------------------------------|

7.5. RTC

mode: Activate Clock Source

mode: Activate Calendar

7.5.1. Parameter Settings:

General:

| | |
|-------------------------------|---------------|
| Hour Format | Hourformat 24 |
| Asynchronous Predivider value | 127 |
| Synchronous Predivider value | 255 |

Calendar Time:

| | |
|--|----------------------|
| Data Format | BCD data format |
| Hours | 0 |
| Minutes | 0 |
| Seconds | 0 |
| Day Light Saving: value of hour adjustment | Daylightsaving None |
| Store Operation | Storeoperation Reset |

Calendar Date:

| | |
|----------|-----------------|
| Week Day | Friday * |
| Month | May * |
| Date | 1 |
| Year | 20 * |

7.6. SPI1

Mode: Full-Duplex Master

7.6.1. Parameter Settings:

Basic Parameters:

| | |
|--------------|-----------|
| Frame Format | Motorola |
| Data Size | 8 Bits |
| First Bit | MSB First |

Clock Parameters:

| | |
|---------------------------|-----------------------|
| Prescaler (for Baud Rate) | 2 |
| Baud Rate | 16.0 MBits/s * |
| Clock Polarity (CPOL) | High * |
| Clock Phase (CPHA) | 2 Edge * |

Advanced Parameters:

| | |
|-----------------|----------|
| CRC Calculation | Disabled |
| NSS Signal Type | Software |

7.7. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

*** User modified value**

8. System Configuration

8.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|---------|----------------|---------------|--|-----------------------------|-------------|------------|
| I2C1 | PA4 | I2C1_SCL | Alternate Function Open Drain | No pull-up and no pull-down | Very High * | |
| | PA10 | I2C1_SDA | Alternate Function Open Drain | No pull-up and no pull-down | Very High * | |
| LPUART1 | PA0-CK_IN | LPUART1_RX | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| | PA1 | LPUART1_TX | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| RCC | PC14-OSC32_IN | RCC_OSC32_IN | n/a | n/a | n/a | |
| | PC15-OSC32_OUT | RCC_OSC32_OUT | n/a | n/a | n/a | |
| SPI1 | PA5 | SPI1_SCK | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| | PA6 | SPI1_MISO | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| | PA7 | SPI1_MOSI | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| SYS | PA13 | SYS_SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_SWCLK | n/a | n/a | n/a | |
| GPIO | PA2 | GPIO_EXTI2 | External Interrupt Mode with Rising edge trigger detection | No pull-up and no pull-down | n/a | |
| | PA3 | GPIO_Input | Input mode | No pull-up and no pull-down | n/a | LB_detect |
| | PB1 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Very High * | SPI1_CS |
| | PA9 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | LED |

8.2. DMA configuration

| DMA request | Stream | Direction | Priority |
|-------------|---------------|----------------------|----------|
| LPUART1_RX | DMA1_Channel3 | Peripheral To Memory | Low |
| LPUART1_TX | DMA1_Channel4 | Memory To Peripheral | Low |

LPUART1_RX: DMA1_Channel3 DMA request Settings:

Mode: Normal
 Peripheral Increment: Disable
 Memory Increment: **Enable ***
 Peripheral Data Width: Byte
 Memory Data Width: Byte

LPUART1_TX: DMA1_Channel4 DMA request Settings:

Mode: Normal
 Peripheral Increment: Disable
 Memory Increment: **Enable ***
 Peripheral Data Width: Byte
 Memory Data Width: Byte

8.3. NVIC configuration

8.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 0 | 0 |
| EXTI line 2 and line 3 interrupts | true | 0 | 0 |
| DMA1 channel 2 and channel 3 interrupts | true | 0 | 0 |
| DMA1 channel 4 and channel 5 interrupts | true | 0 | 0 |
| LPTIM1 global interrupt / LPTIM1 wake-up interrupt through EXTI line 29 | true | 0 | 0 |
| I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23 | true | 0 | 0 |
| SPI1 global interrupt | true | 0 | 0 |
| LPUART1 global interrupt / LPUART1 wake-up interrupt through EXTI line 28 | true | 0 | 0 |
| Flash and EEPROM global interrupt | unused | | |
| RCC global interrupt | unused | | |

8.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| System service call via SWI instruction | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |
| EXTI line 2 and line 3 interrupts | false | true | true |
| DMA1 channel 2 and channel 3 interrupts | false | true | true |
| DMA1 channel 4 and channel 5 interrupts | false | true | true |
| LPTIM1 global interrupt / LPTIM1 wake-up interrupt through EXTI line 29 | false | true | true |
| I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23 | false | true | true |
| SPI1 global interrupt | false | true | true |
| LPUART1 global interrupt / LPUART1 wake-up interrupt through EXTI line 28 | false | true | true |

* User modified value

9. System Views

9.1. Category view

9.1.1. Current

| Middleware | | | | |
|-------------|--------|----------|--------------|-----------|
| System Core | Analog | Timers | Connectivity | Computing |
| DMA ✓ | | LPTIM1 ✓ | I2C1 ✓ | |
| GPIO ✓ | | RTC ✓ | LPUART1 ✓ | |
| NVIC ✓ | | | SPI1 ✓ | |
| RCC ✓ | | | | |
| SYS ✓ | | | | |

10. Docs & Resources

| Type | Link |
|-------------------|---|
| Presentations | https://www.st.com/resource/en/product_presentation/gt_stm32f0-l0.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf |
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| Brochures | https://www.st.com/resource/en/brochure/brstm32l0.pdf |
| Brochures | https://www.st.com/resource/en/brochure/brstm32ulp.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flnucleolrwan.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32l0vline.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32nucleo.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32trust.pdf |
| Magazine Articles | https://www.st.com/resource/en/magazine/design-elektronik_october2016.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2548-using-the-stm32f0f1f3gxlx-series-dma-controller-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf |

Application Notes https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2867-oscillator-design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3155-uart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3236-increase-the-number-of-touchkeys-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3960-esd-considerations-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4230-stm32-microcontroller-random-number-generation-validation-using-the-nist-statistical-test-suite-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4299-improve-

conducted-noise-robustness-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4310-sampling-capacitor-selection-guide-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4445-stm32l0xx-ultralow-power-features-overview-stmicroelectronics.pdf

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- Application Notes https://www.st.com/resource/en/application_note/an4809-migrating-between-stm32l0-series-and-stm32l4-series--stm32l4-series-microcontrollers-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an5543-enhanced-methods-to-handle-spi-communication-on-stm32-devices-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4899-stm32-microcontroller-gpio-hardware-settings-and-lowpower-consumption-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an1202_freertos_guide-for_related_Tools_freertos_guide-stmicroelectronics.pdf
& Software
- Application Notes https://www.st.com/resource/en/application_note/an1602_semihosting_in_for_related_Tools_truestudio-how-to-do-semihosting-in-truestudio-stmicroelectronics.pdf
& Software
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