1. Description

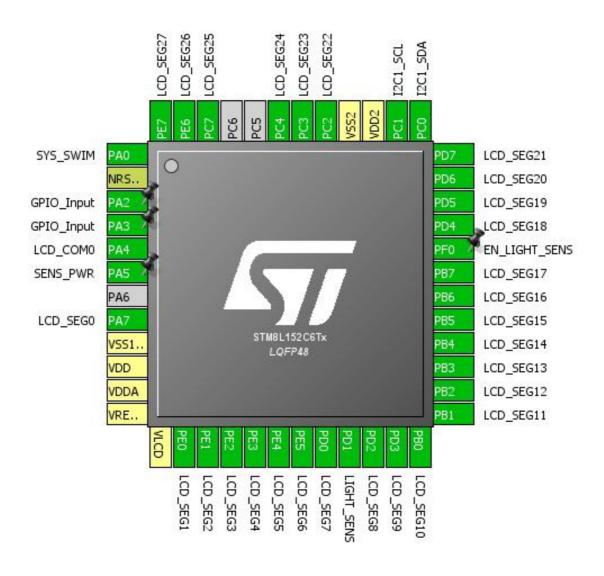
1.1. Project

Project Name	trh_indicator
Board Name	No information
Generated with:	STM8CubeMX 1.2.0
Date	01/09/2019

1.2. MCU

MCU Series	STM8L
MCU Line	STM8L151/152
MCU name	STM8L152C6Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



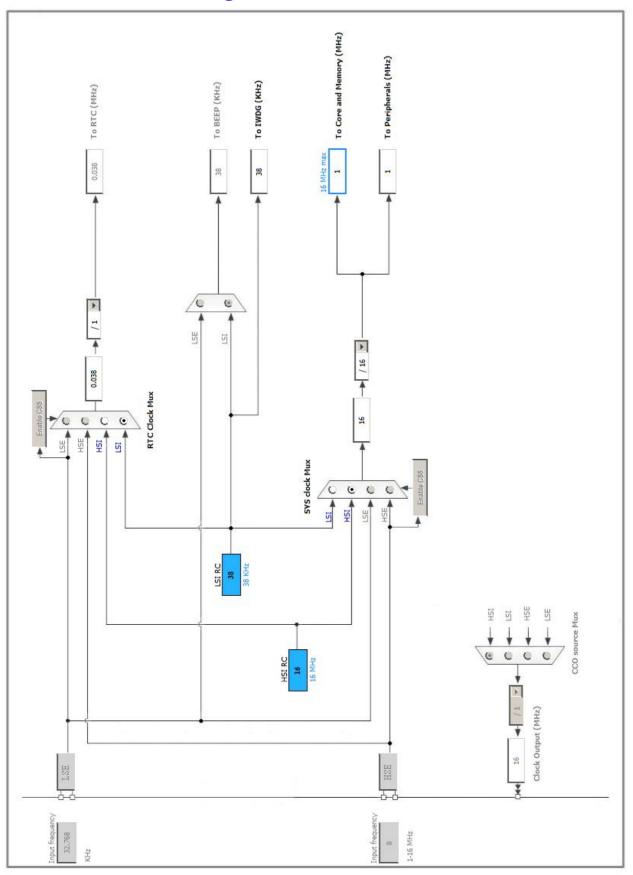
3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP48	(function after		Function(s)	
	reset)		,	
1	PA0	I/O	SYS_SWIM	
2	NRST/PA1	Reset		
3	PA2 *	I/O	GPIO_Input	
4	PA3 *	I/O	GPIO_Input	
5	PA4	I/O	LCD_COM0	
6	PA5 *	I/O	GPIO_Output	SENS_PWR
8	PA7	I/O	LCD_SEG0	
9	VSS1/VSSA/VREF-	Power		
10	VDD	Power		
11	VDDA	Power		
12	VREF+	Power		
13	VLCD	Power		
14	PE0	I/O	LCD_SEG1	
15	PE1	I/O	LCD_SEG2	
16	PE2	I/O	LCD_SEG3	
17	PE3	I/O	LCD_SEG4	
18	PE4	I/O	LCD_SEG5	
19	PE5	I/O	LCD_SEG6	
20	PD0	I/O	LCD_SEG7	
21	PD1	I/O	ADC1_IN21	LIGHT_SENS
22	PD2	I/O	LCD_SEG8	
23	PD3	I/O	LCD_SEG9	
24	PB0	I/O	LCD_SEG10	
25	PB1	I/O	LCD_SEG11	
26	PB2	I/O	LCD_SEG12	
27	PB3	I/O	LCD_SEG13	
28	PB4	I/O	LCD_SEG14	
29	PB5	I/O	LCD_SEG15	
30	PB6	I/O	LCD_SEG16	
31	PB7	I/O	LCD_SEG17	
32	PF0 *	I/O	GPIO_Output	EN_LIGHT_SENS
33	PD4	I/O	LCD_SEG18	
34	PD5	I/O	LCD_SEG19	
35	PD6	I/O	LCD_SEG20	
36	PD7	I/O	LCD_SEG21	
37	PC0	I/O	I2C1_SDA	

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
38	PC1	I/O	I2C1_SCL	
39	VDD2	Power		
40	VSS2	Power		
41	PC2	I/O	LCD_SEG22	
42	PC3	I/O	LCD_SEG23	
43	PC4	I/O	LCD_SEG24	
46	PC7	I/O	LCD_SEG25	
47	PE6	I/O	LCD_SEG26	
48	PE7	I/O	LCD_SEG27	

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



5. Power Consumption Calculator report

5.1. Microcontroller Selection

Series	STM8L
Line	STM8L151/152
MCU	STM8L152C6Tx
Datasheet	15962_Rev14

5.2. Parameter Selection

Temperature	25
Vdd	3.0